



Radxa ROCK S0

schematic

V1.1

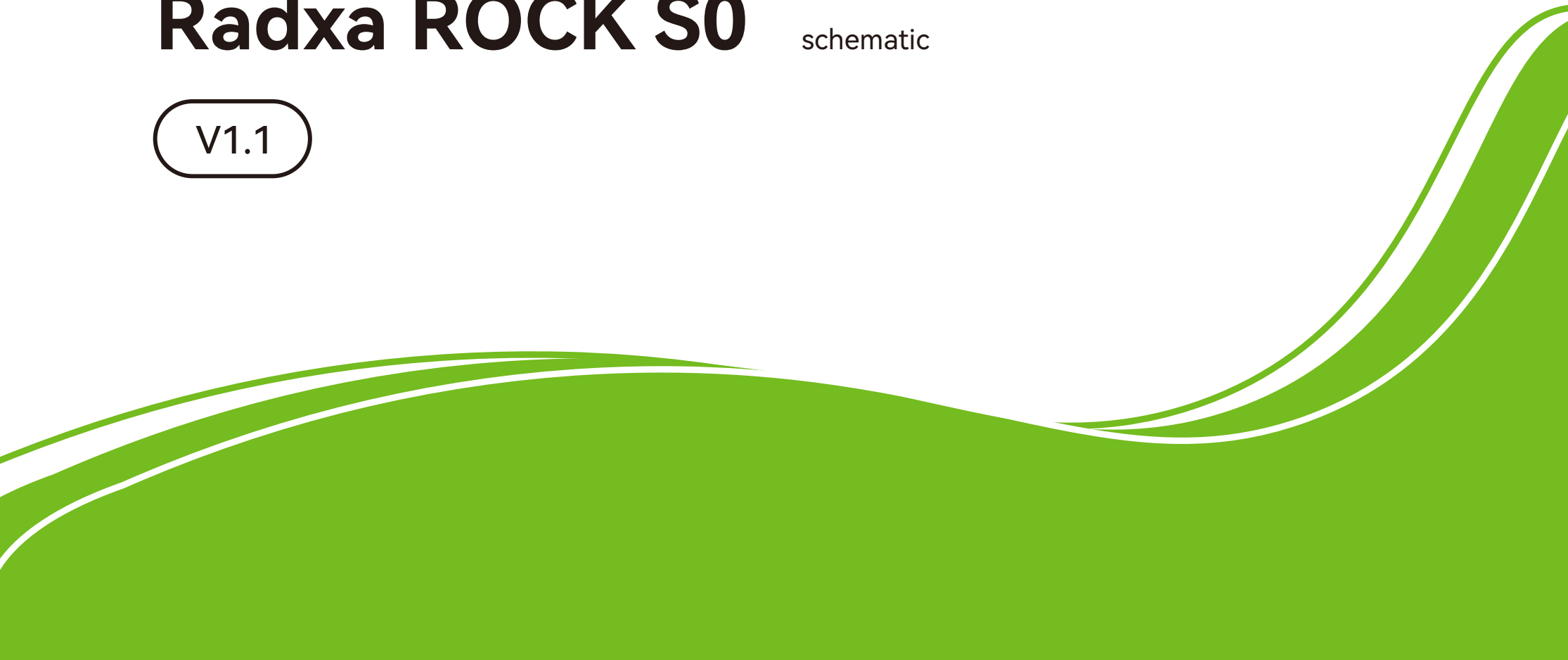


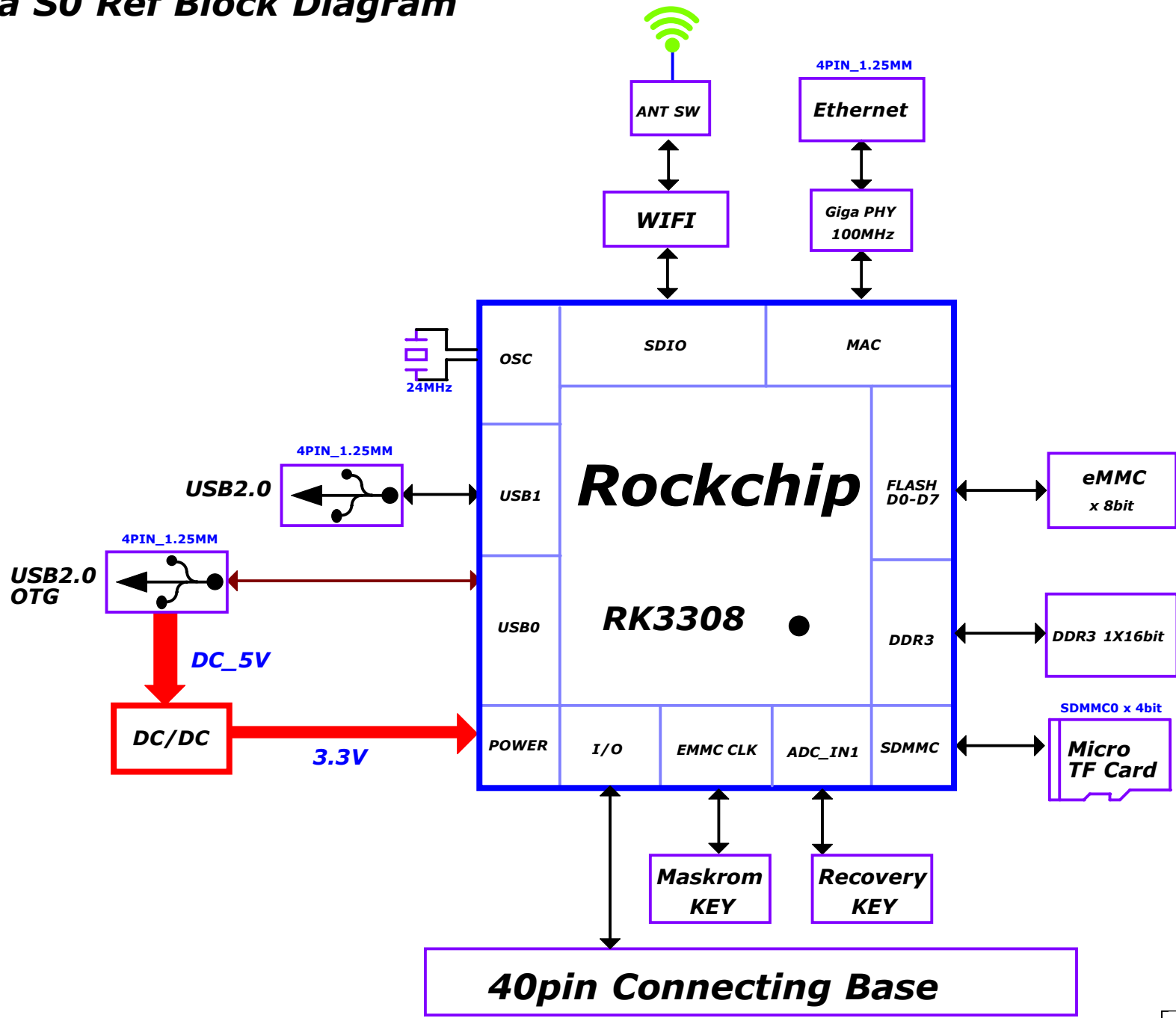
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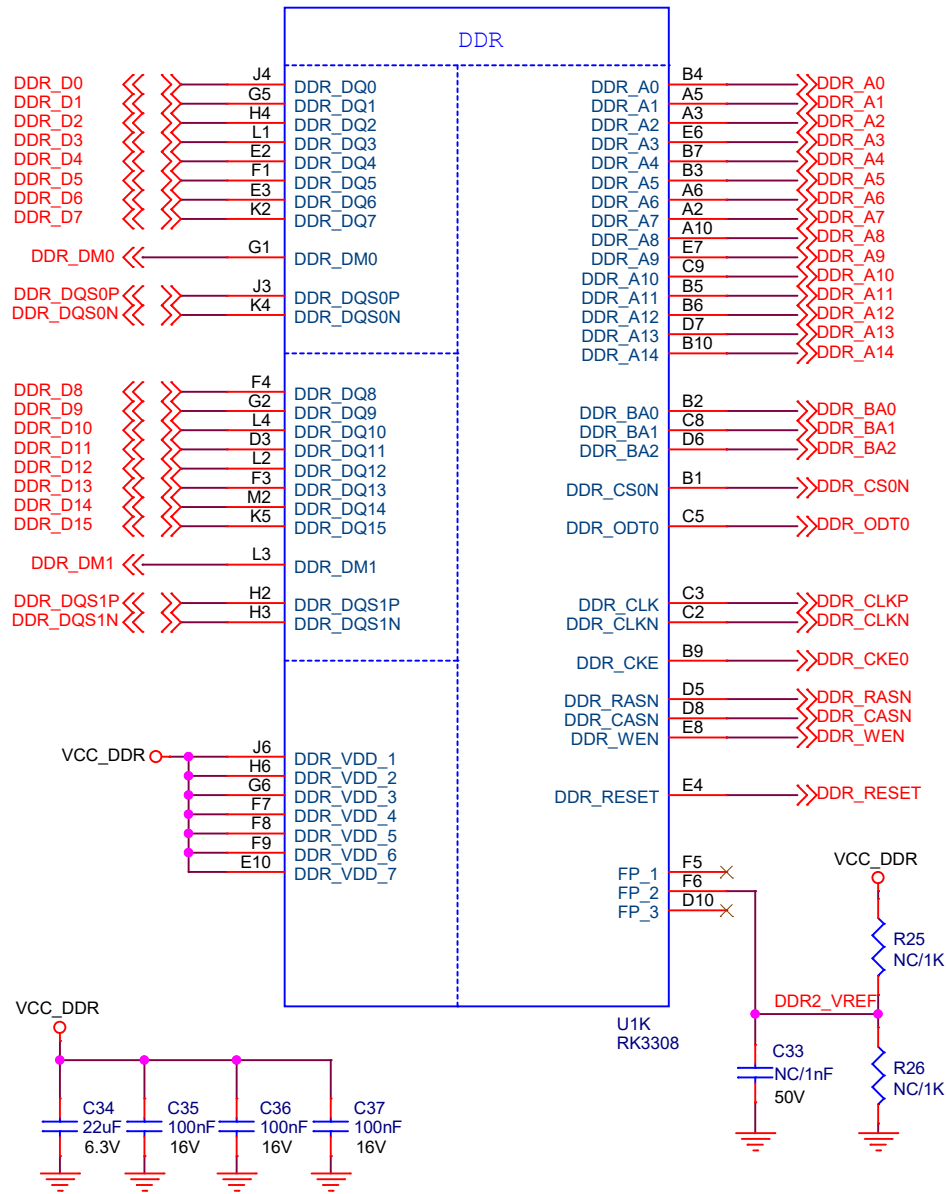


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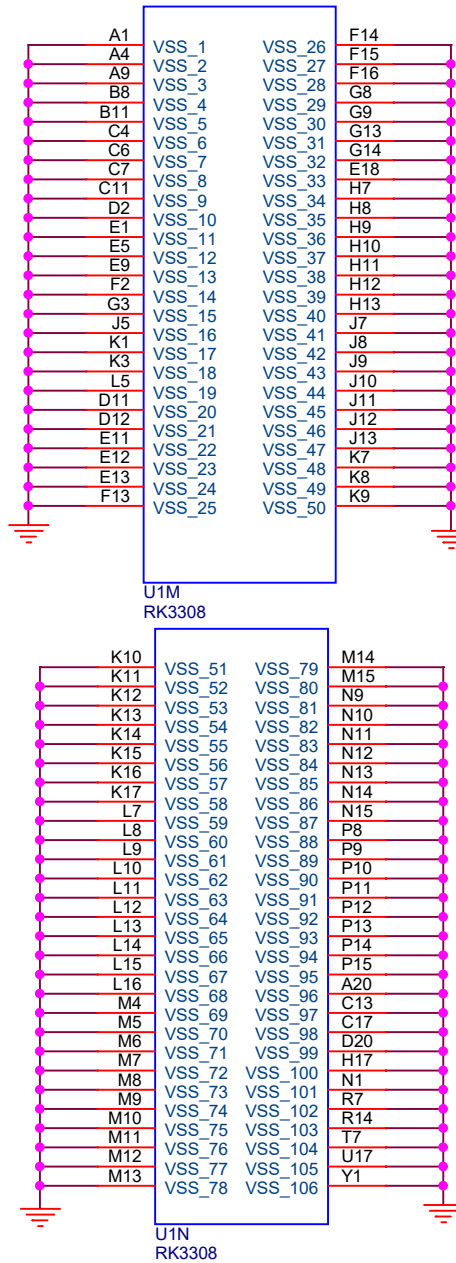
Radxa S0 Ref Block Diagram



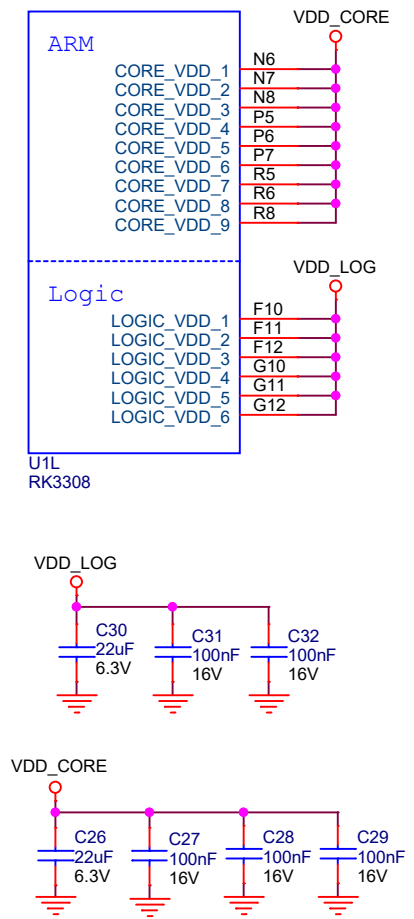
RK3308 Part-K



RK3308 Part-N

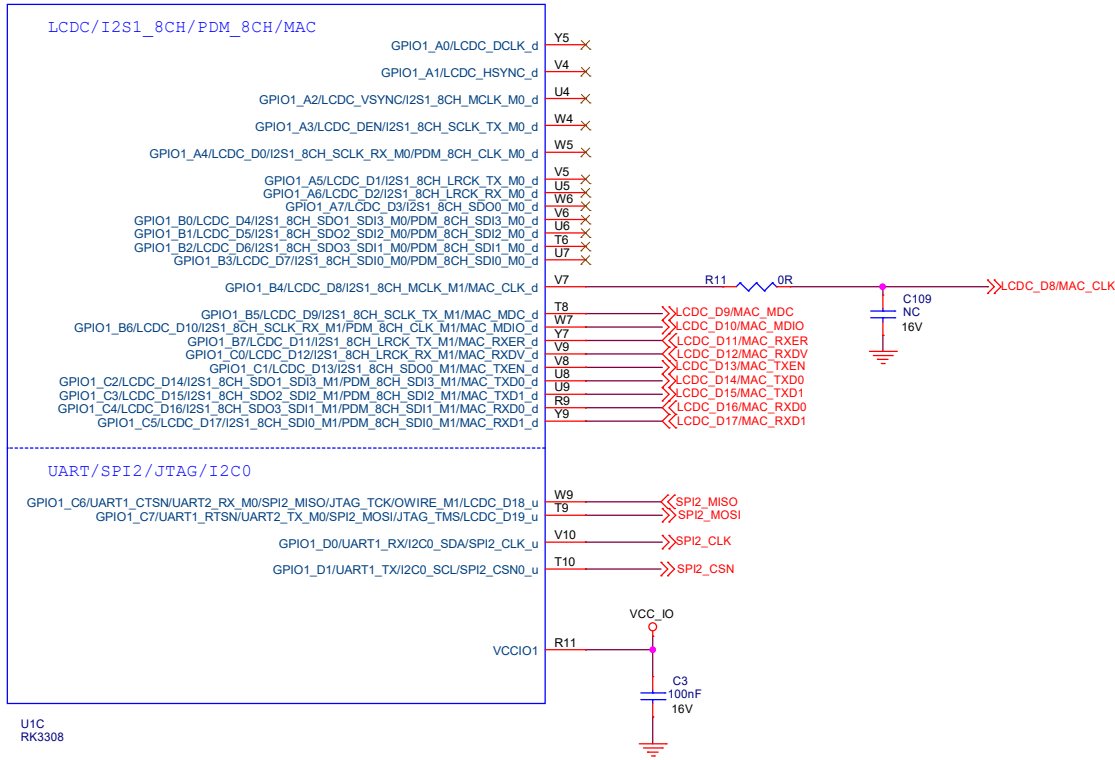


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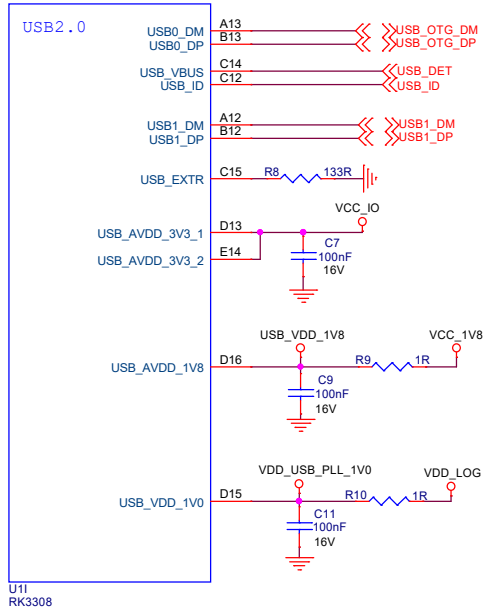


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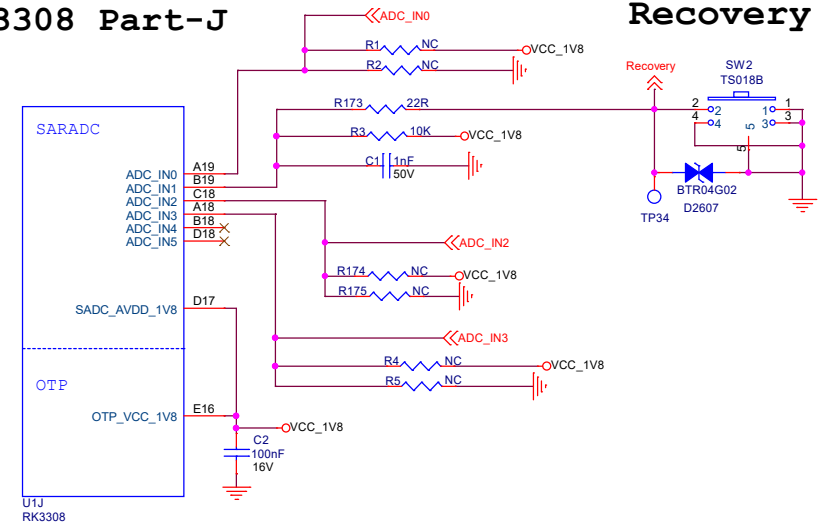
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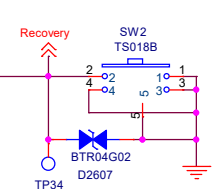
RK3308 Part-I



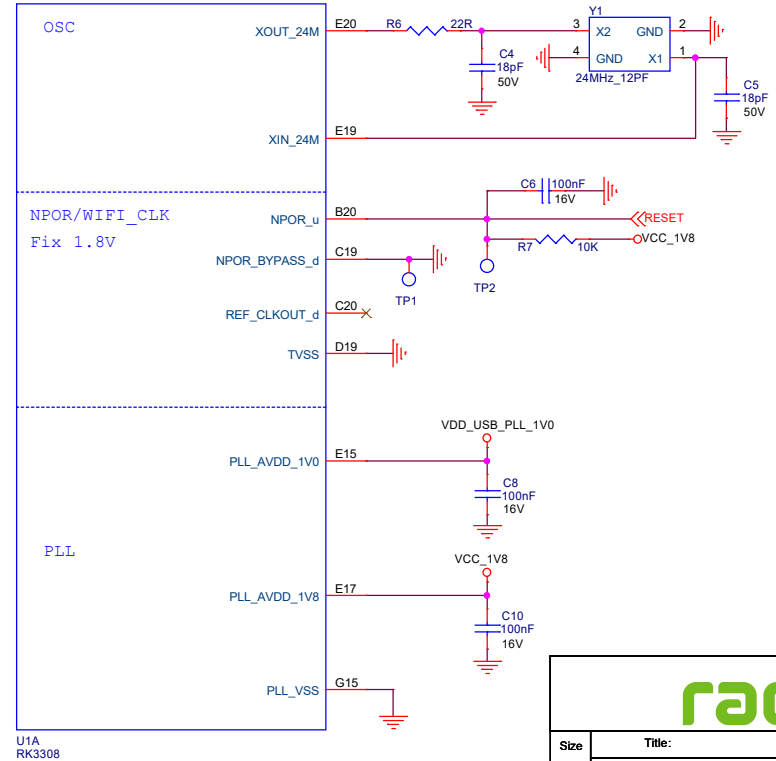
RK3308 Part-J



Recovery

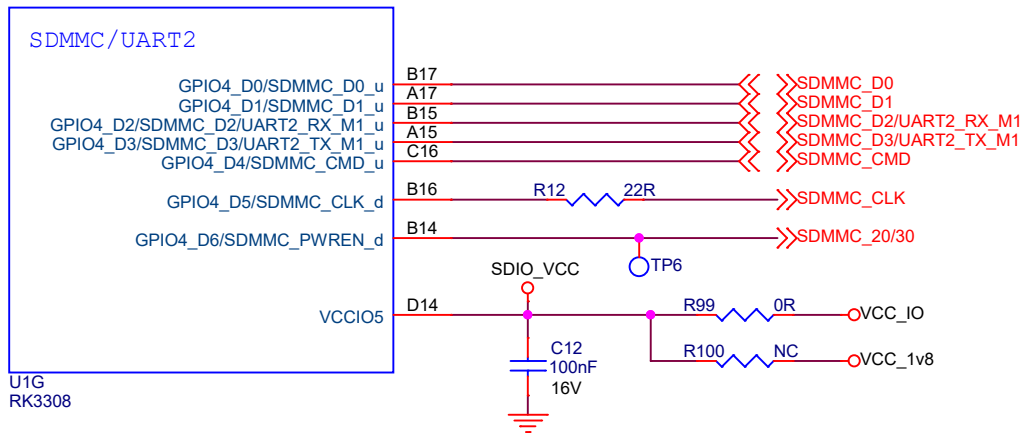


RK3308 Part-A

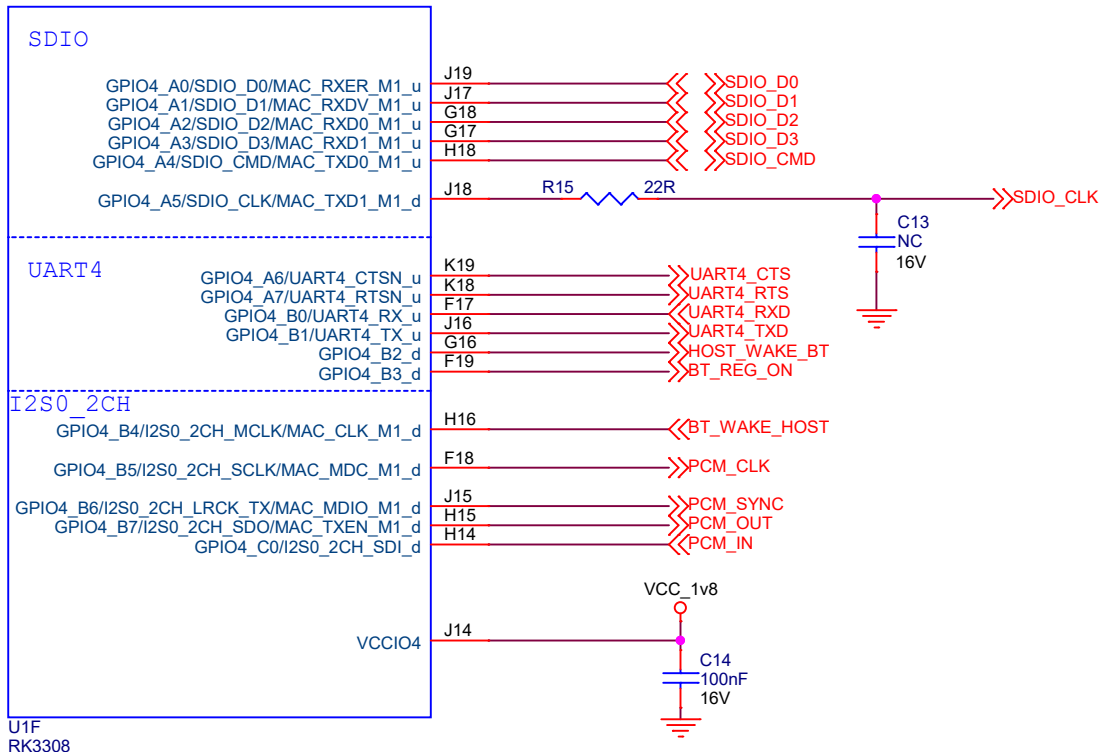


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RK3308 Part-G

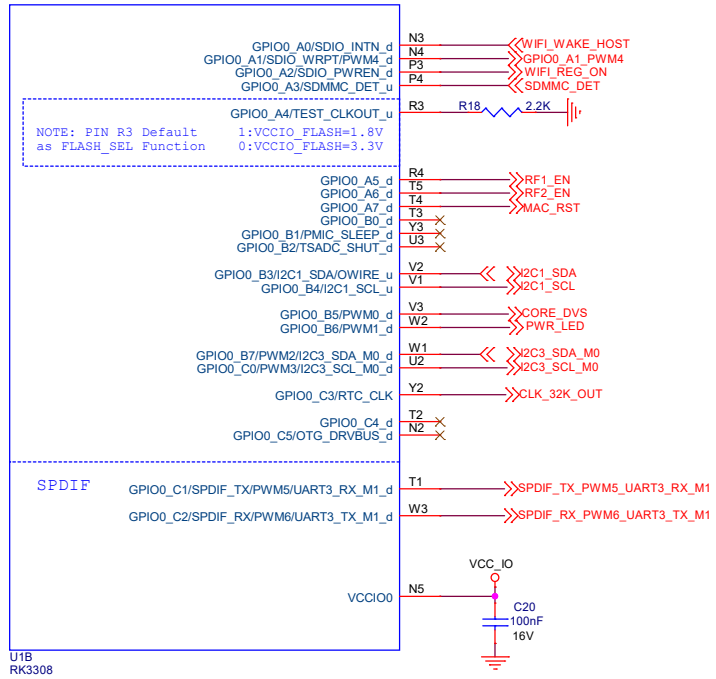


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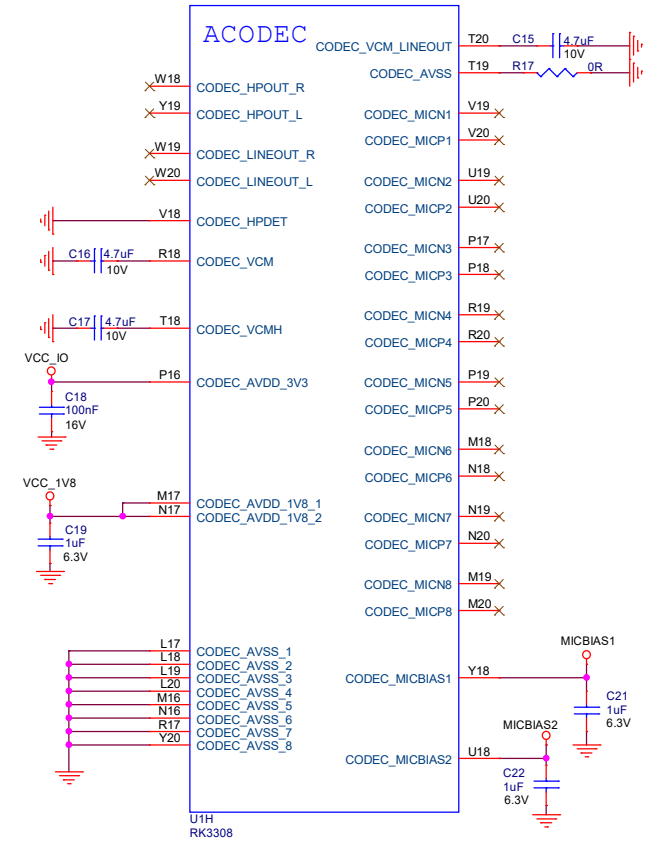
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RK3308 Part-B



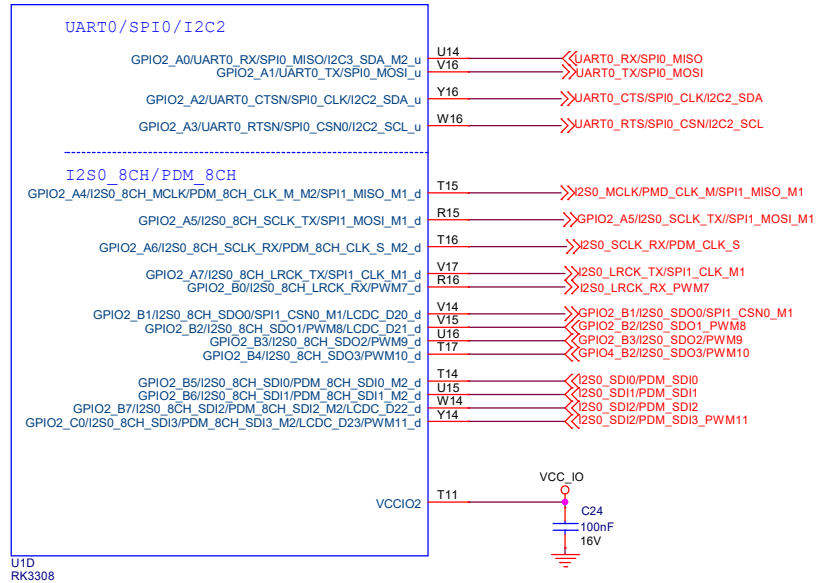
U1B
RK3308

RK3308 Part-H



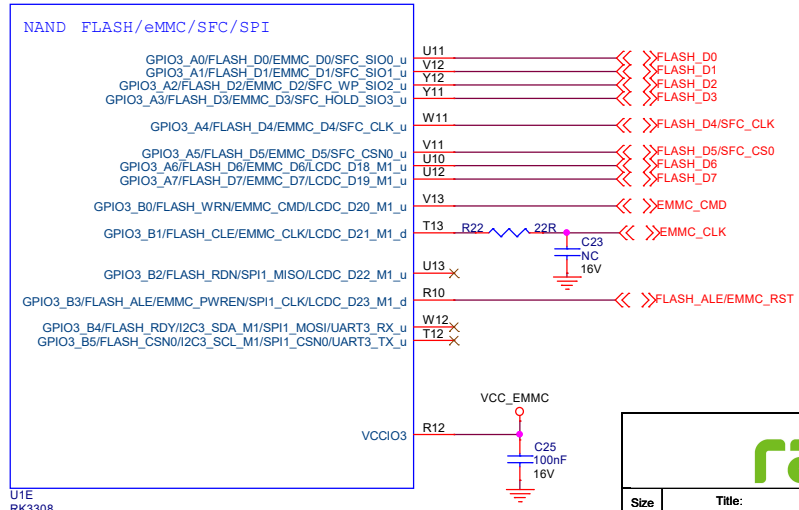
U1H
RK3308

RK3308 Part-D



U1D
RK3308

RK3308 Part-E

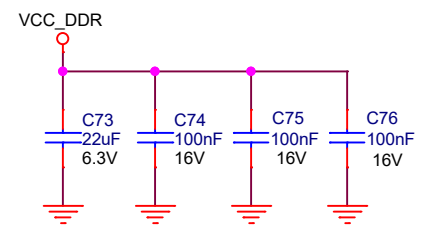
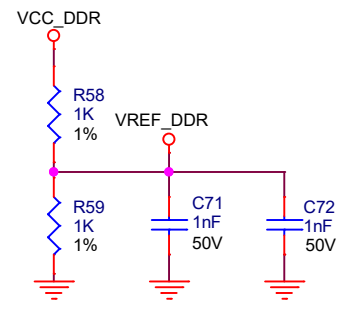
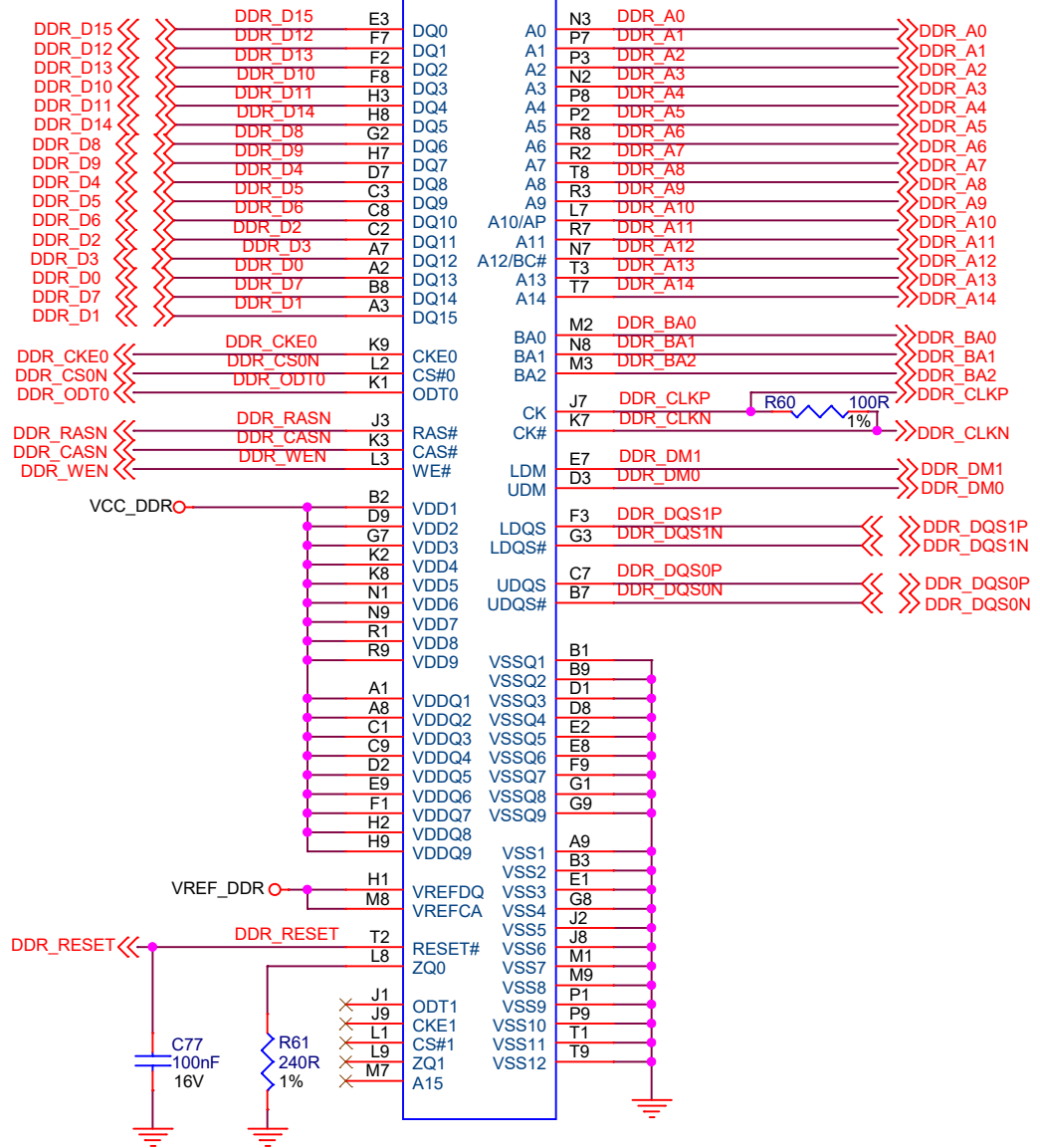



U1E
RK3308

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DDR3 1x16bit

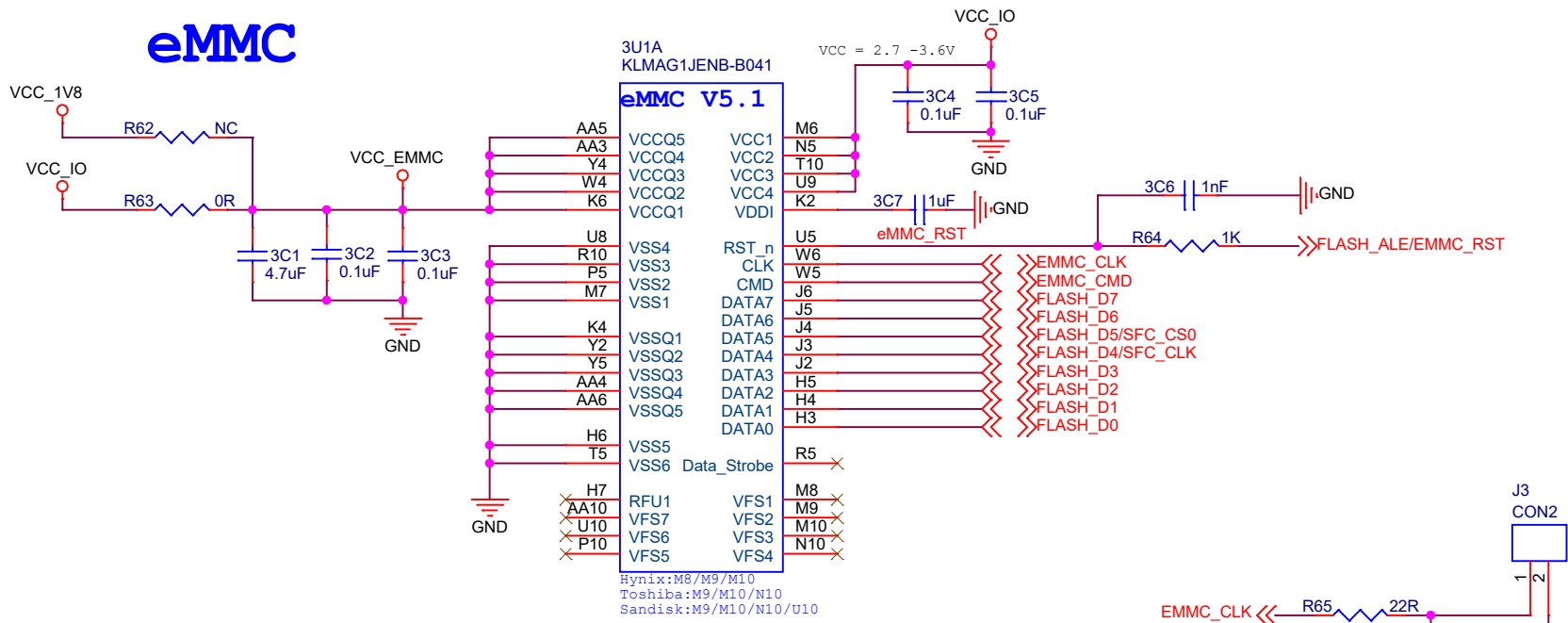
U7
K4B2G1646F-BYK0





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eMMC



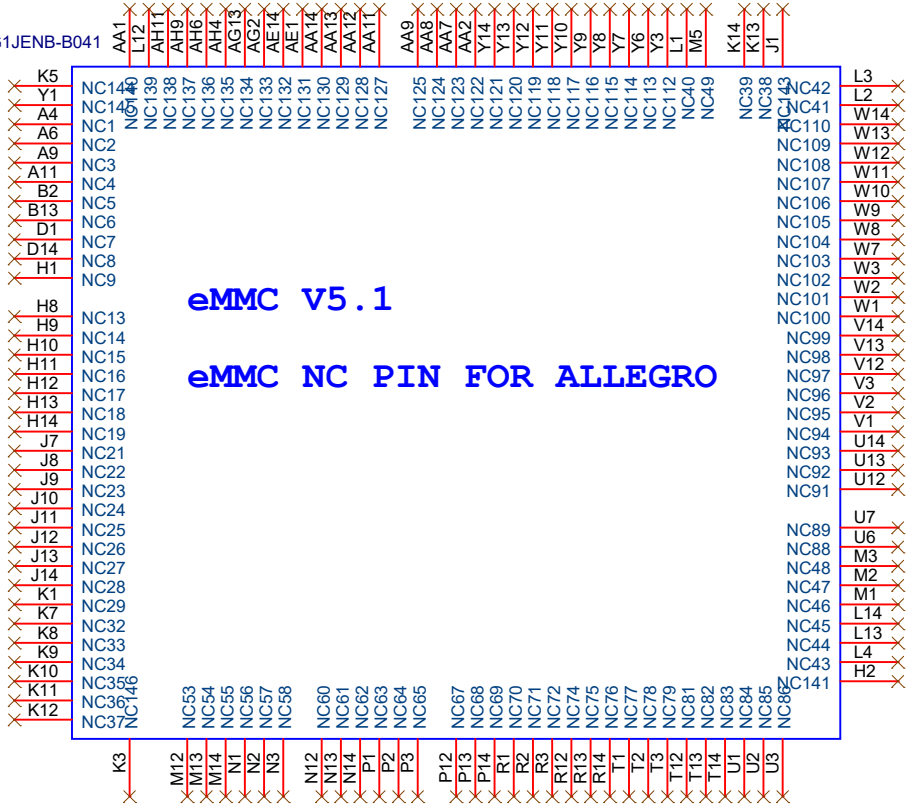
3U1A
KLMAG1JENB-B041

eMMC V5.1

- AA5 VCCQ5
- AA3 VCCQ4
- Y4 VCCQ3
- W4 VCCQ2
- K6 VCCQ1
- U8 VSS4
- R10 VSS3
- P5 VSS2
- M7 VSS1
- K4 VSSQ1
- Y2 VSSQ2
- Y5 VSSQ3
- AA4 VSSQ4
- AA6 VSSQ5
- H6 VSS5
- T5 VSS6
- H7 RFU1
- AA10 VFS7
- U10 VFS6
- P10 VFS5
- VCC1 M6
- VCC2 N5
- VCC3 T10
- VCC4 U9
- VDDI K2
- RST_n U5
- CLK W6
- CMD J6
- DATA7 J5
- DATA6 J4
- DATA5 J3
- DATA4 J2
- DATA3 H5
- DATA2 H4
- DATA1 H3
- DATA0 H3
- Data_Strobe R5
- VFS1 M8
- VFS2 M9
- VFS3 M10
- VFS4 N10

Hynix:M8/M9/M10
Toshiba:M9/M10/N10
Sandisk:M9/M10/N10/U10

3U1B
KLMAG1JENB-B041



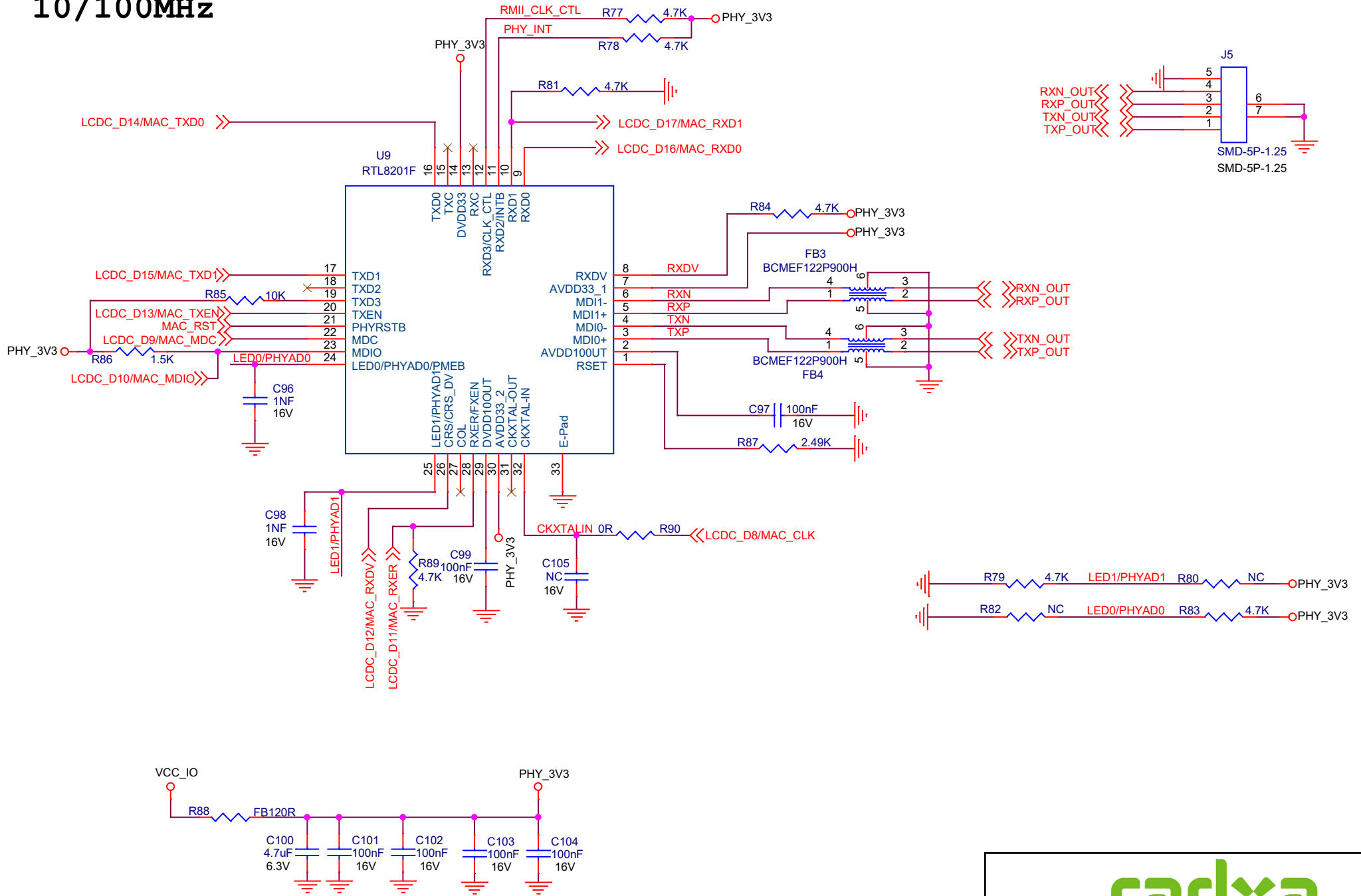
eMMC V5.1

eMMC NC PIN FOR ALLEGRO

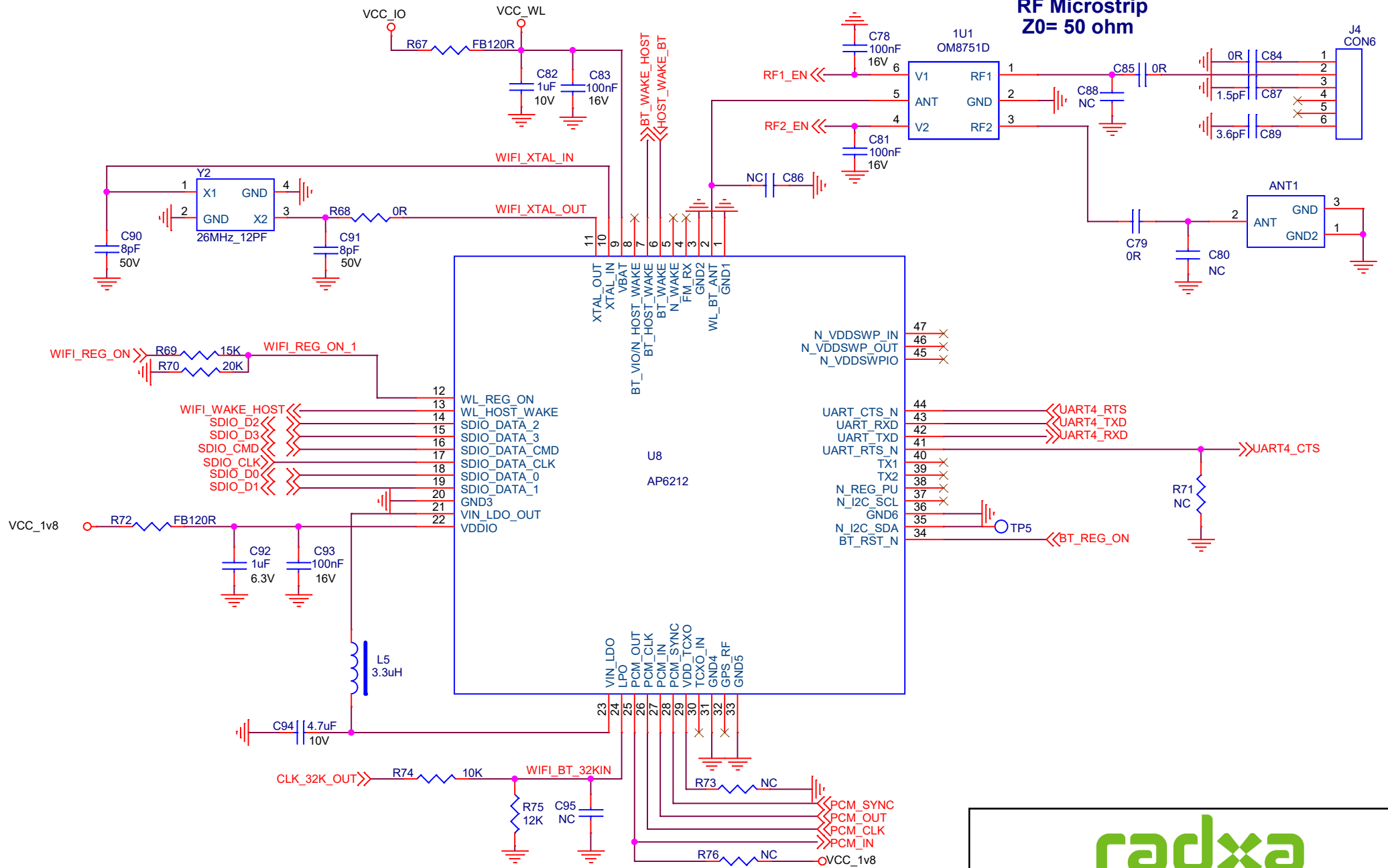


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A4	Page Name: EMMC Flash	1.1
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10/100MHz



radxa		
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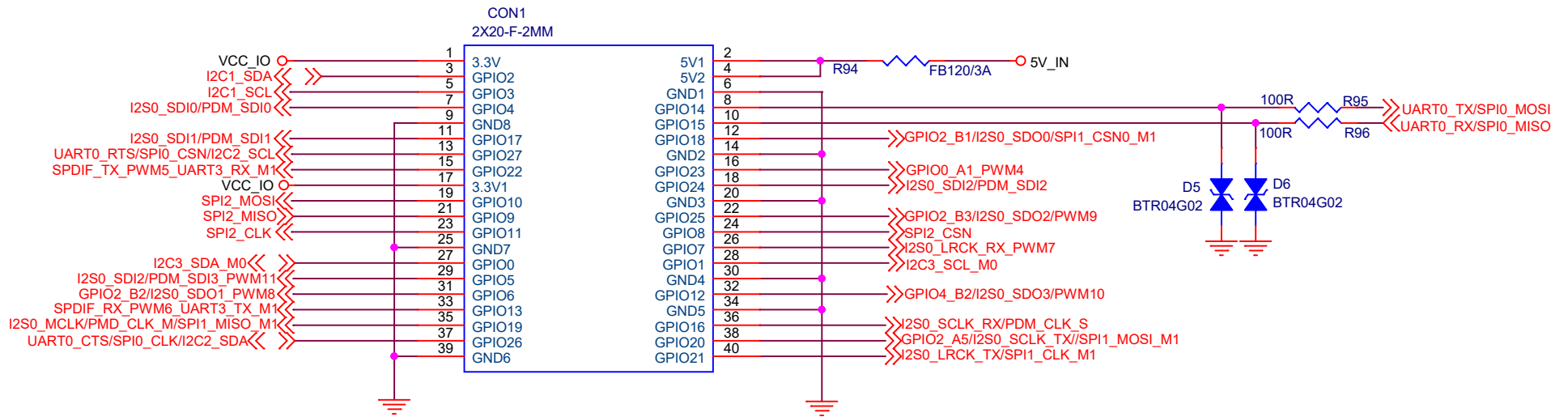
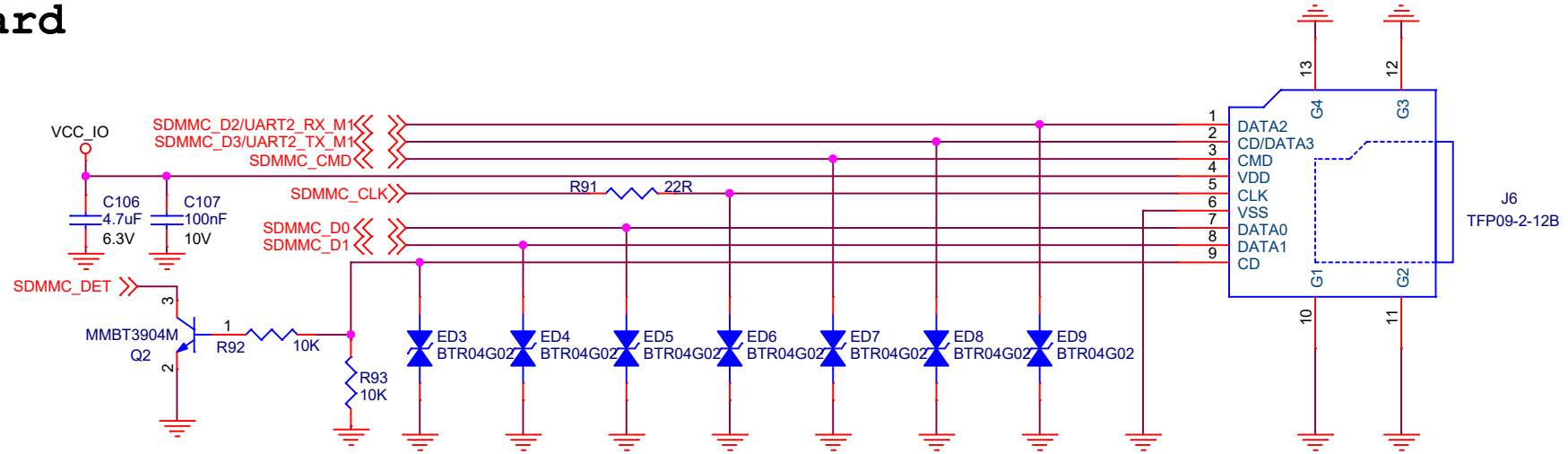


**RF Microstrip
Z0= 50 ohm**



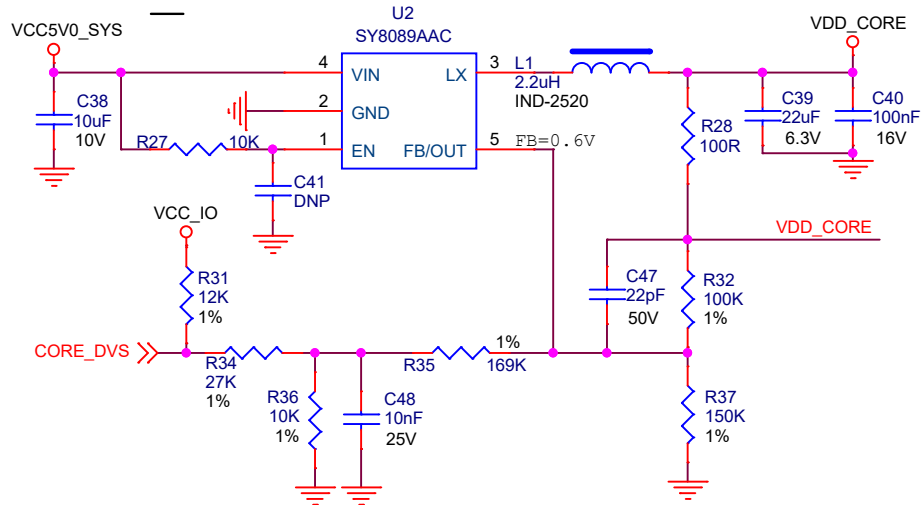
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TF Card

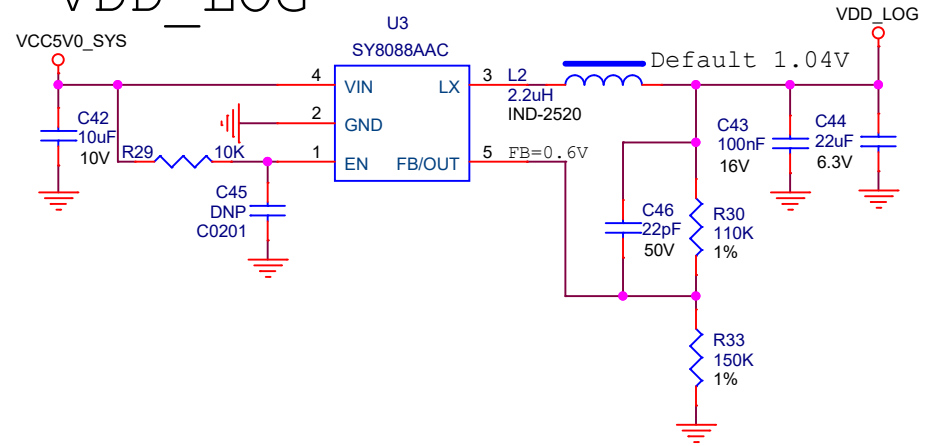


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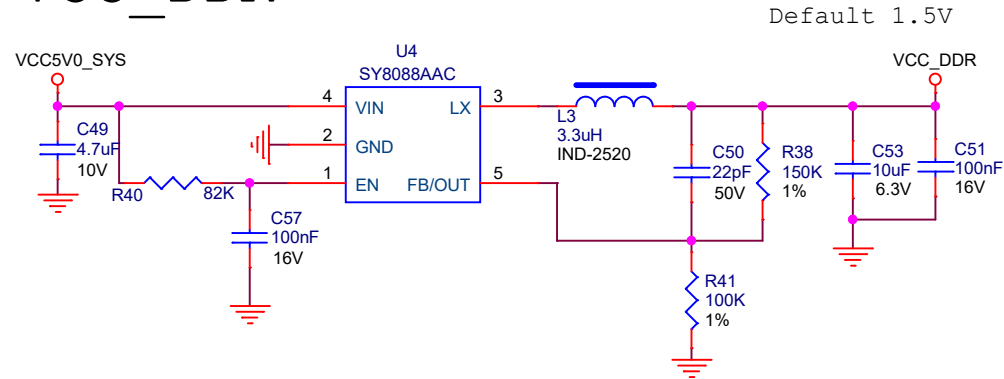
VDD_ARM



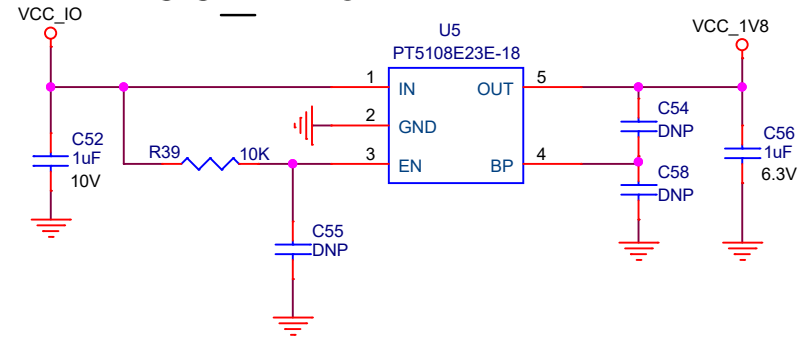
VDD_LOG



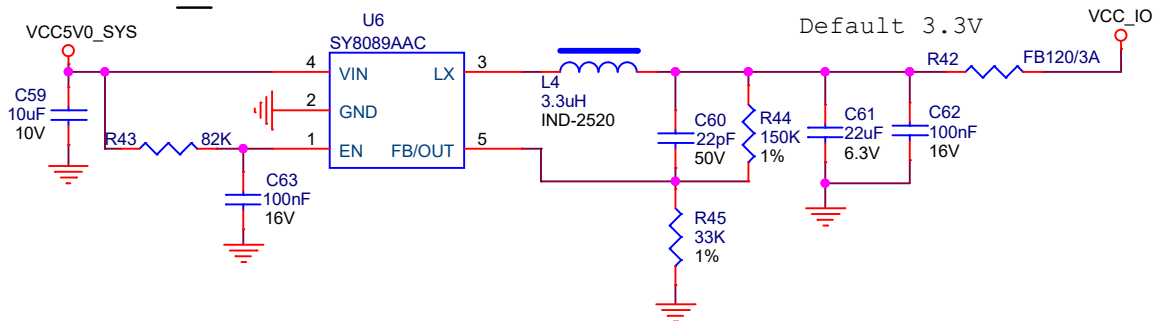
VCC_DDR



VCC_1V8

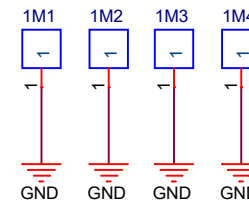
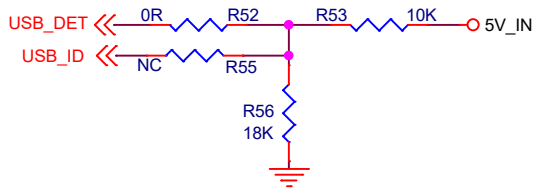
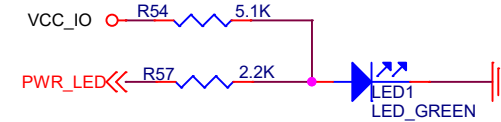
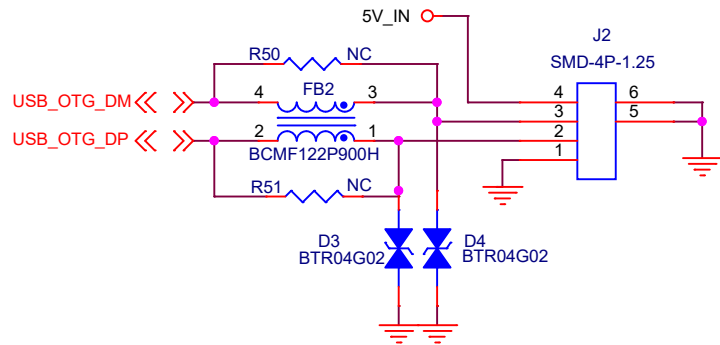
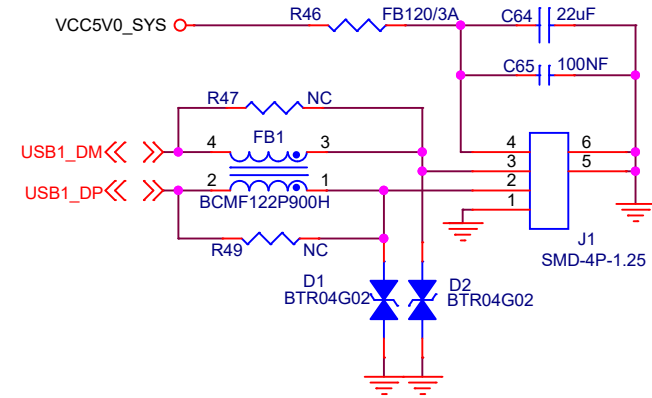
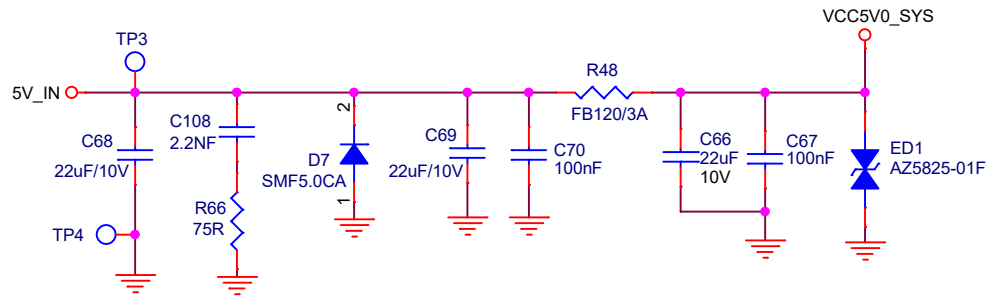


VCC_IO



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USB OTG



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