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Generate Bill of Materials

Header:

Item\tPart\tDescription\tPCB Footprint\tReference\tQuantity\tOption

Combined property string:

{Item}\t{Value}\t{Description}\t{PCB Footprint}\t{Reference}\t{Quantity}\t{Option}

Description

Note

Option

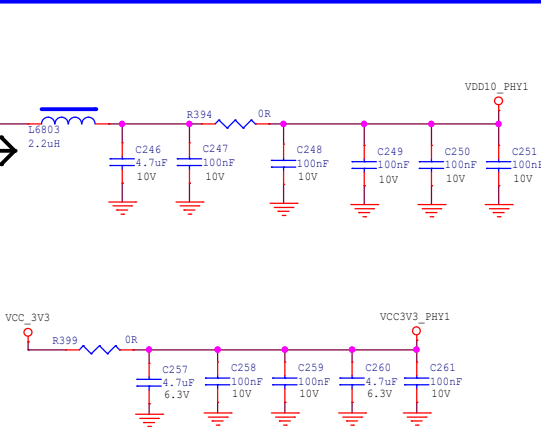
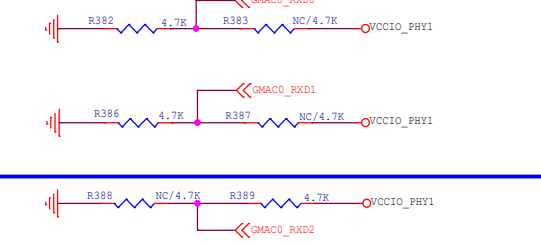
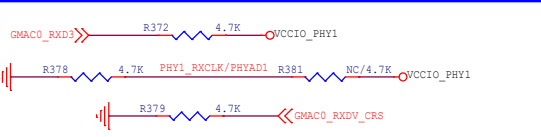
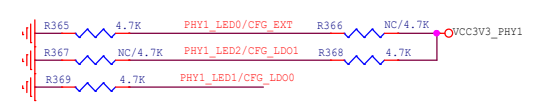
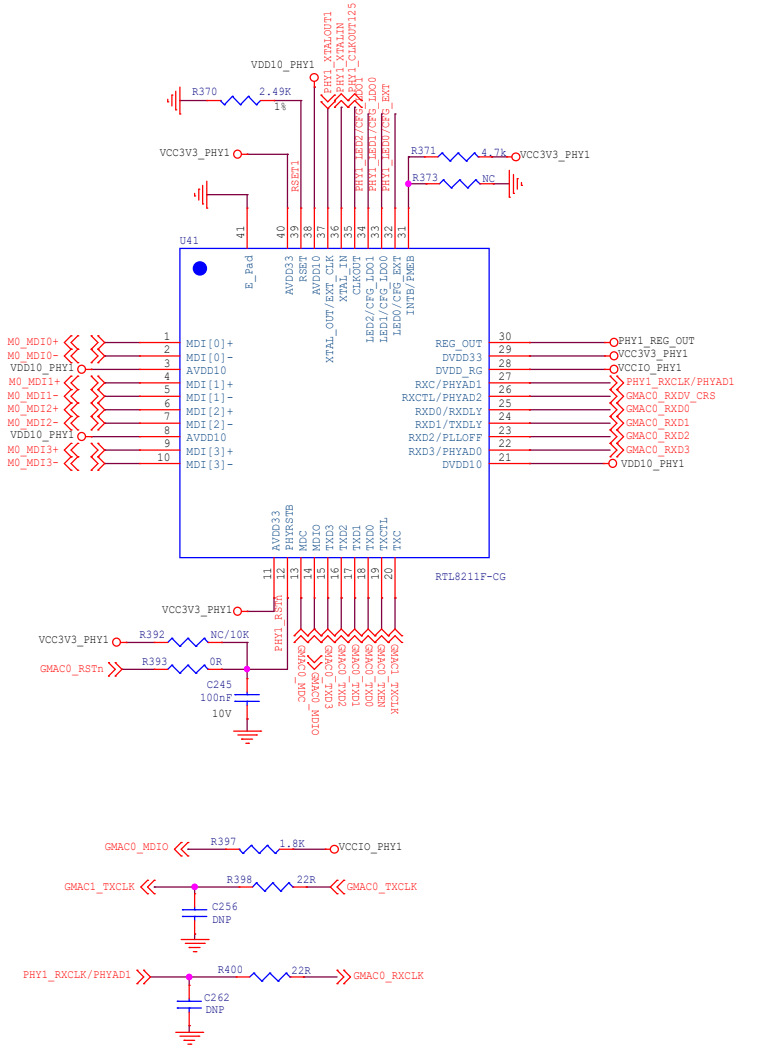
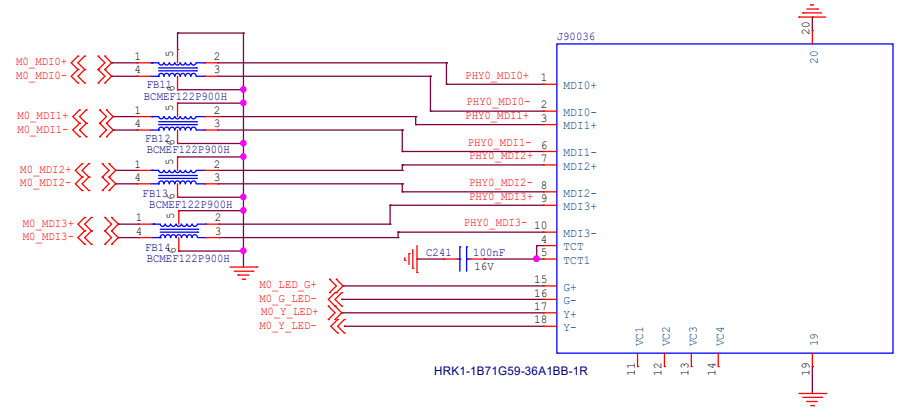
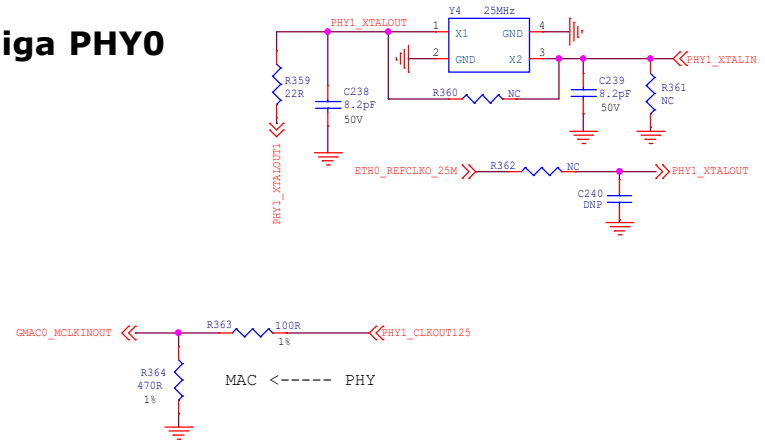
Notes

NOTE 1:
Component parameter description
1. DNP stands for component not mounted temporarily
2. If Value or option is DNP, which means the area is reserved without being mounted

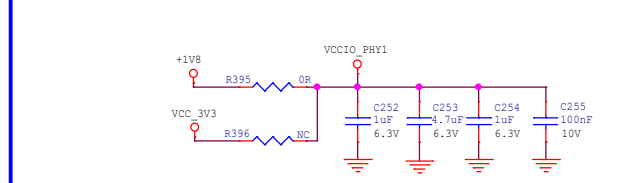
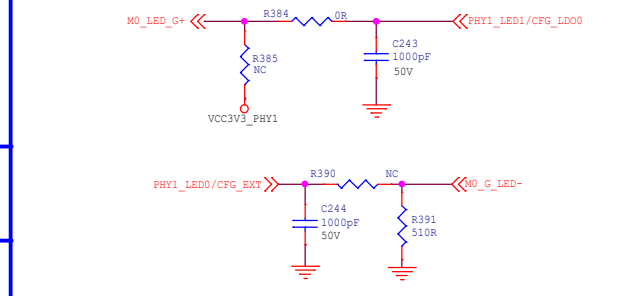
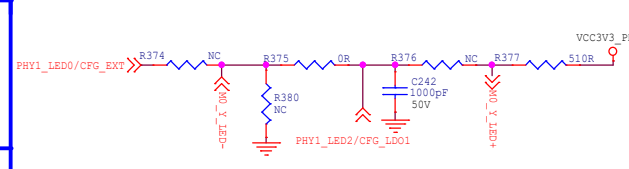
NOTE 2:
Please use our recommended components to avoid too many changes.
For more informations about the second source, please refer to our AVL.




Giga PHY0



RGMIIPower Source	CFG_EXT	CFG_LD0[1:0]
External 3.3V	1'b1	2'b00
External 1.8V	1'b1	2'b10
Internal 1.8V(default)	1'b0	2'b10



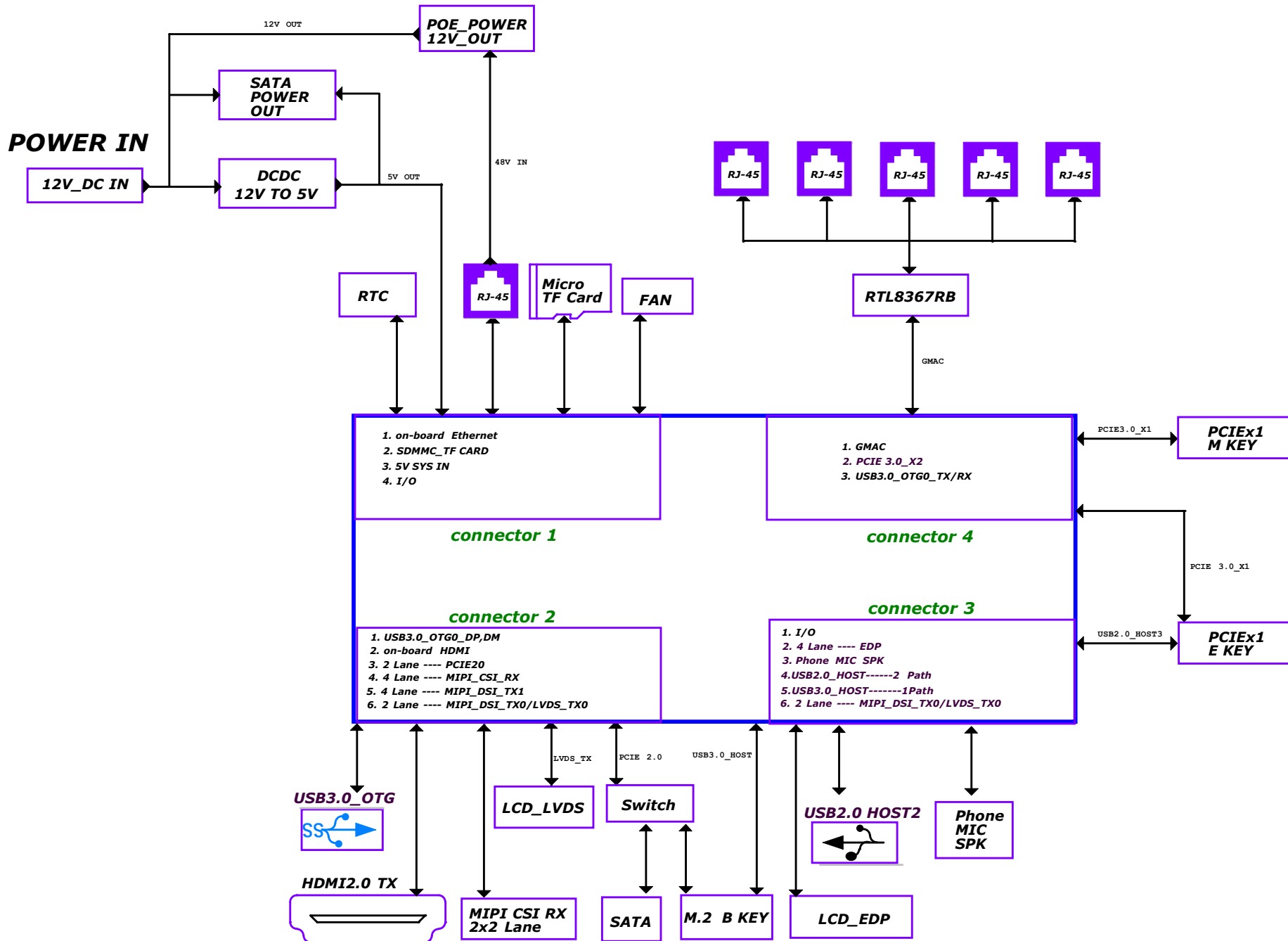


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Date: Sunday, April 23, 2023	Sheet 1 of 25	

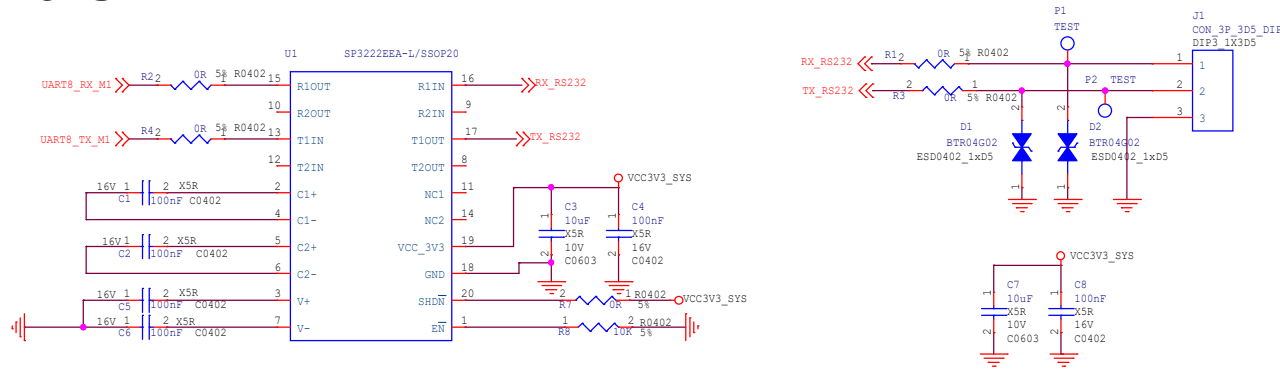
Revision History

Version	Date	By	Change Description	Approved
V1.0	2022-04			

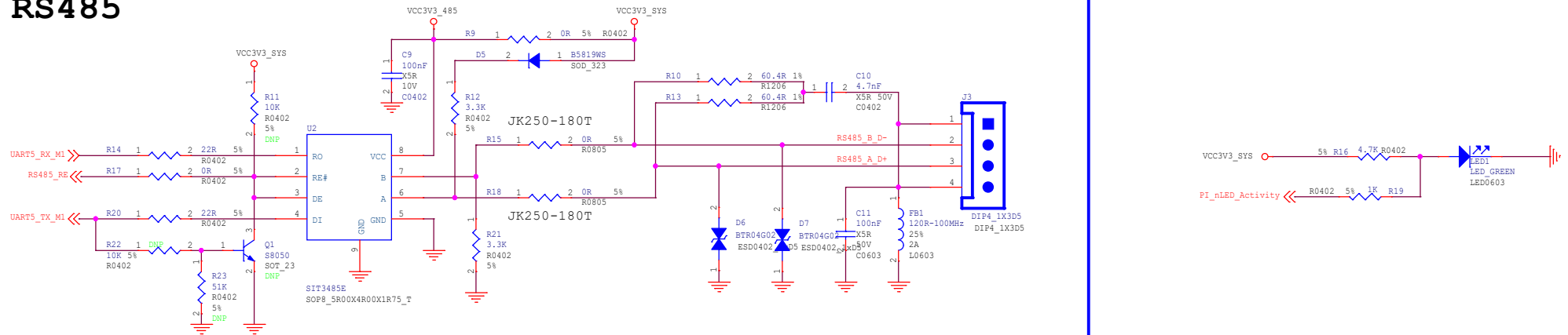




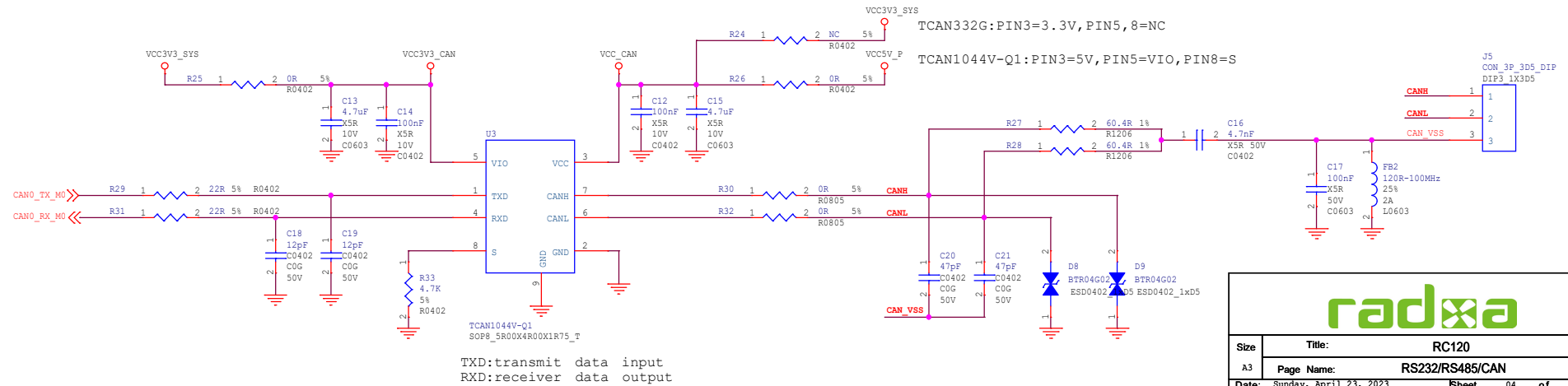
RS232



RS485

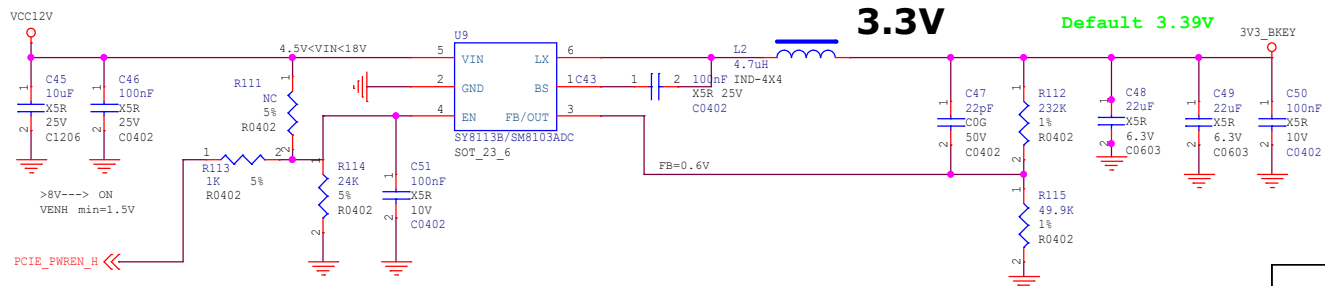
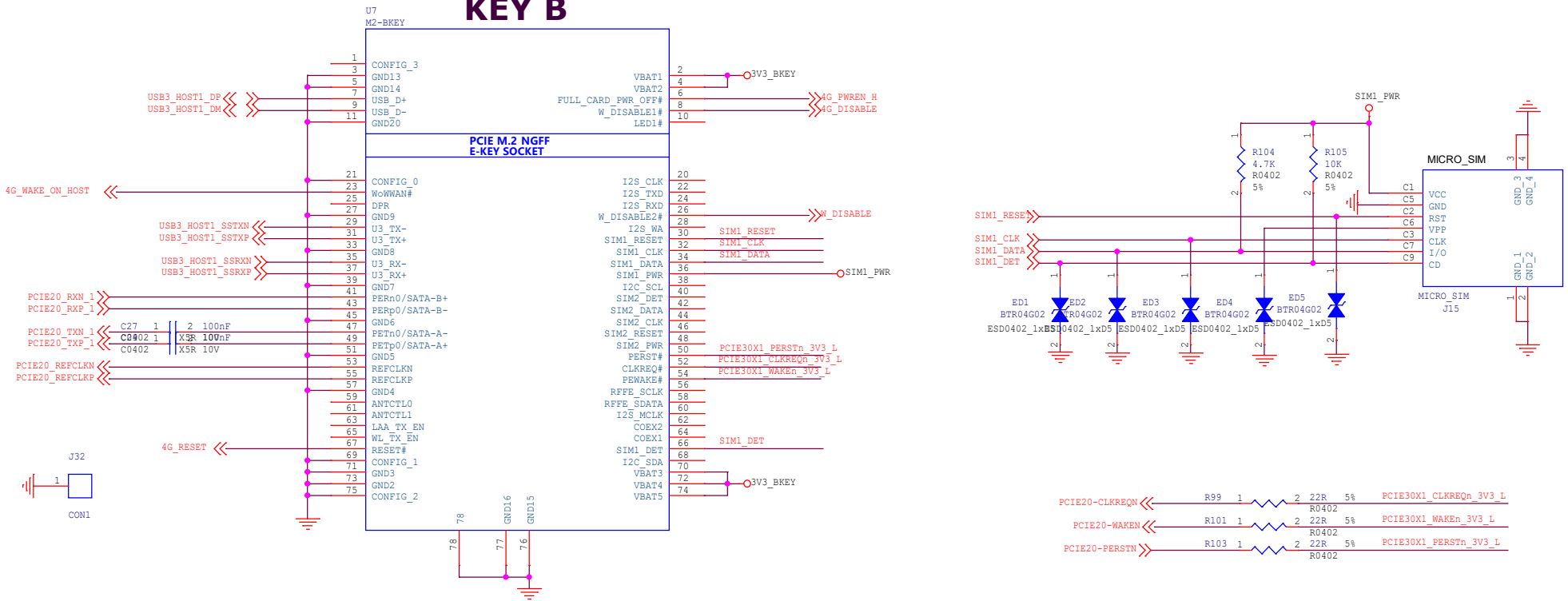


CAN

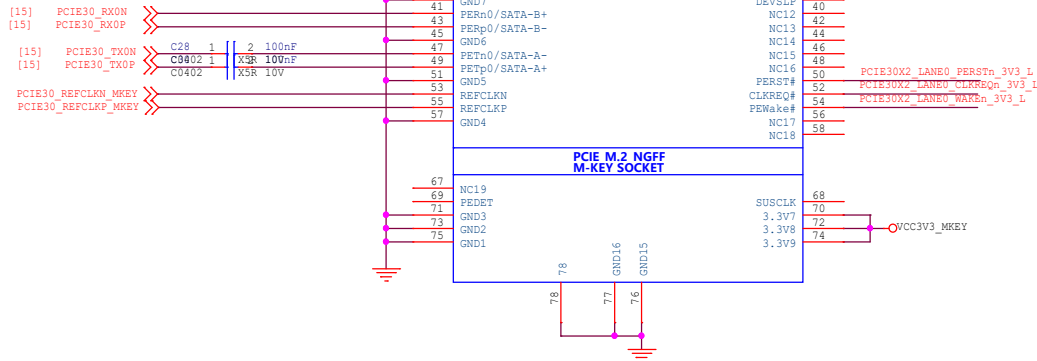
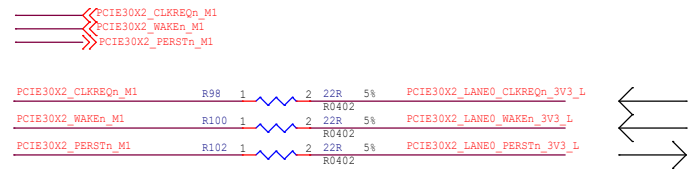
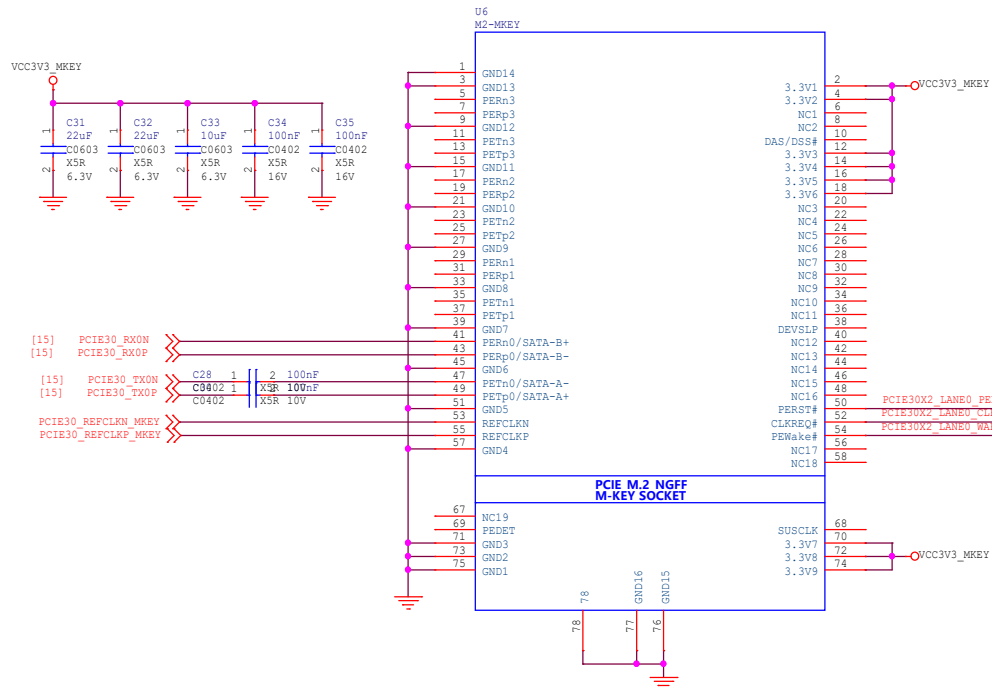


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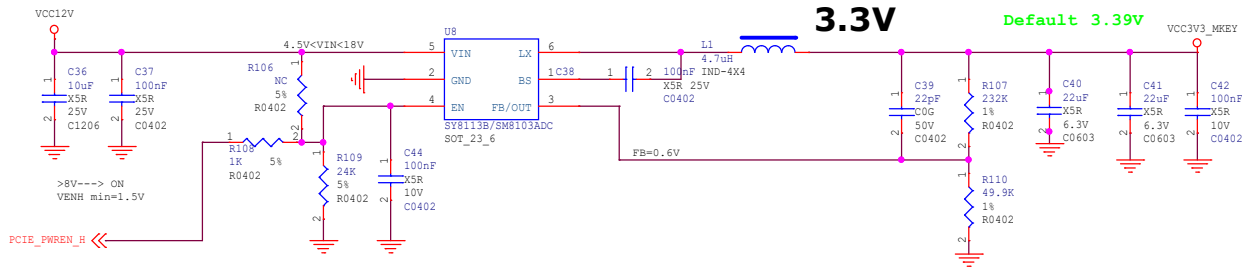
KEY B



Size	Title:	RC120	REV
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Date:	Sunday, April 23, 2023	Sheet	08 of 27

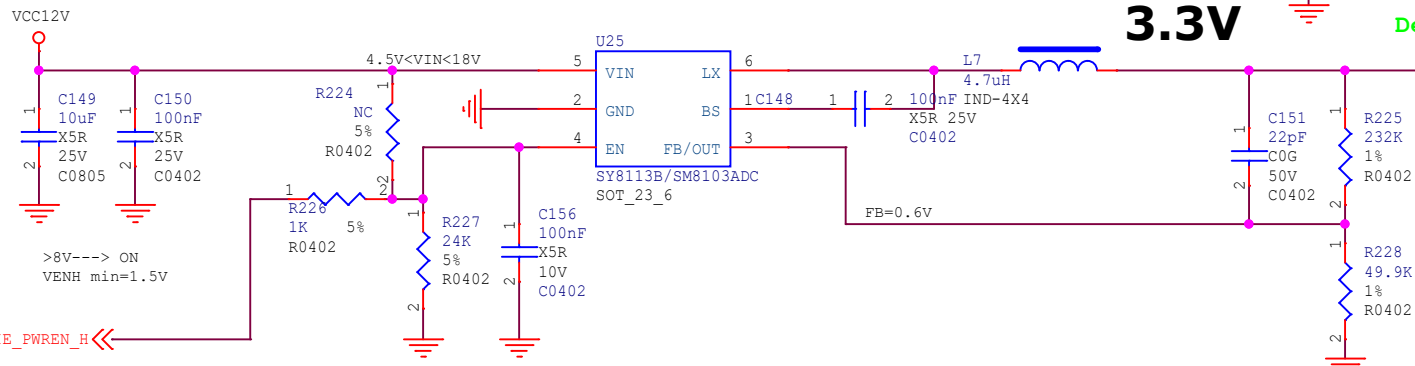
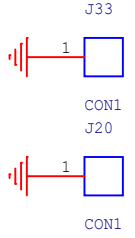
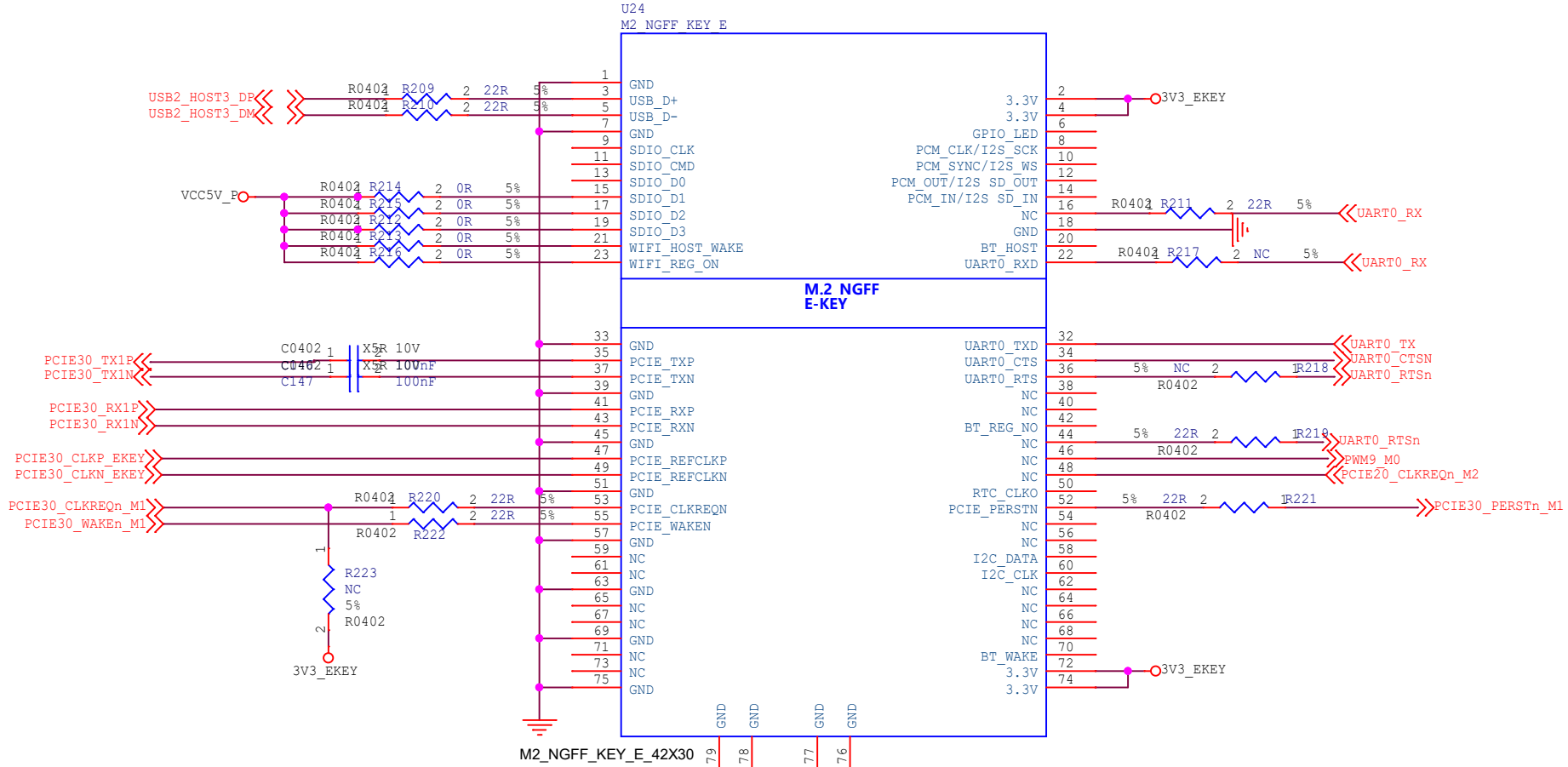


3.3V Default 3.39V

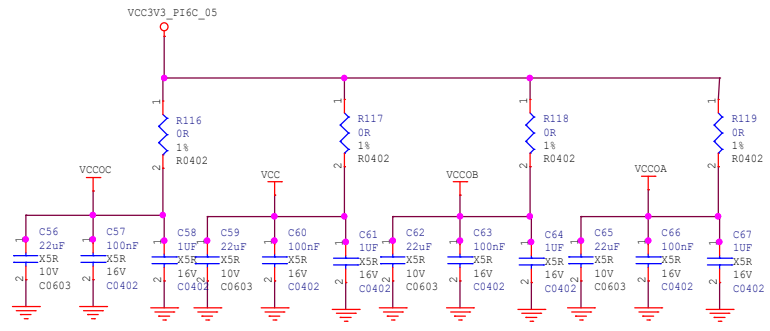
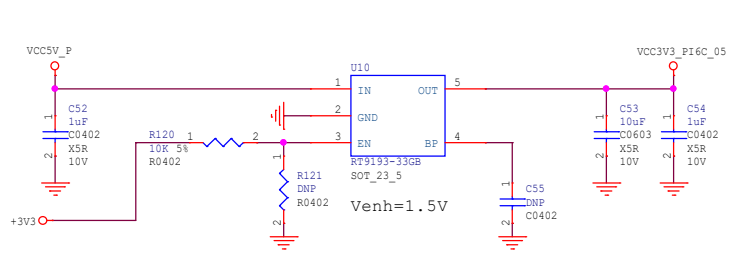


SDIO WIFI/BT MODULE

WIFI NGFF/M.2



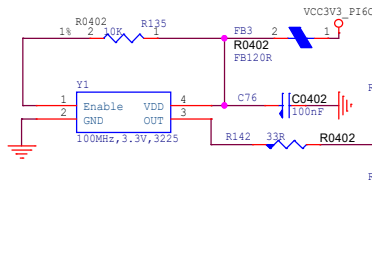
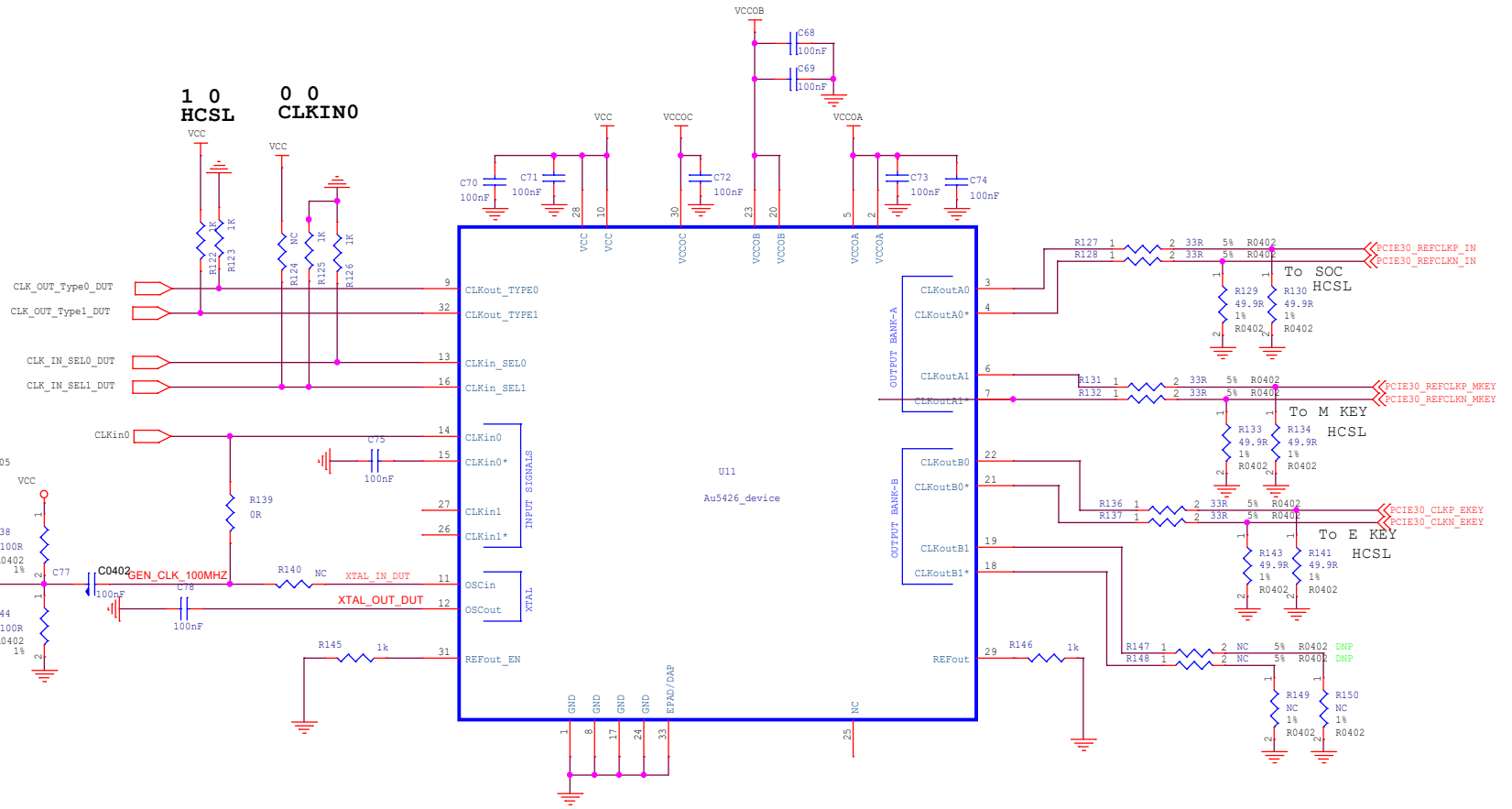
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A4	Page Name:	M.2 E KEY	V1.0
Date:	Sunday, April 23, 2023	Sheet	10 of 27



```

CLKOUT_Type1 CLKOUT_Type0 CLK Buffer Type
0 0 LVPECL
0 1 LVDS
1 0 HCSL
1 1 HZ

CLKin_SEL[1] CLKin_SEL[0] Selected Clock
0 0 CLKIN0, CLKIN1*
0 1 CLKIN1, CLKIN1*
1 0 Crystal
Or
Crystal bypass AC
coupled mode
1 1 Crystal bypass DC
coupled mode
  
```



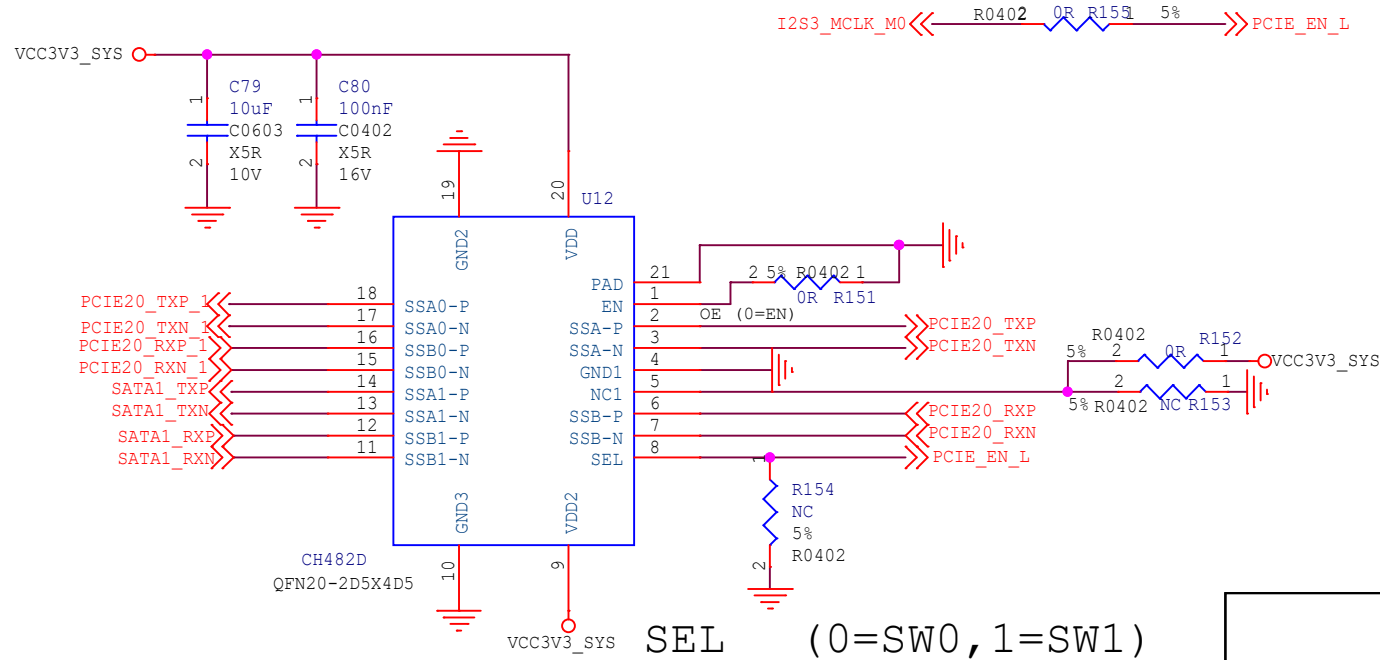
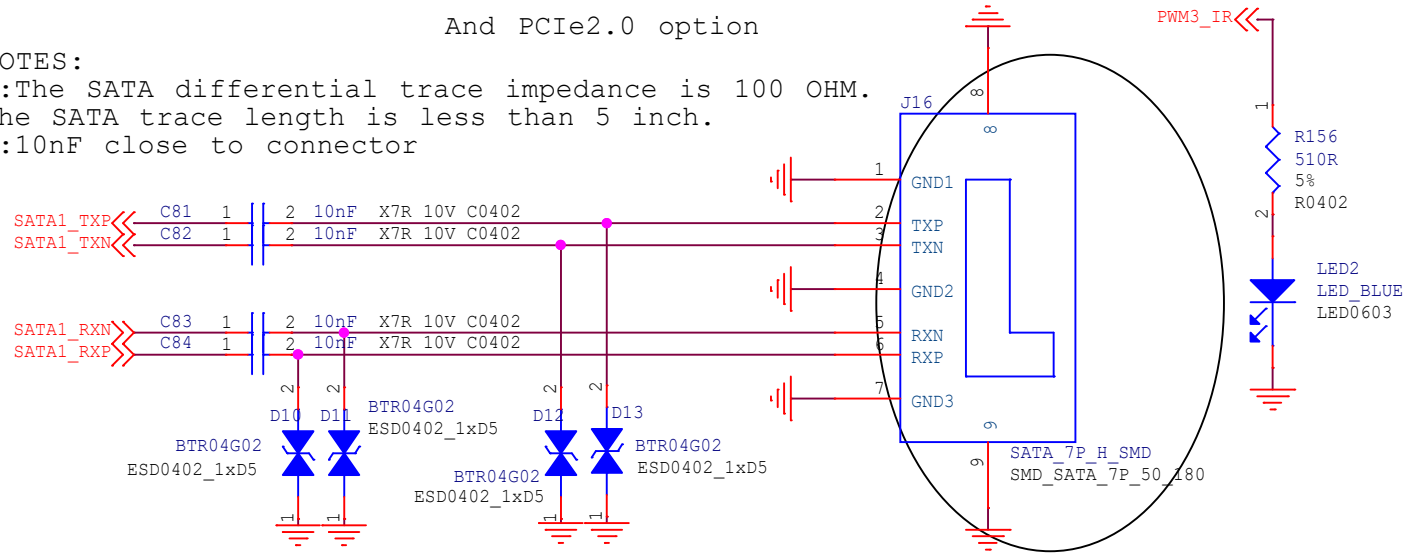
Size	Title:	RC120	REV
A3	Page Name:	PCIE_CLK	V1.0
Date:	Sunday, April 23, 2023	Sheet	11 of 27

SATA3.0 Port2

And PCIe2.0 option

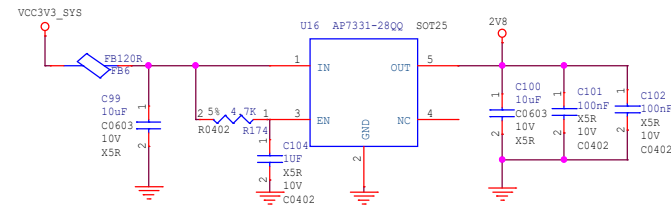
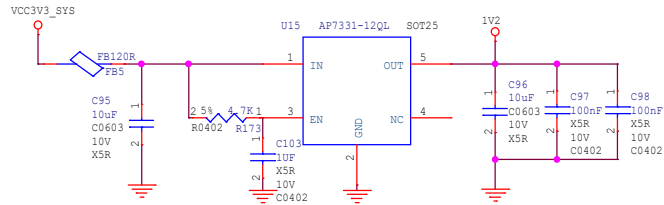
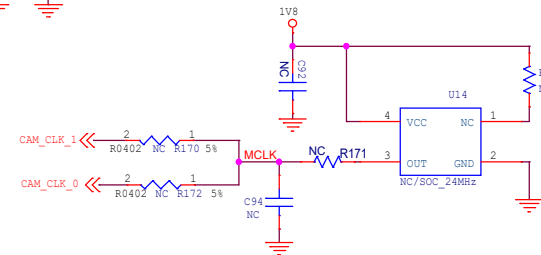
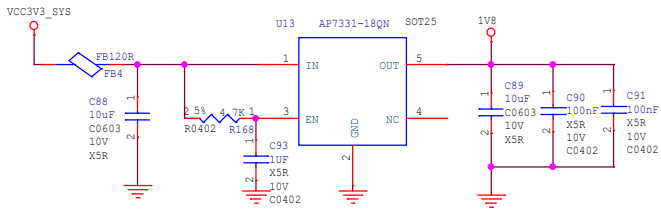
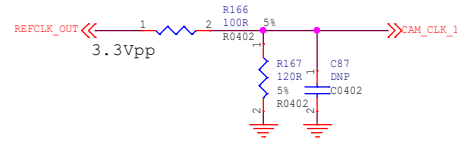
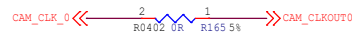
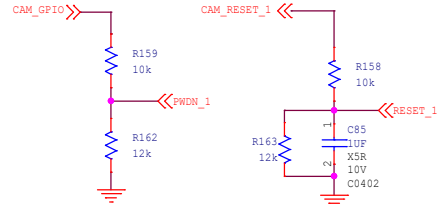
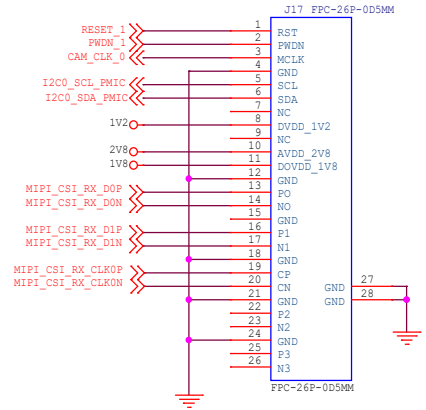
NOTES:

- 1:The SATA differential trace impedance is 100 OHM.
- The SATA trace length is less than 5 inch.
- 2:10nF close to connector

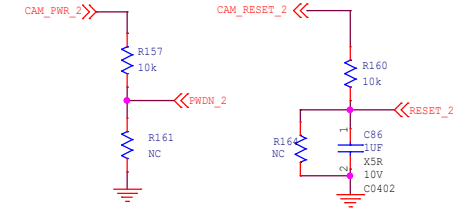
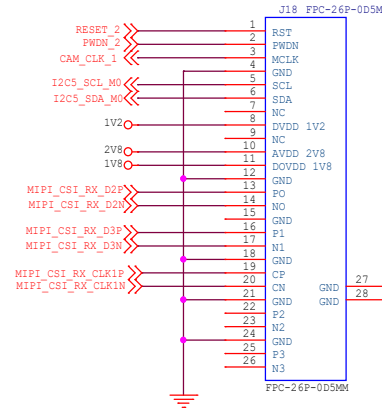


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Date:		Sunday, April 23, 2023	Sheet	12 of 27

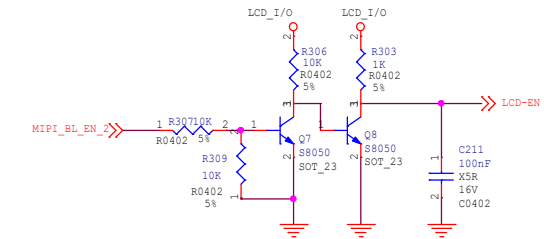
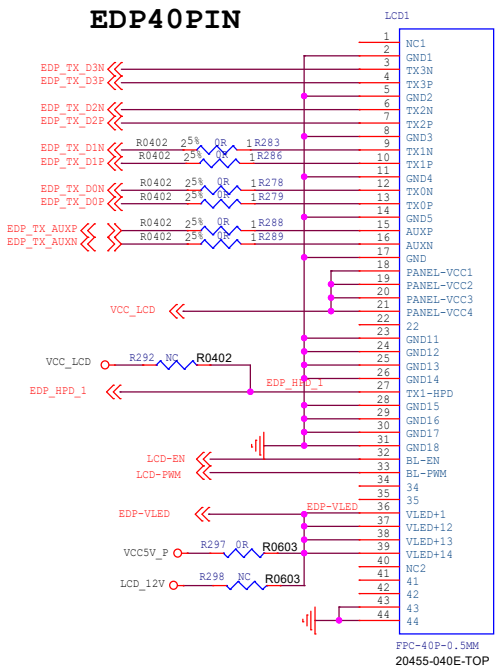
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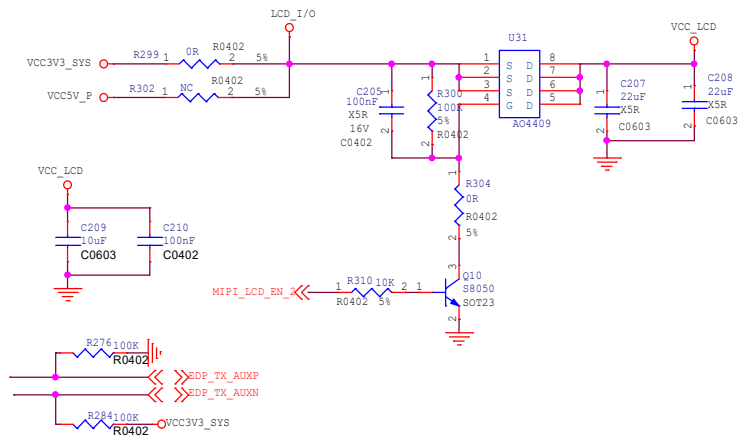
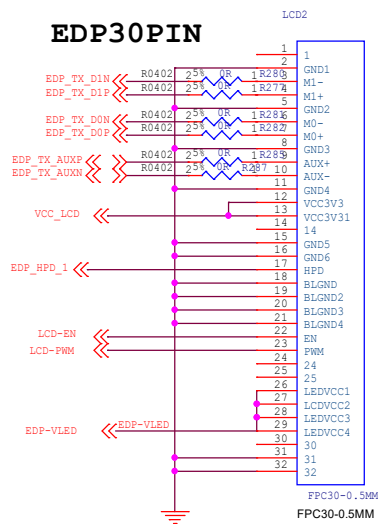
MIPI_CSI_RX 2Lanes



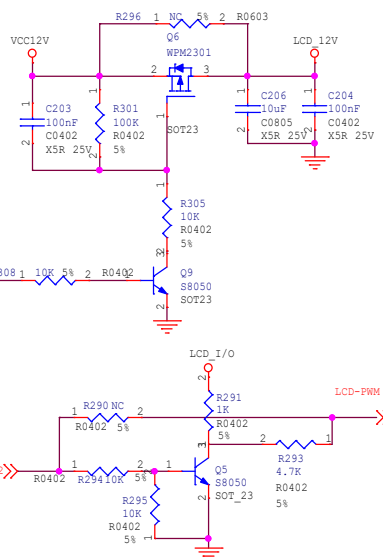
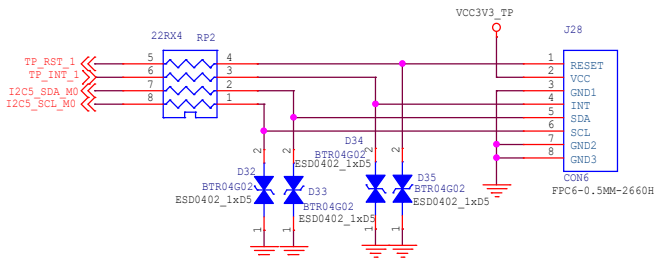
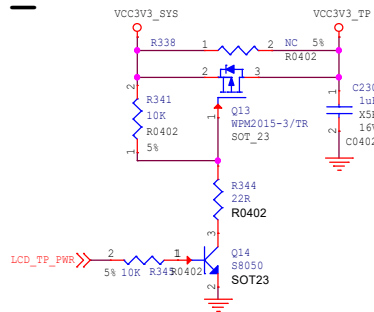
EDP40PIN



EDP30PIN

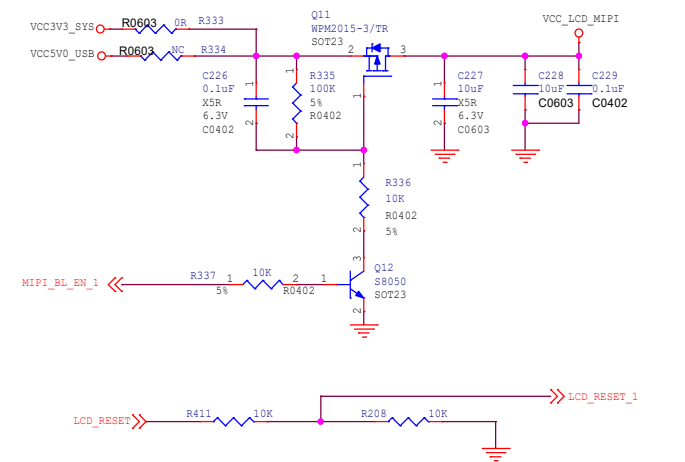
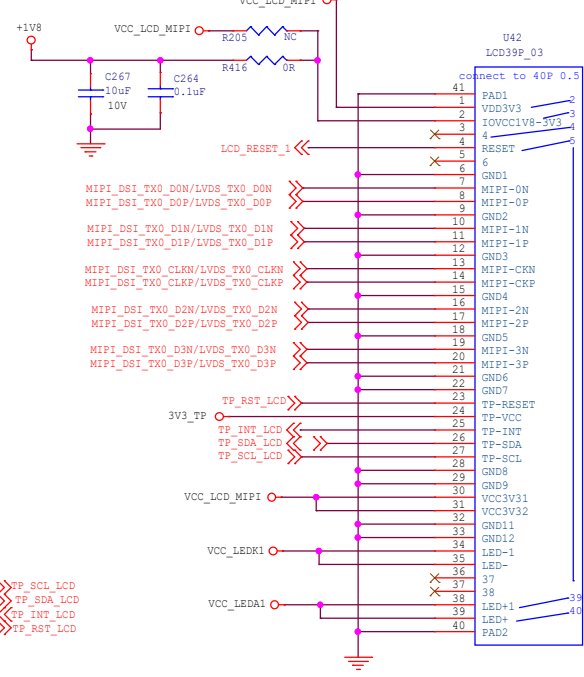
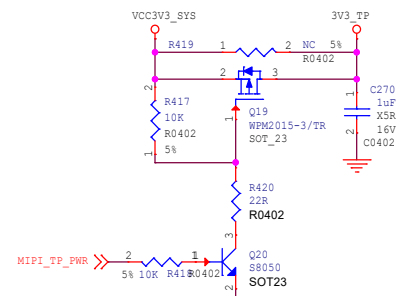
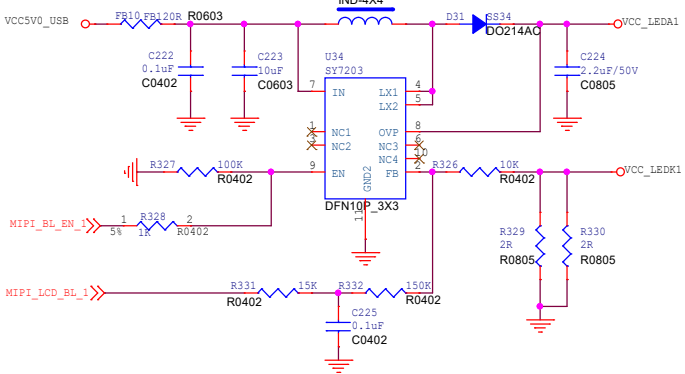


LCD_TP

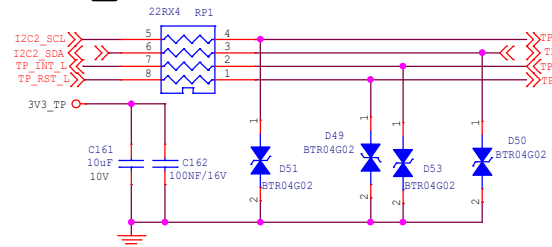


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LVDS_LCD_TX0

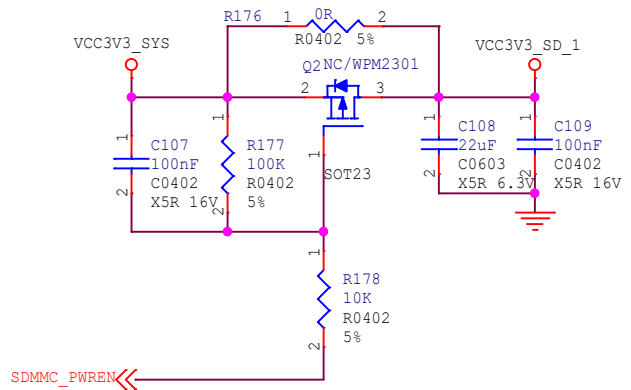
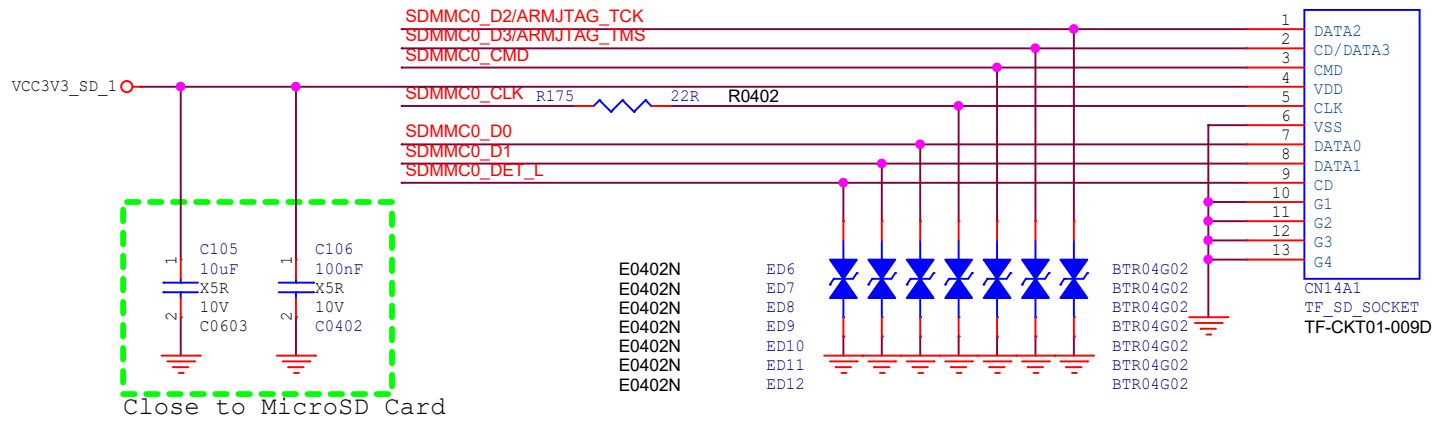
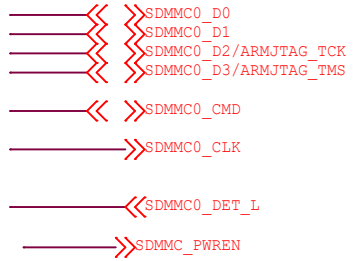


LCD_TP



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Date: Sunday, April 23, 2023	Sheet	15 of 27
	REV	V1.0

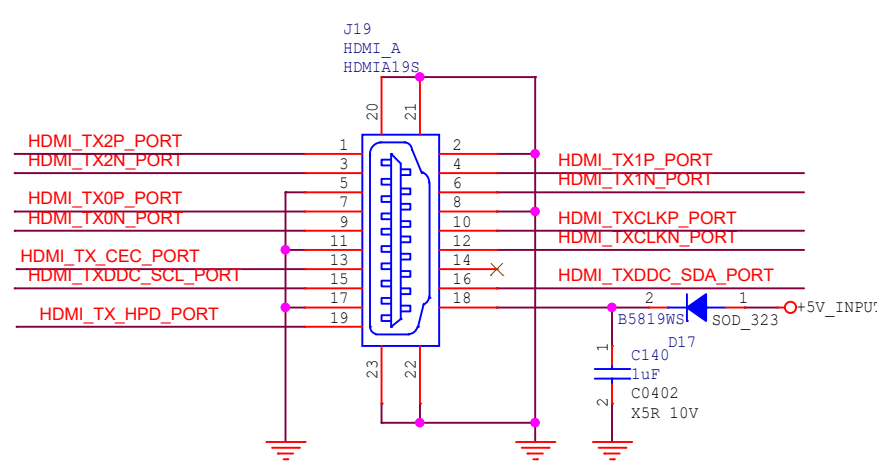
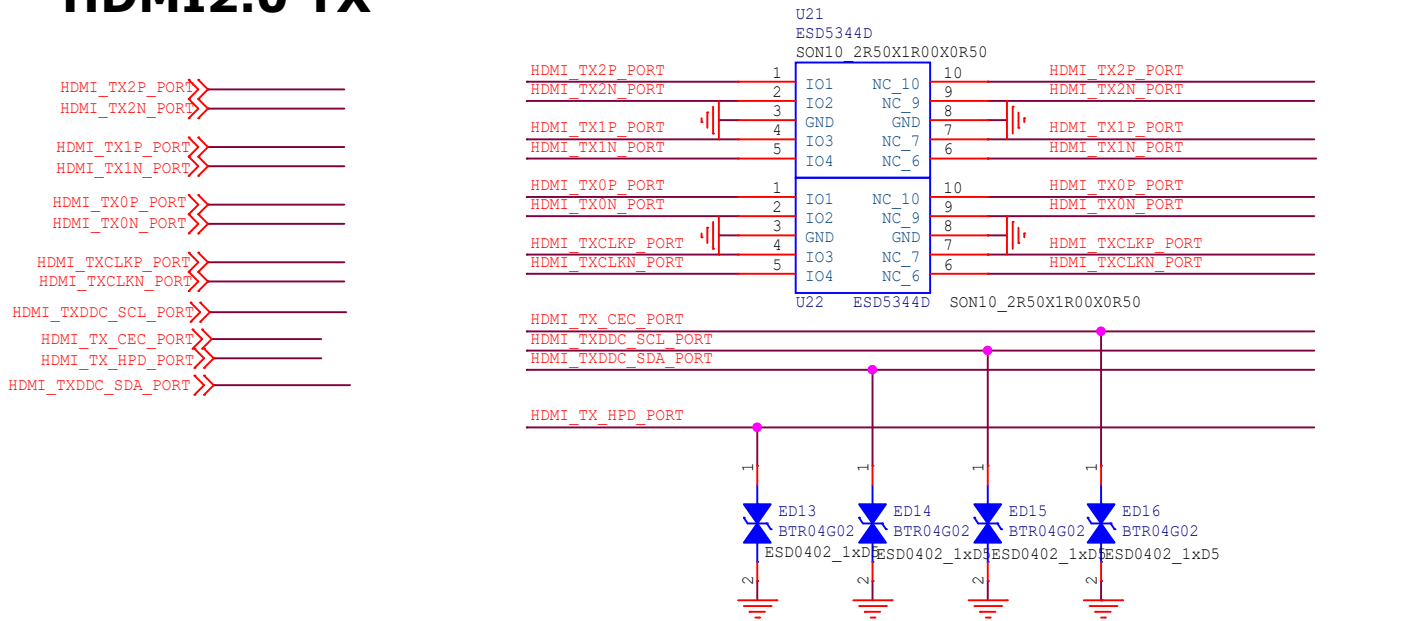
MicroSD Card



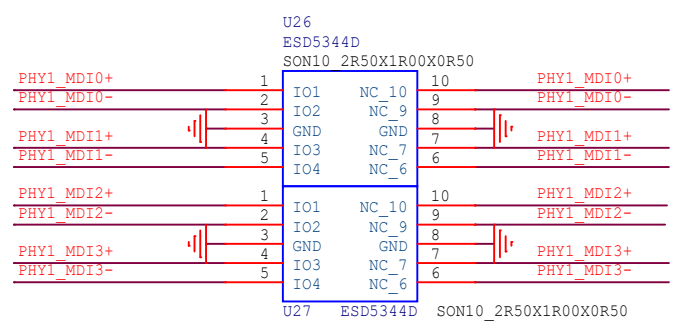
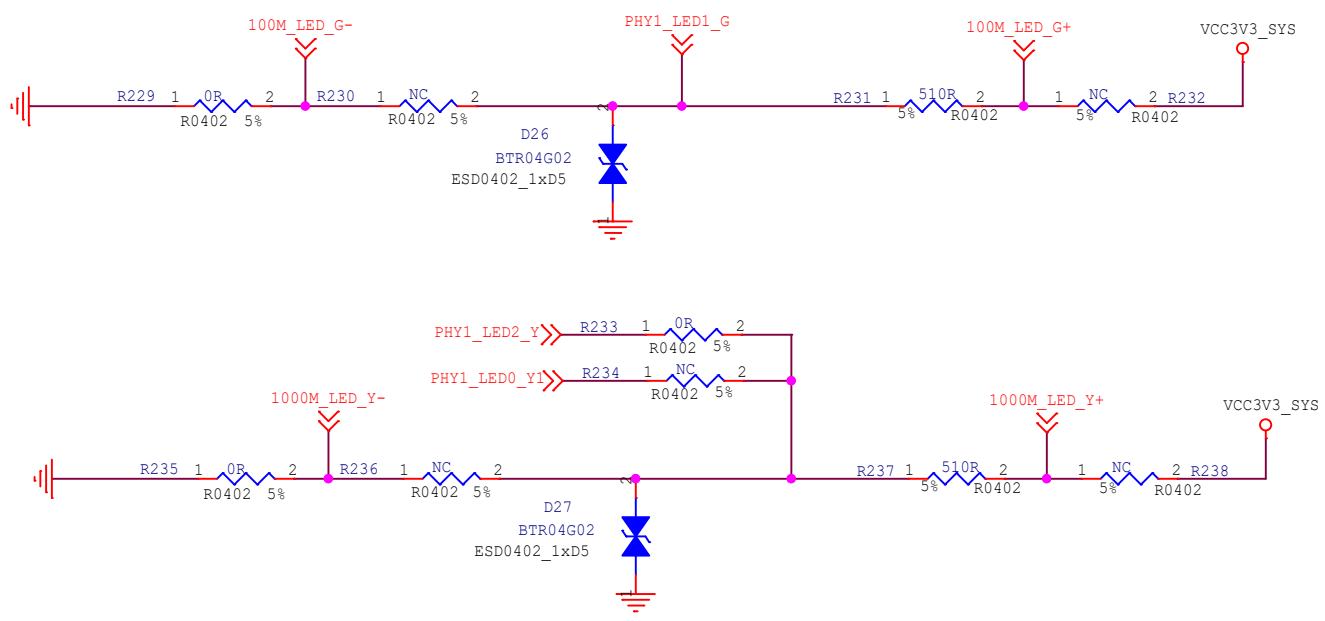
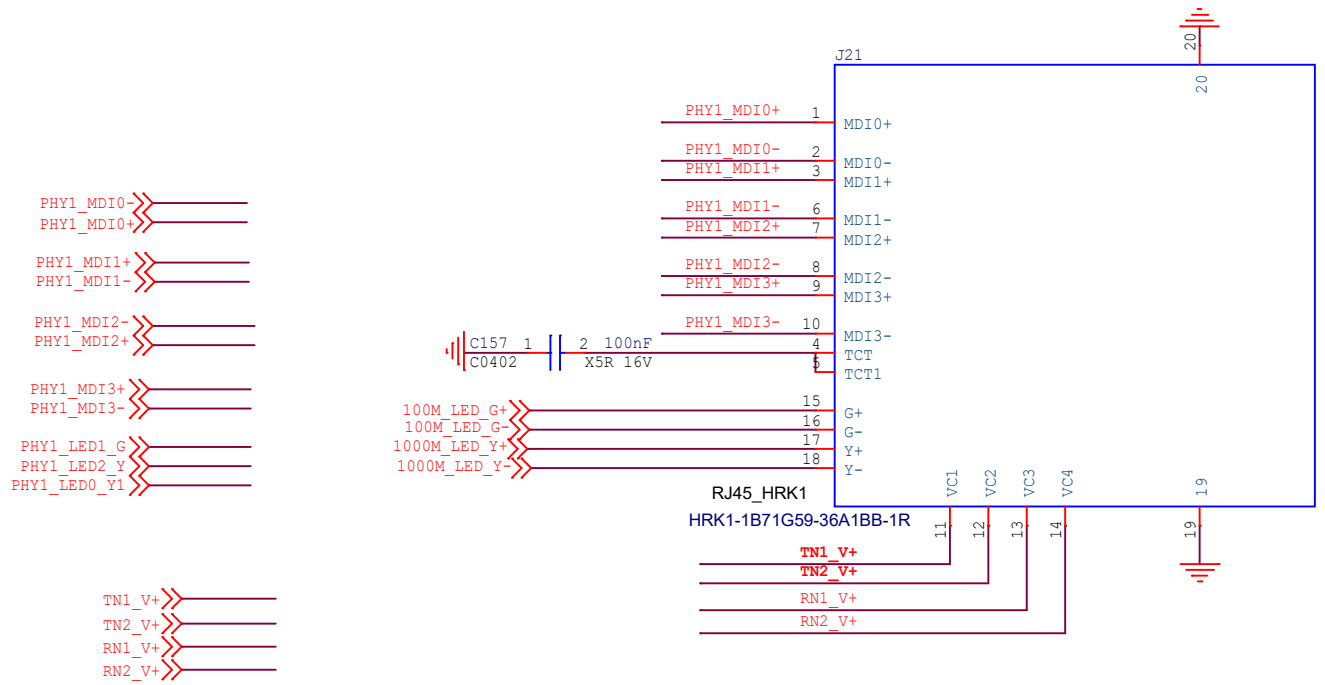
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Date: Sunday, April 23, 2023	Sheet 16 of 27	

HDMI2.0 TX

$C_j \leq 0.4 \text{ pF}$

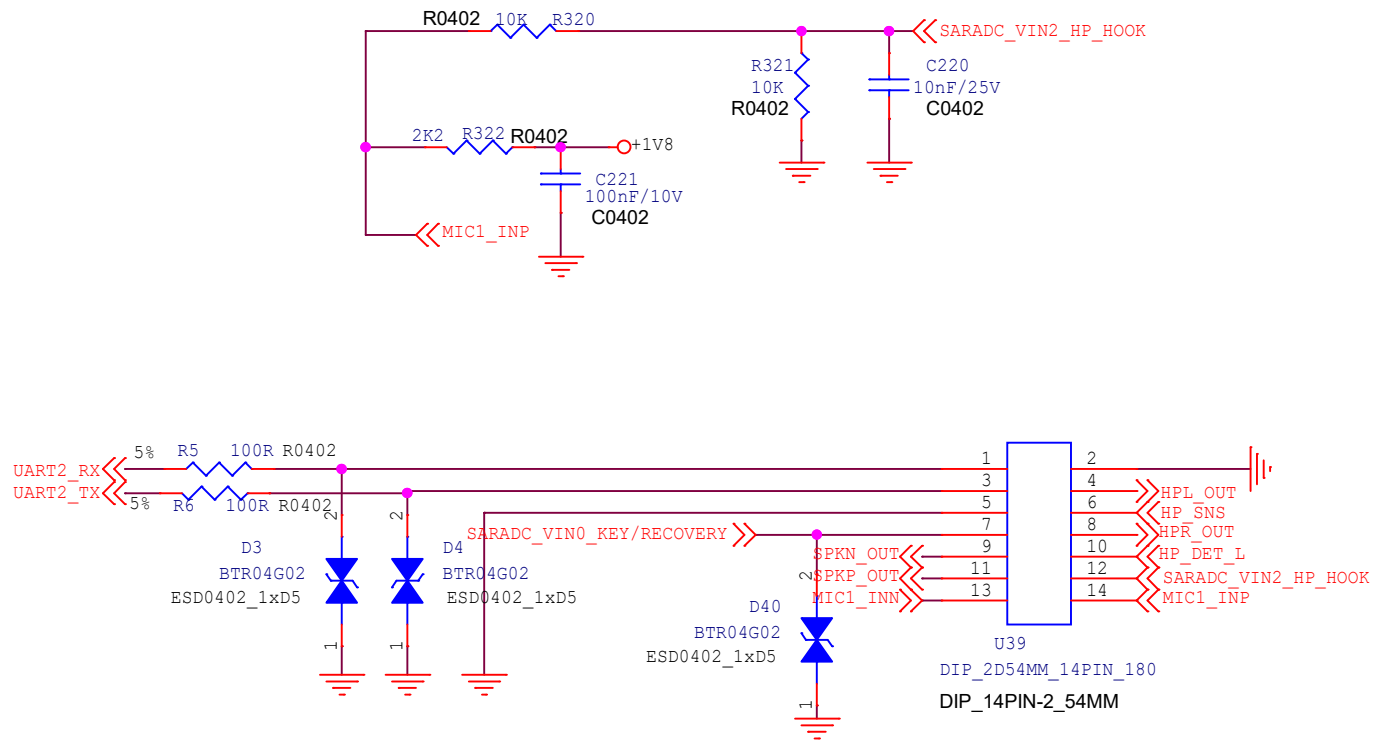


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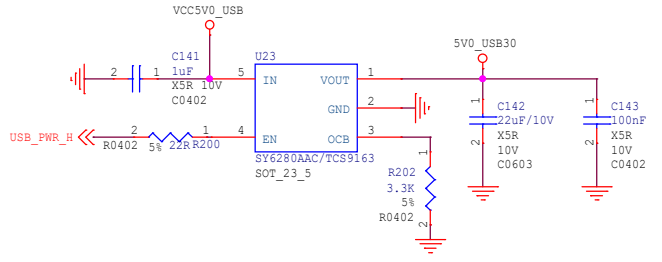
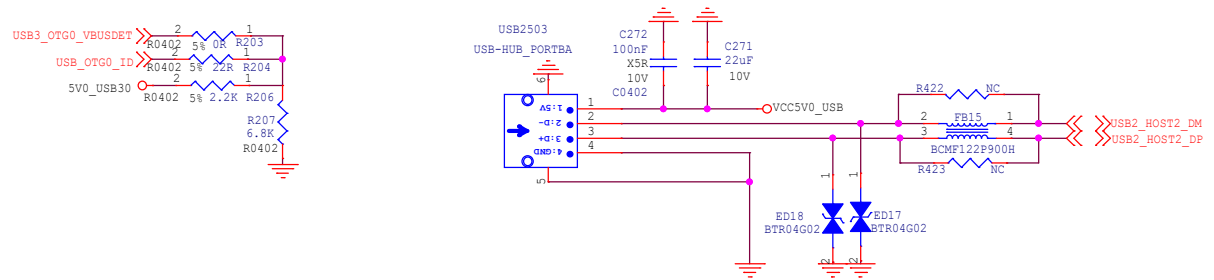
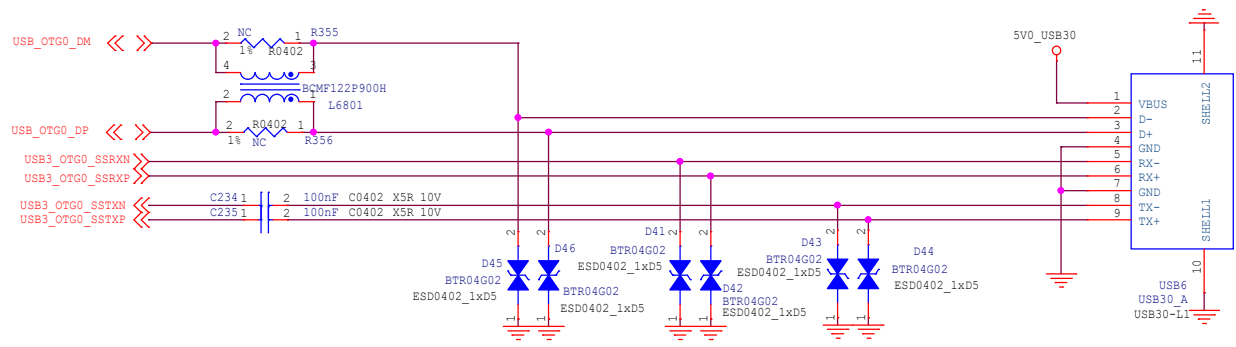


radxa

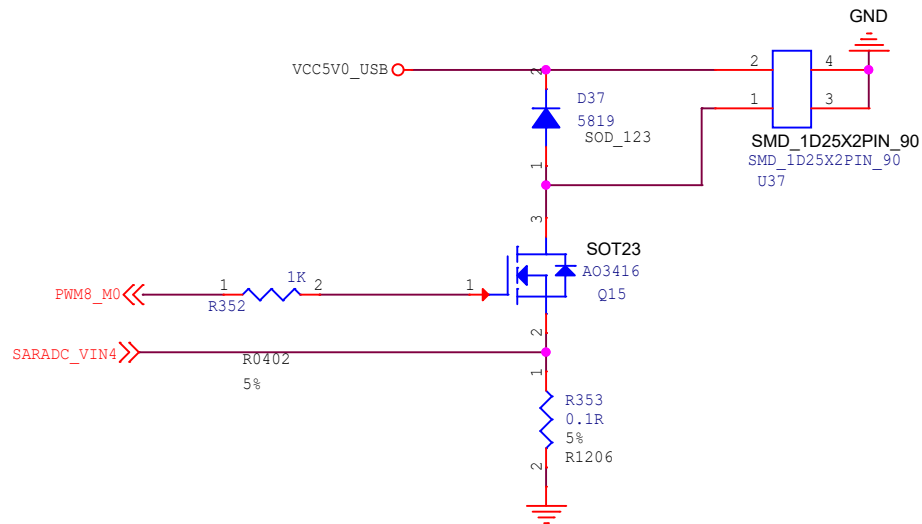
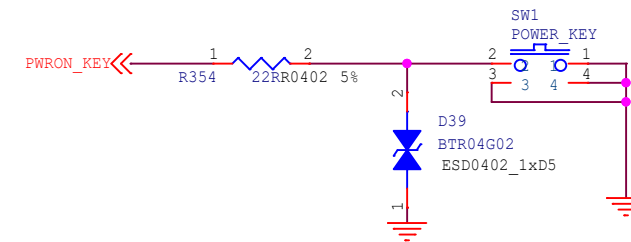
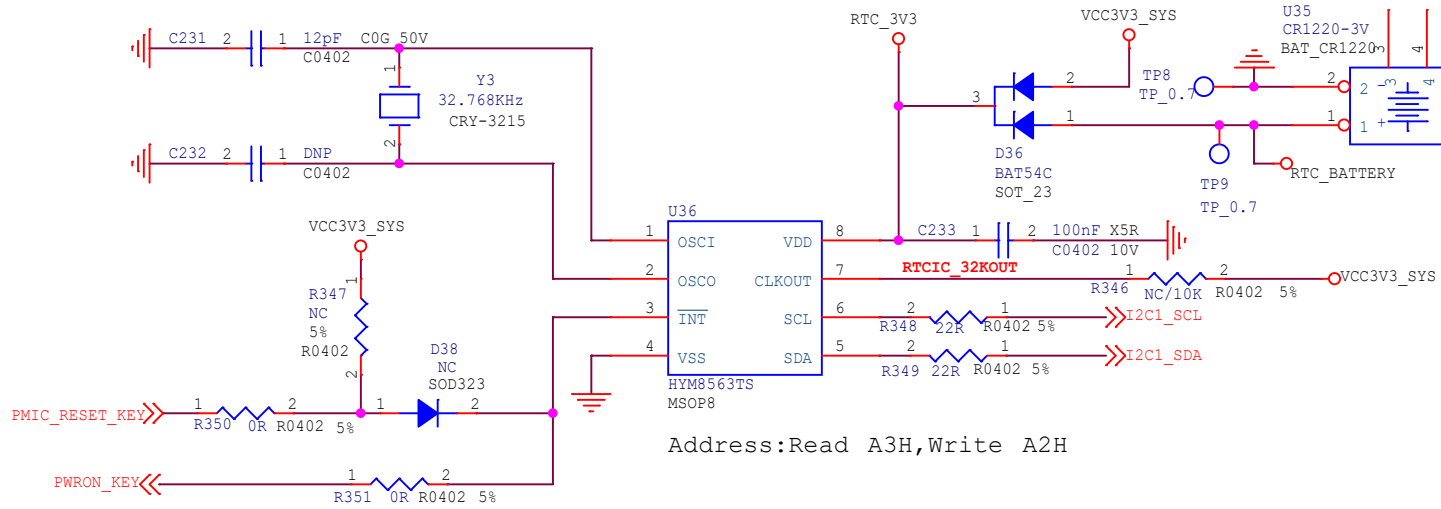
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A4	Page Name:	Ethernet_1000M	V1.0
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radxa		
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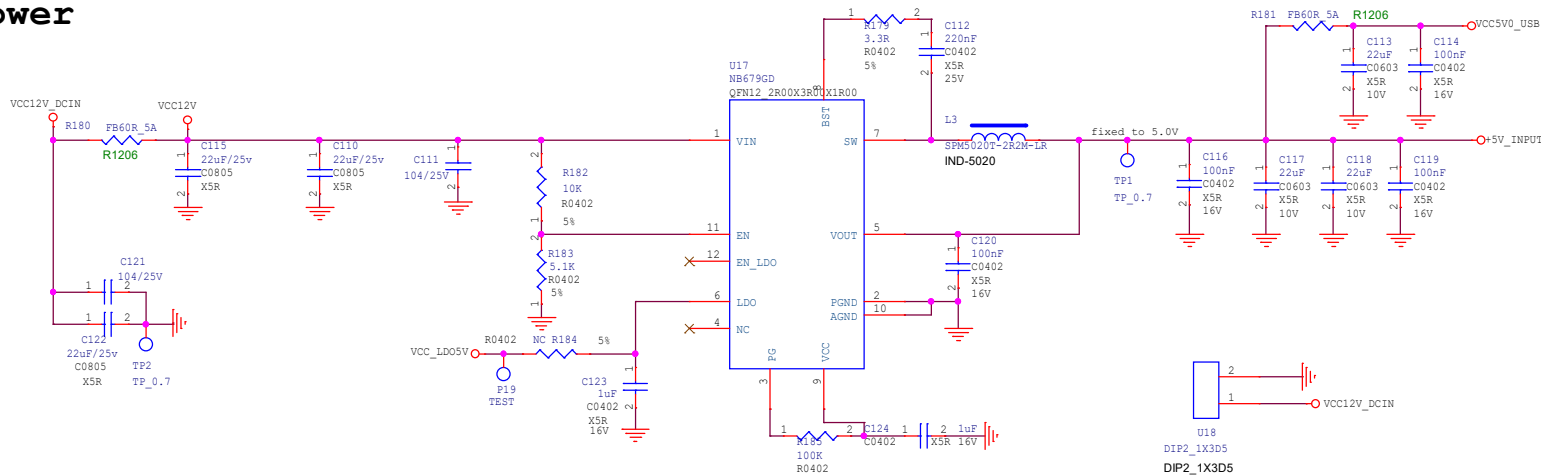


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Date:	Sunday, April 23, 2023	Sheet	20 of 27

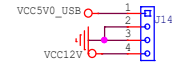
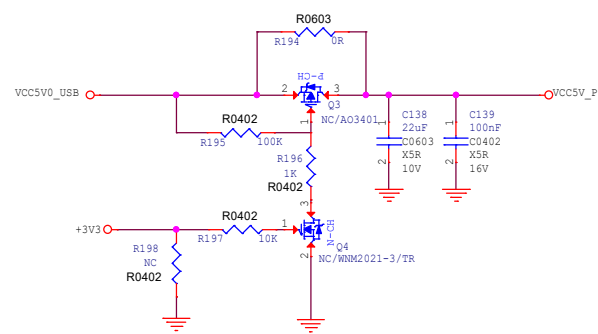
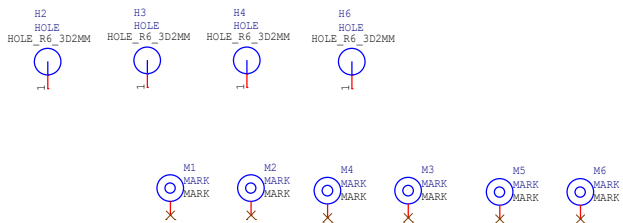
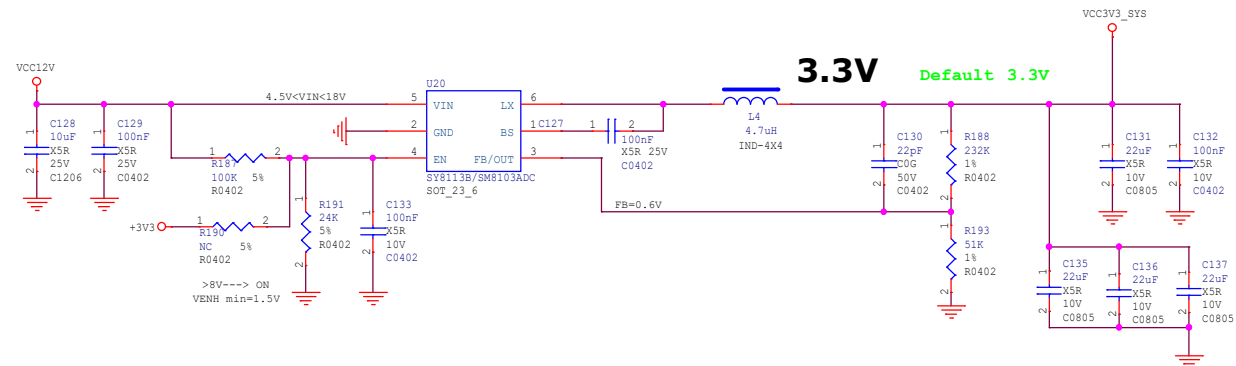
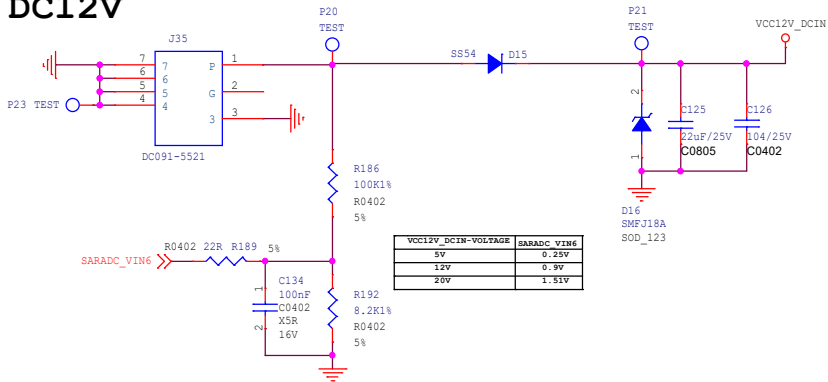


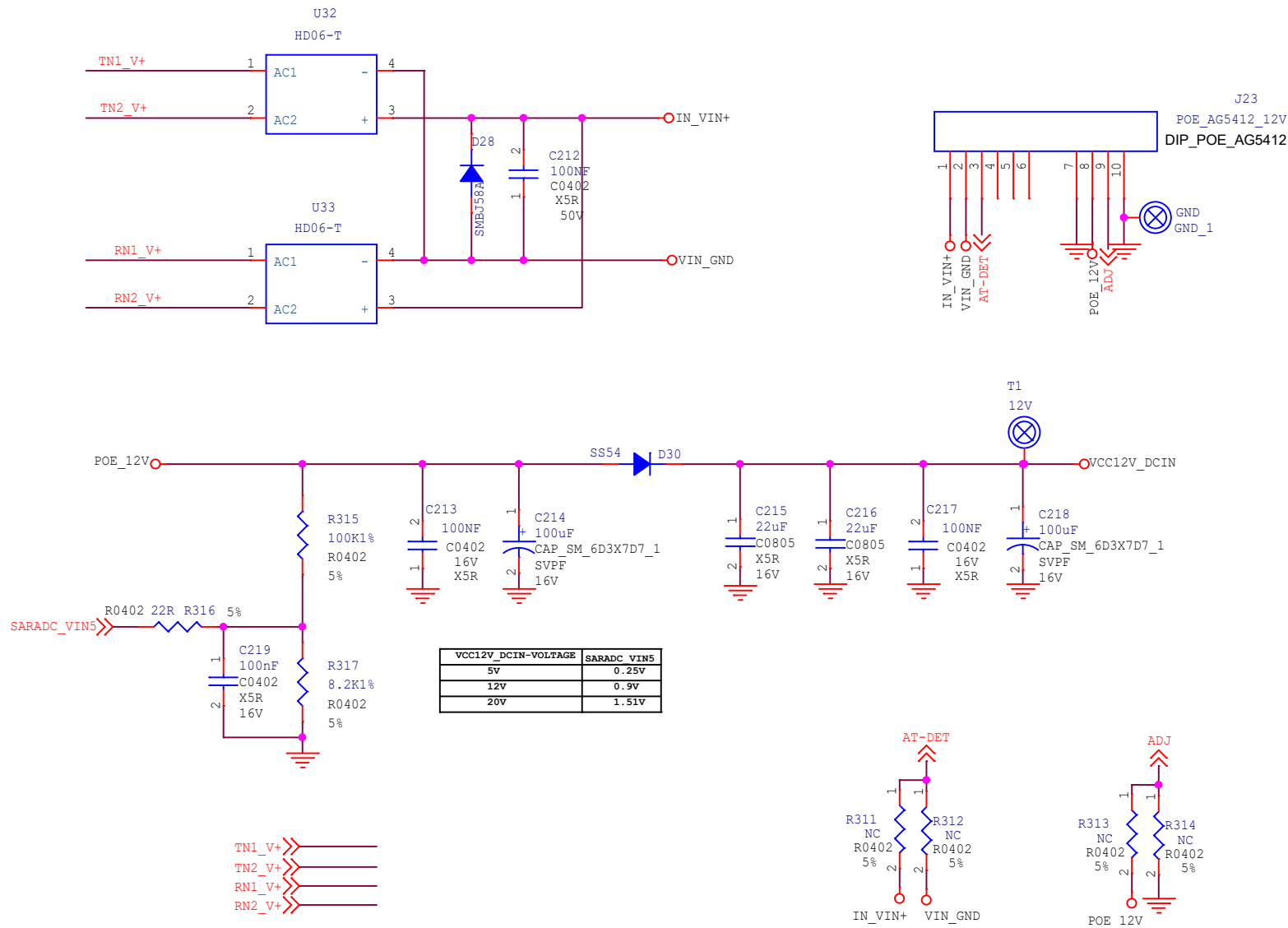
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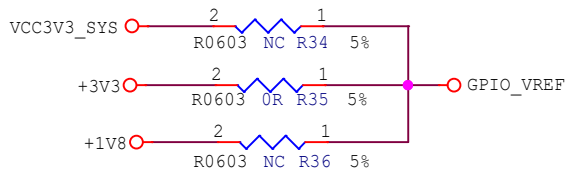
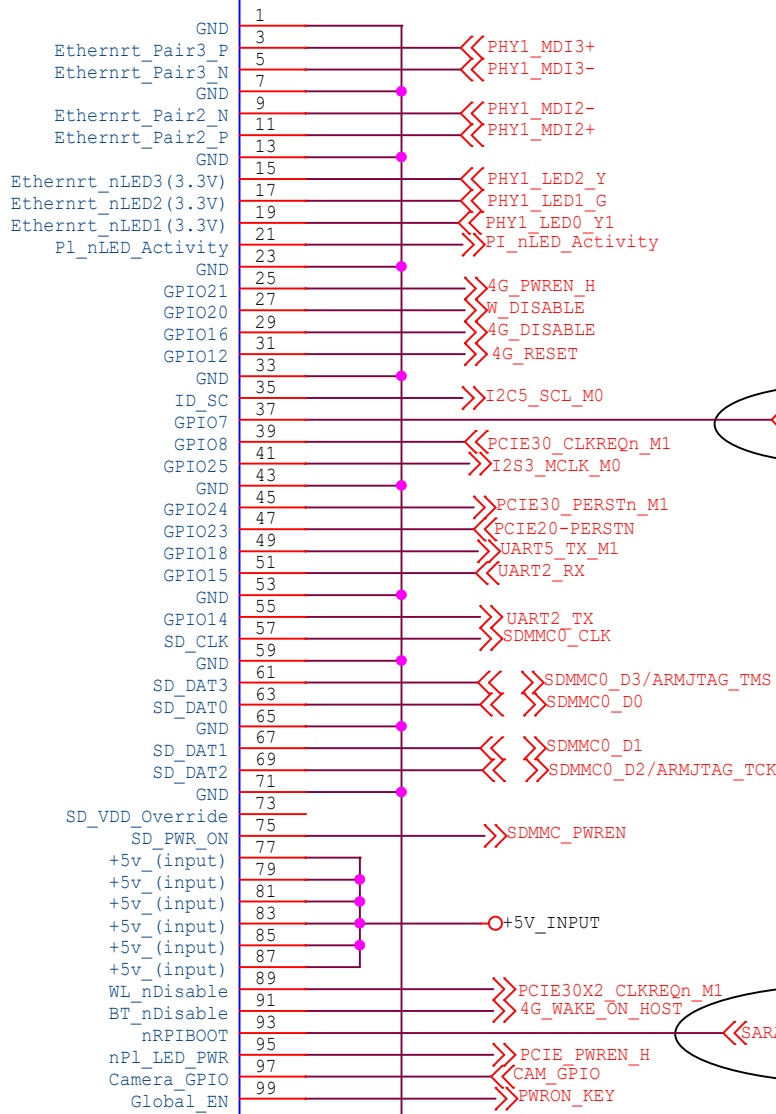
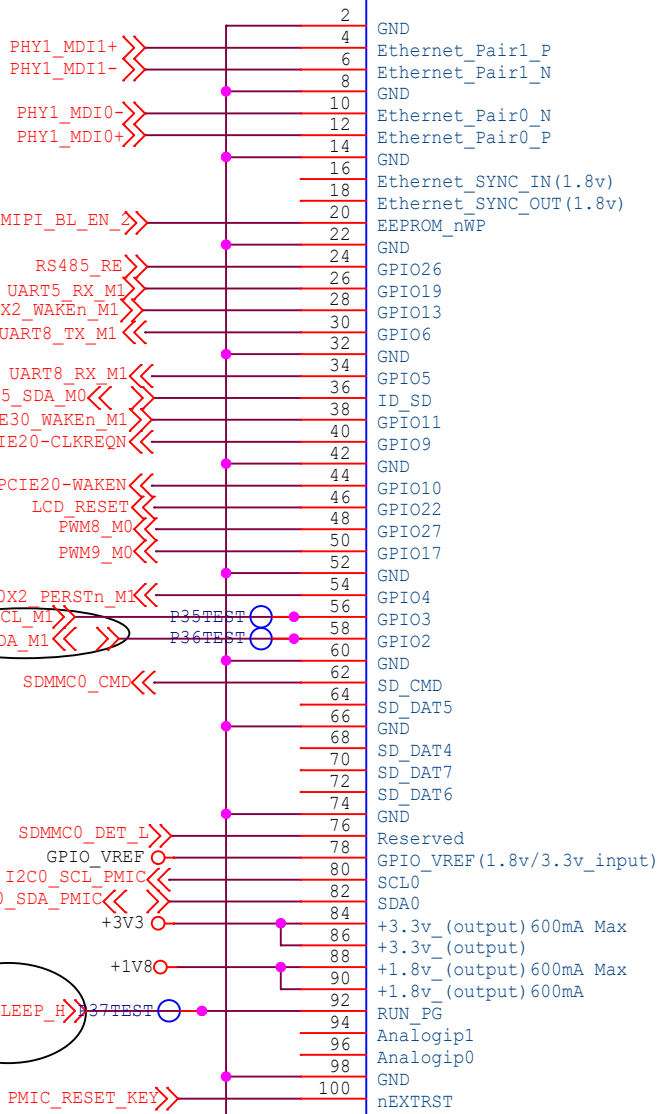
DC IN&SYSTEM Power



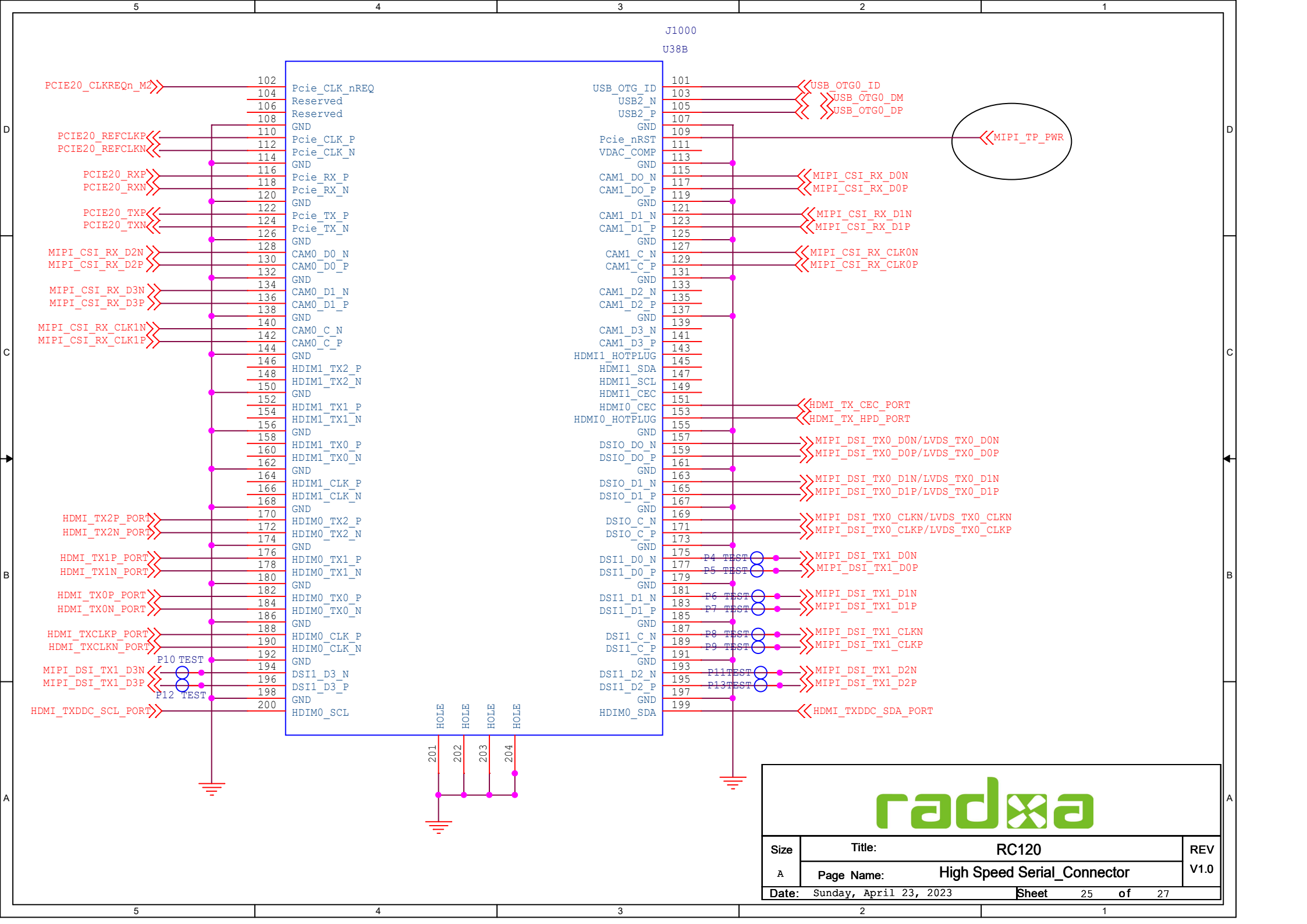
DC12V



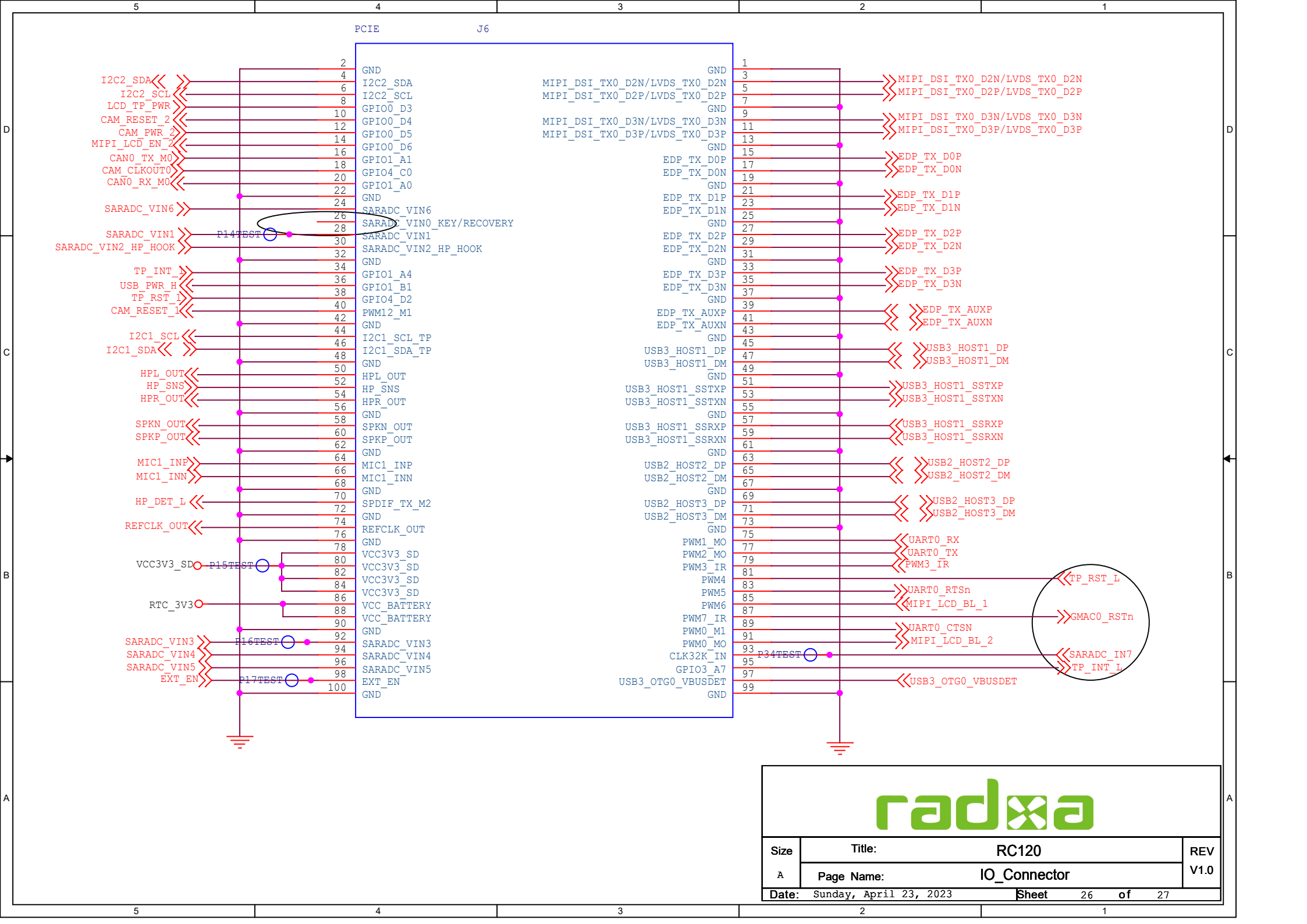




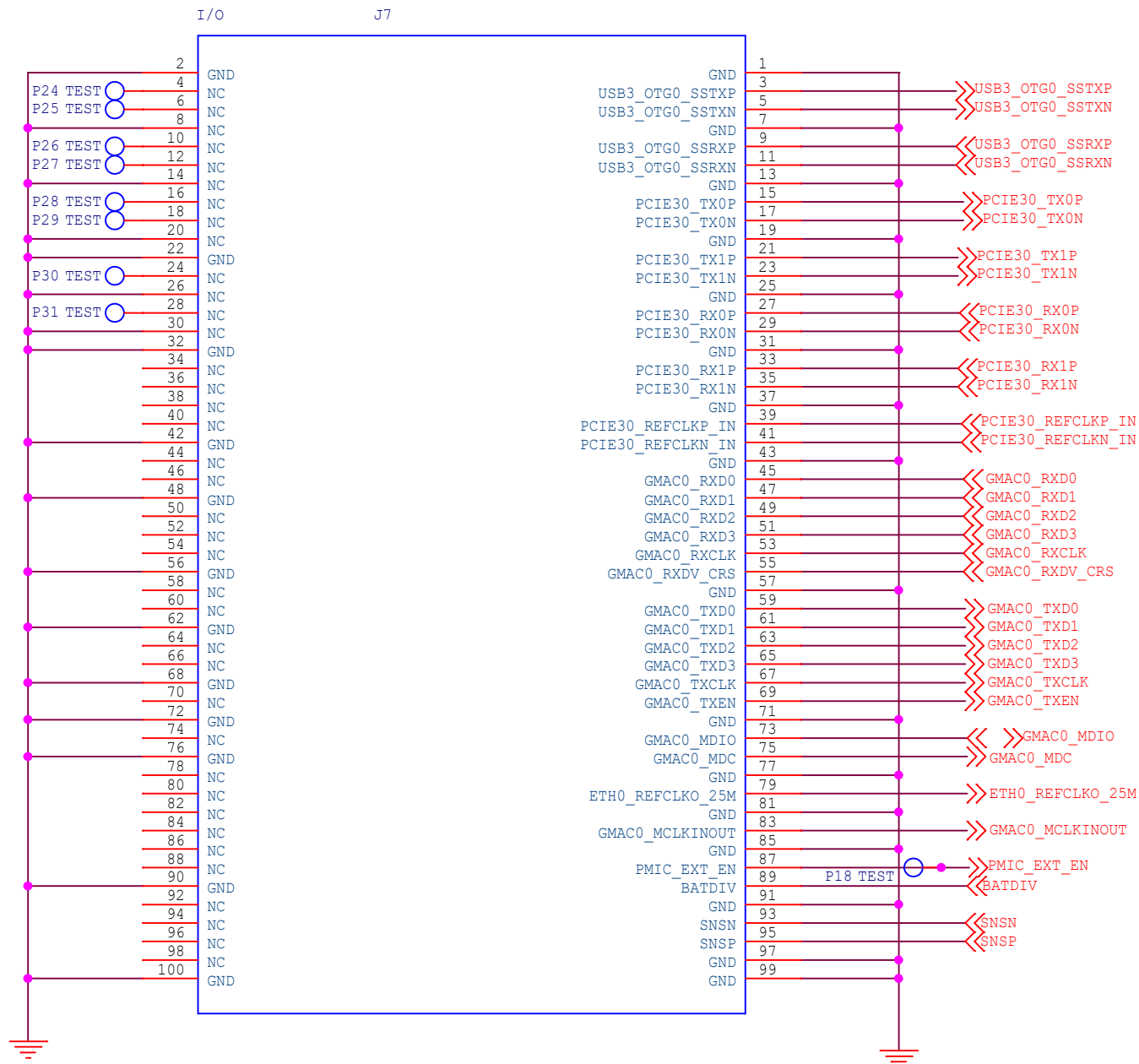
Size	Title: RC120	REV
A	Page Name: GPIO+VCC_Connector	V1.0
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Size	Title: RC120	REV
A	Page Name: High Speed Serial_Connector	V1.0
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Size	Title: RC120	REV
A	Page Name: IO_Connector	V1.0
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Size	Title: RC120	REV
A	Page Name: IO2_Connector	V1.0
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