



Radxa Cubie A7A

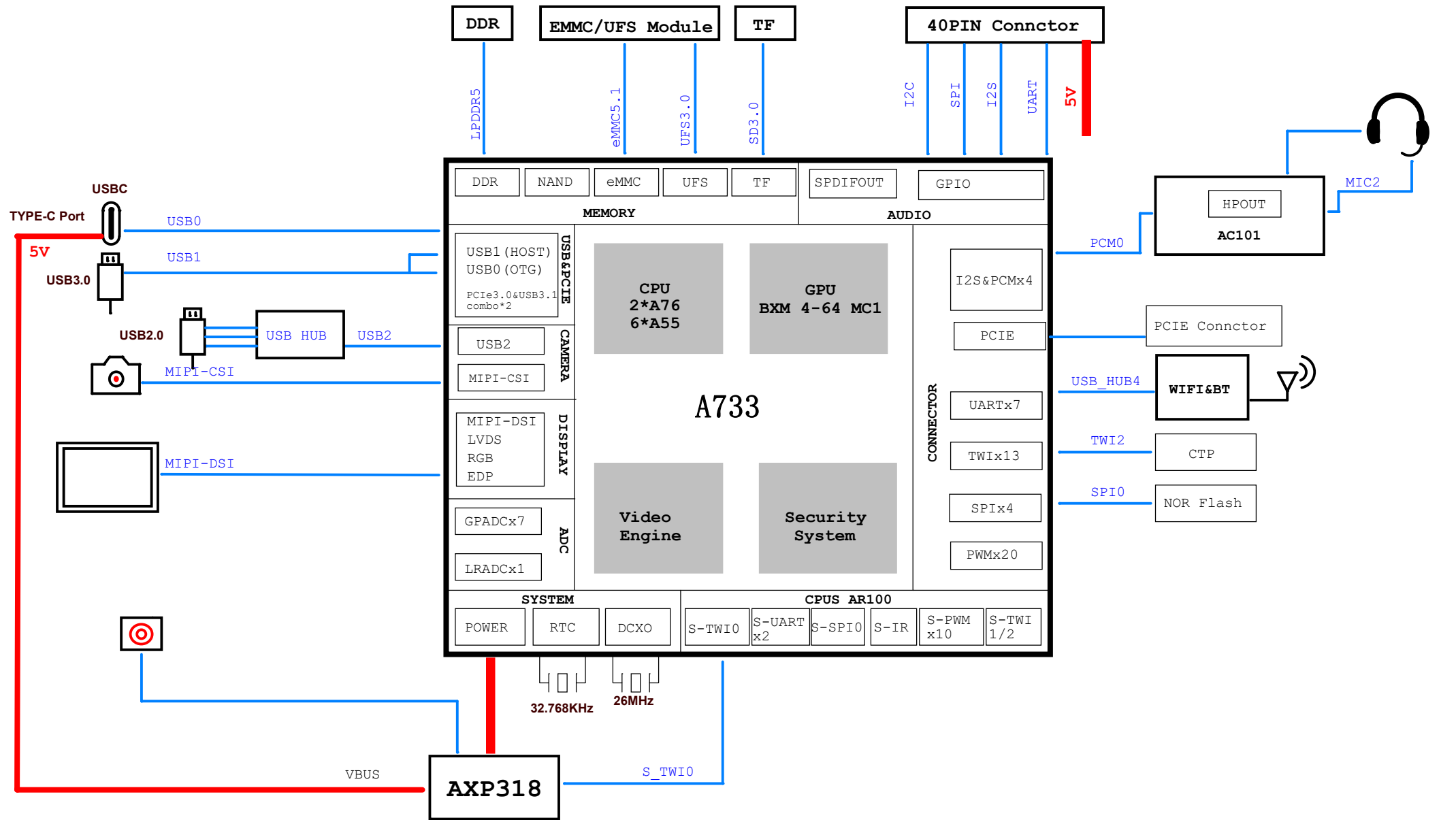
Schematic

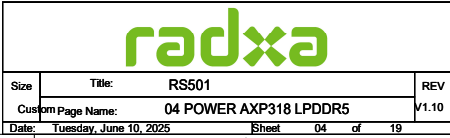
V1.10

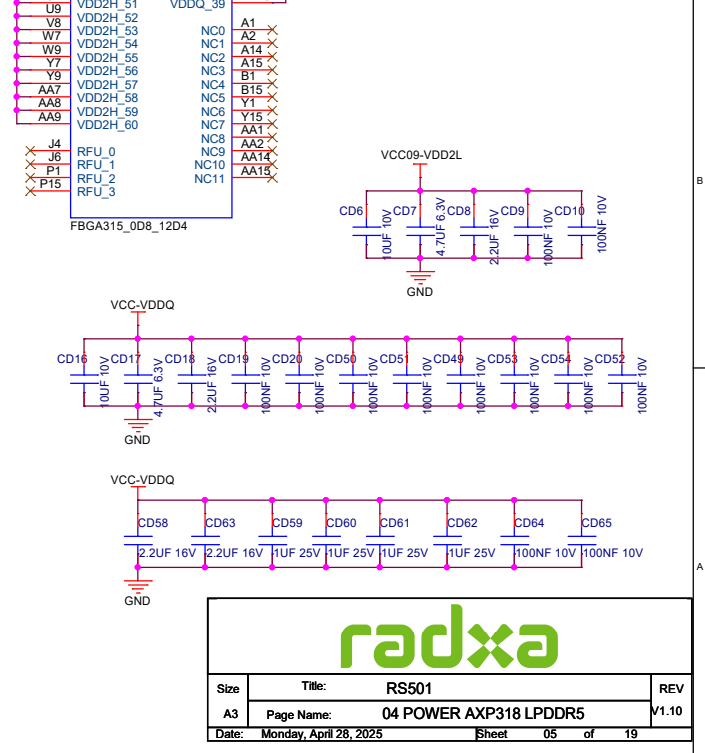
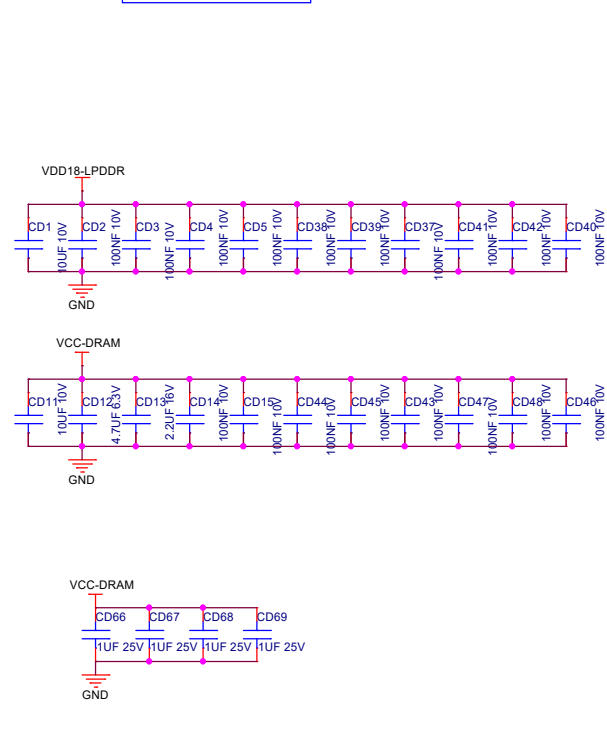
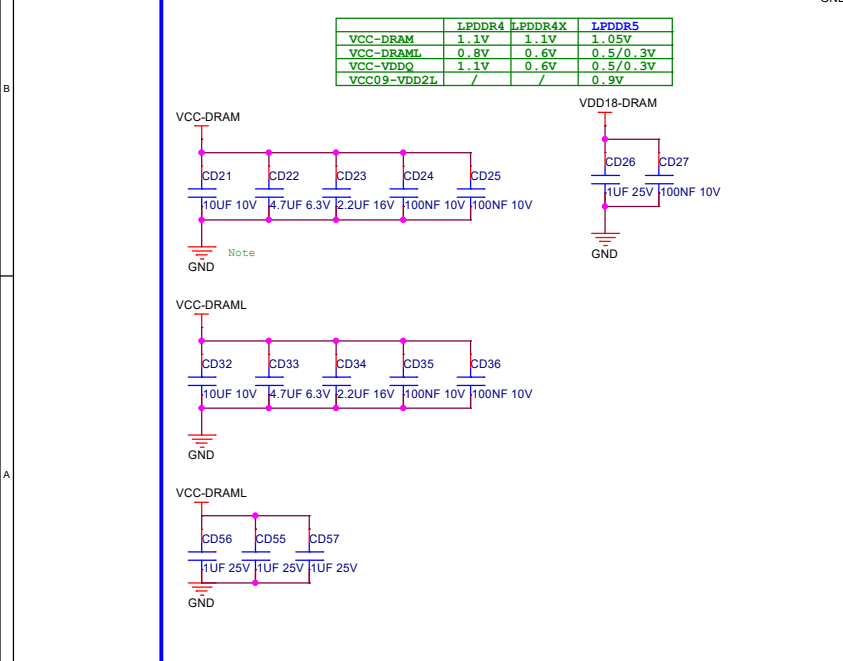
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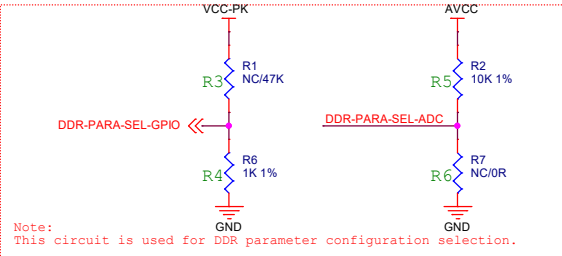
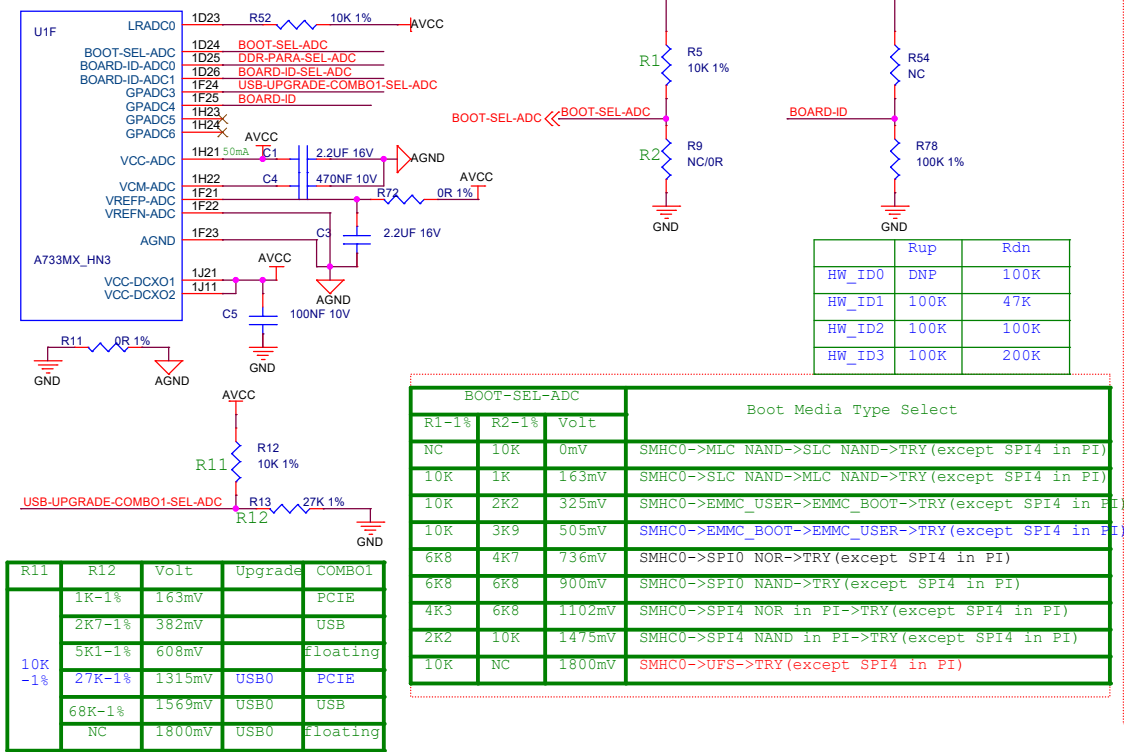
BLOCK





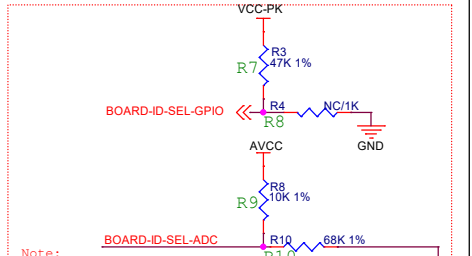


ADC



Note:
This circuit is used for DDR parameter configuration selection.

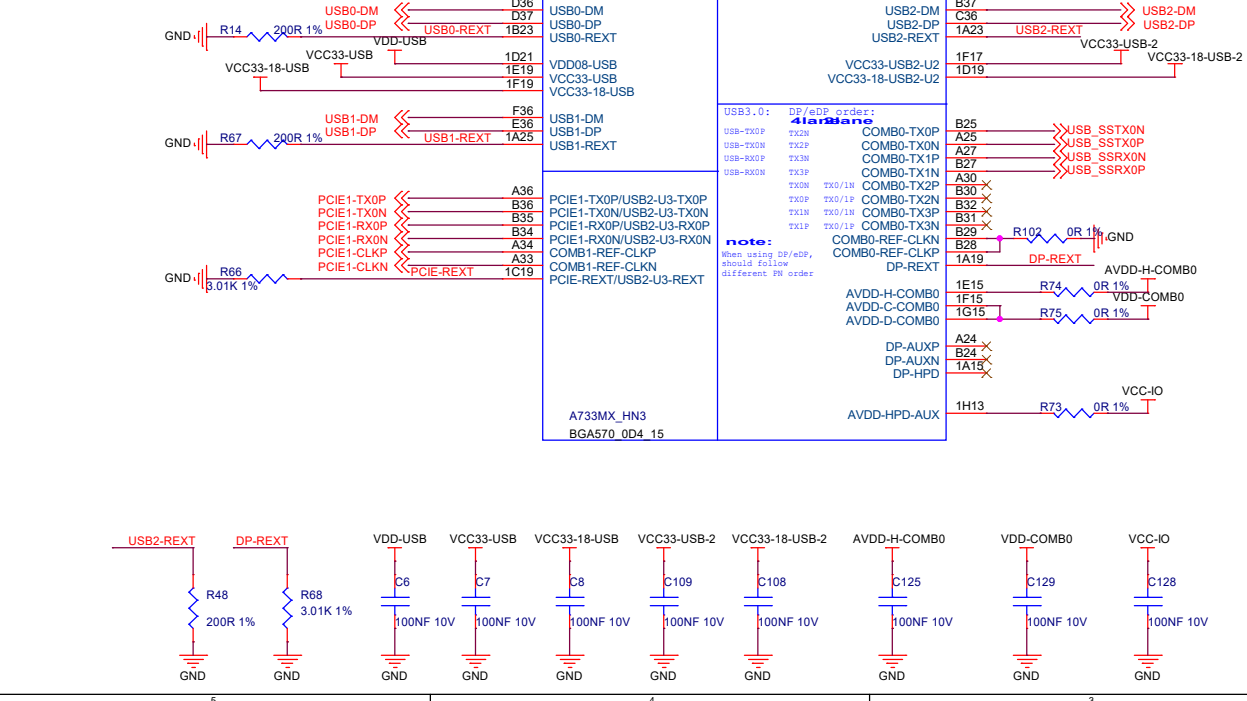
GPIO Level(Set by the R3 pull-up and R4 pull-down resistance of PH16 GPIO)	GPADC Voltage(Fixed pull-up R5 is 10K-1%,Set the voltage by adjusting pull-down resistance of PH16 GPIO)	DDR PARA
0	163mV (1K-1%)	DDR PARA 1
0	382mV (2.7K-1%)	DDR PARA 2
0	608mV (5.1K-1%)	DDR PARA 3
0	811mV (8.2K-1%)	DDR PARA 4
0	1050mV (14K-1%)	DDR PARA 5
0	1315mV (27K-1%)	DDR PARA 6
0	1569mV (68K-1%)	DDR PARA 7
0	1800mV (NC)	DDR PARA 8
1	163mV (1K-1%)	DDR PARA 9
1	382mV (2.7K-1%)	DDR PARA 10
1	608mV (5.1K-1%)	DDR PARA 11
1	811mV (8.2K-1%)	DDR PARA 12
1	1050mV (14K-1%)	DDR PARA 13
1	1315mV (27K-1%)	DDR PARA 14
1	1569mV (68K-1%)	DDR PARA 15
1	1800mV (NC)	DDR PARA 16



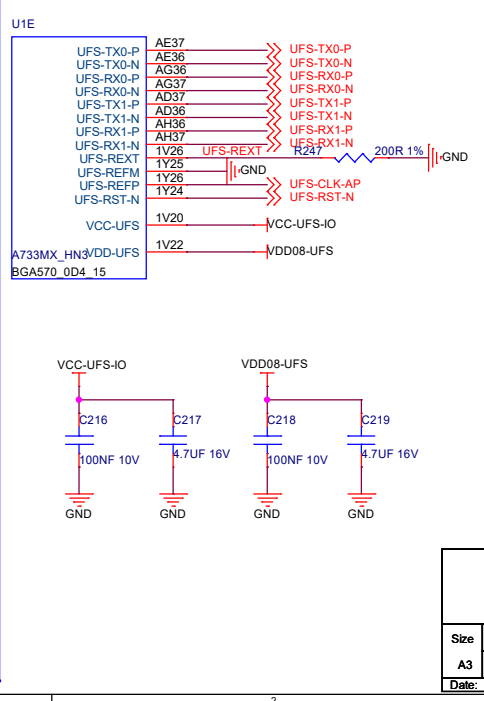
Note:
This circuit is used for board template selection.

GPIO-SET		BOARD-ID-SEL-ADC			BoardID Config
R7	R8	R9	R10	Volt	
NC	1K pull down	10K ~1%	1K-1%	163mV	Config-1
			2K7-1%	382mV	Config-2
			5K1-1%	608mV	Config-3
			8K2-1%	811mV	Config-4
			14K-1%	1050mV	Config-5
			27K-1%	1315mV	Config-6
			68K-1%	1569mV	Config-7
			NC	1800mV	Config-8
47K pull up	NC	10K ~1%	1K-1%	163mV	Config-9
			2K7-1%	382mV	Config-10
			5K1-1%	608mV	Config-11
			8K2-1%	811mV	Config-12
			14K-1%	1050mV	Config-13
			27K-1%	1315mV	Config-14
			68K-1%	1569mV	Config-15
			NC	1800mV	Config-16

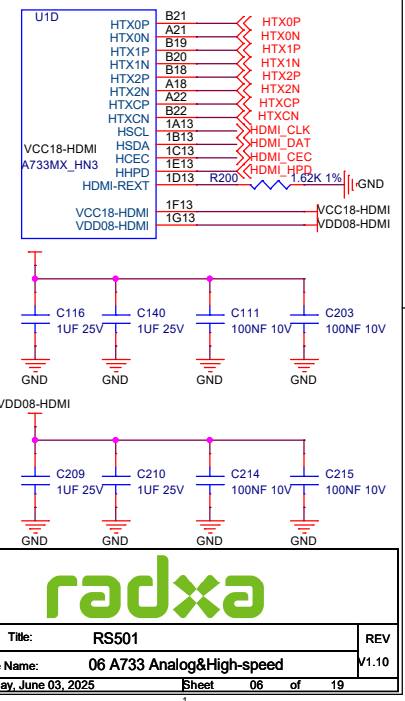
USB&EDP&PCIE



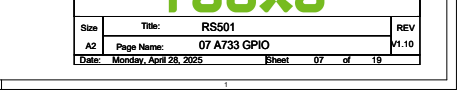
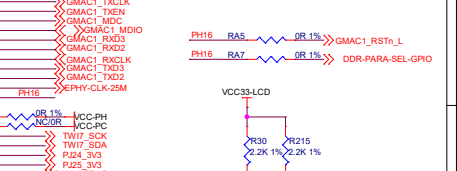
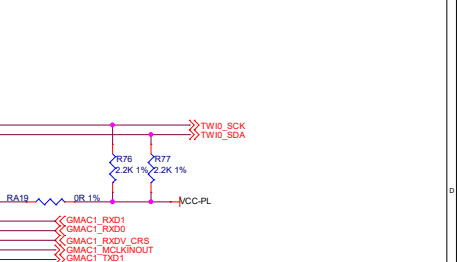
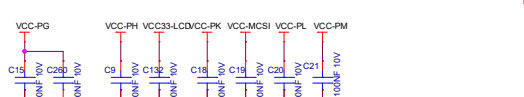
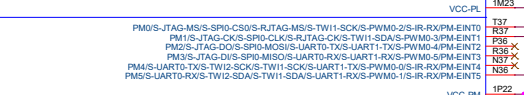
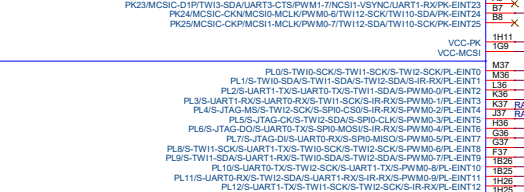
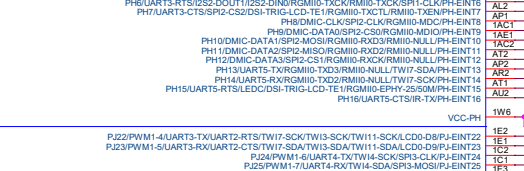
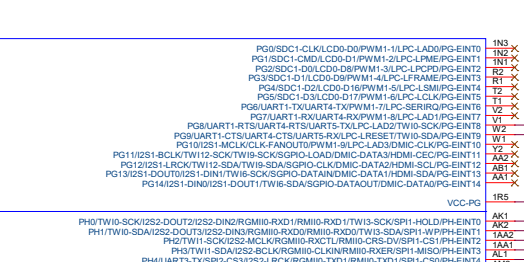
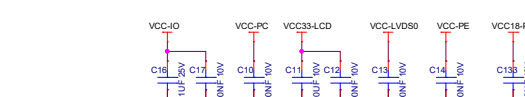
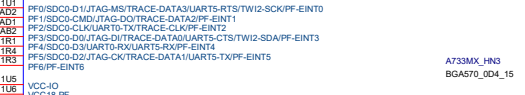
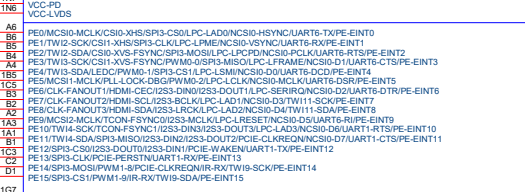
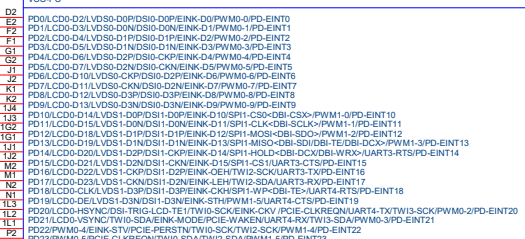
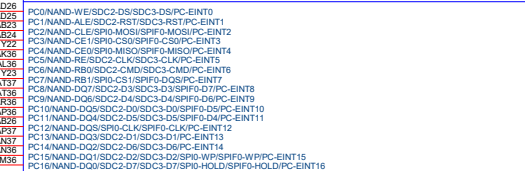
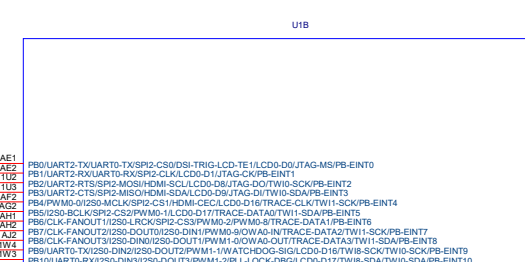
UFS



HDMI

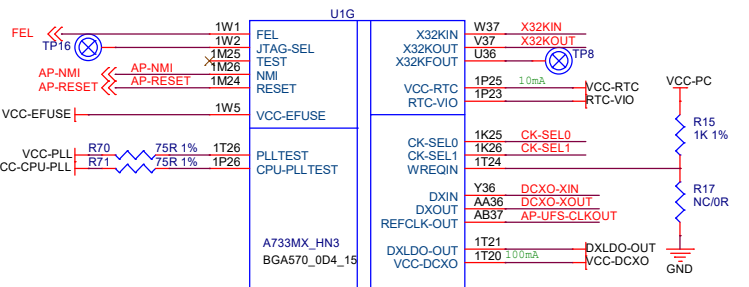


GPIO

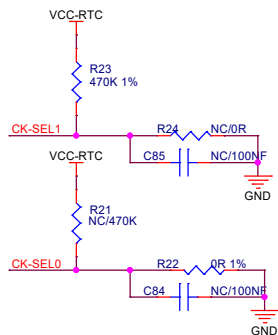
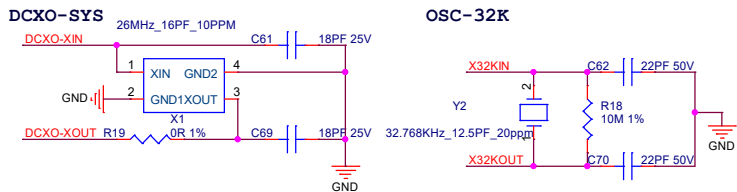
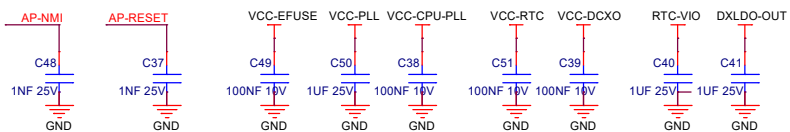


radxa

SYS

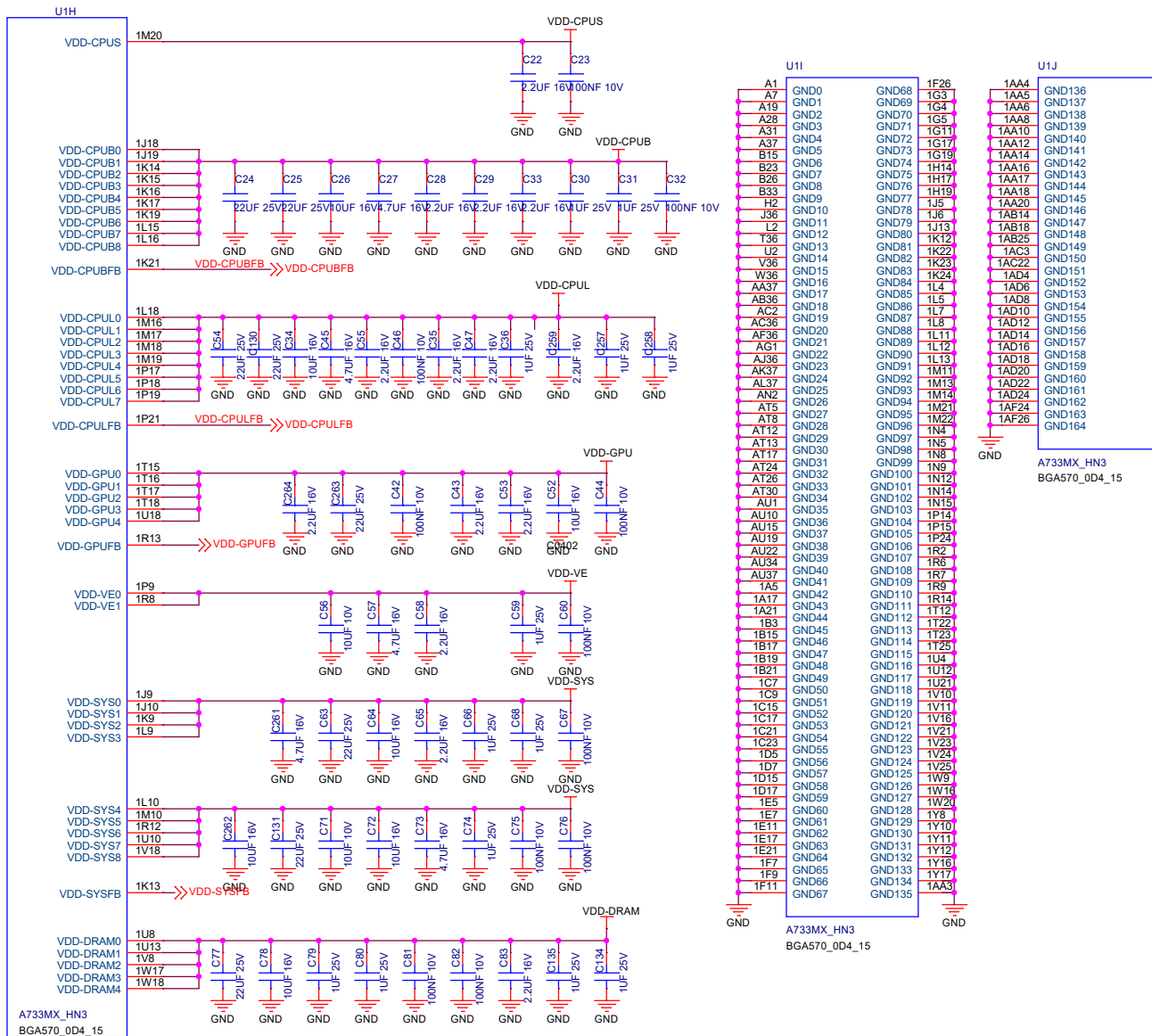


AP-UFS-CLKOUT >> AP-UFS-CLKOUT



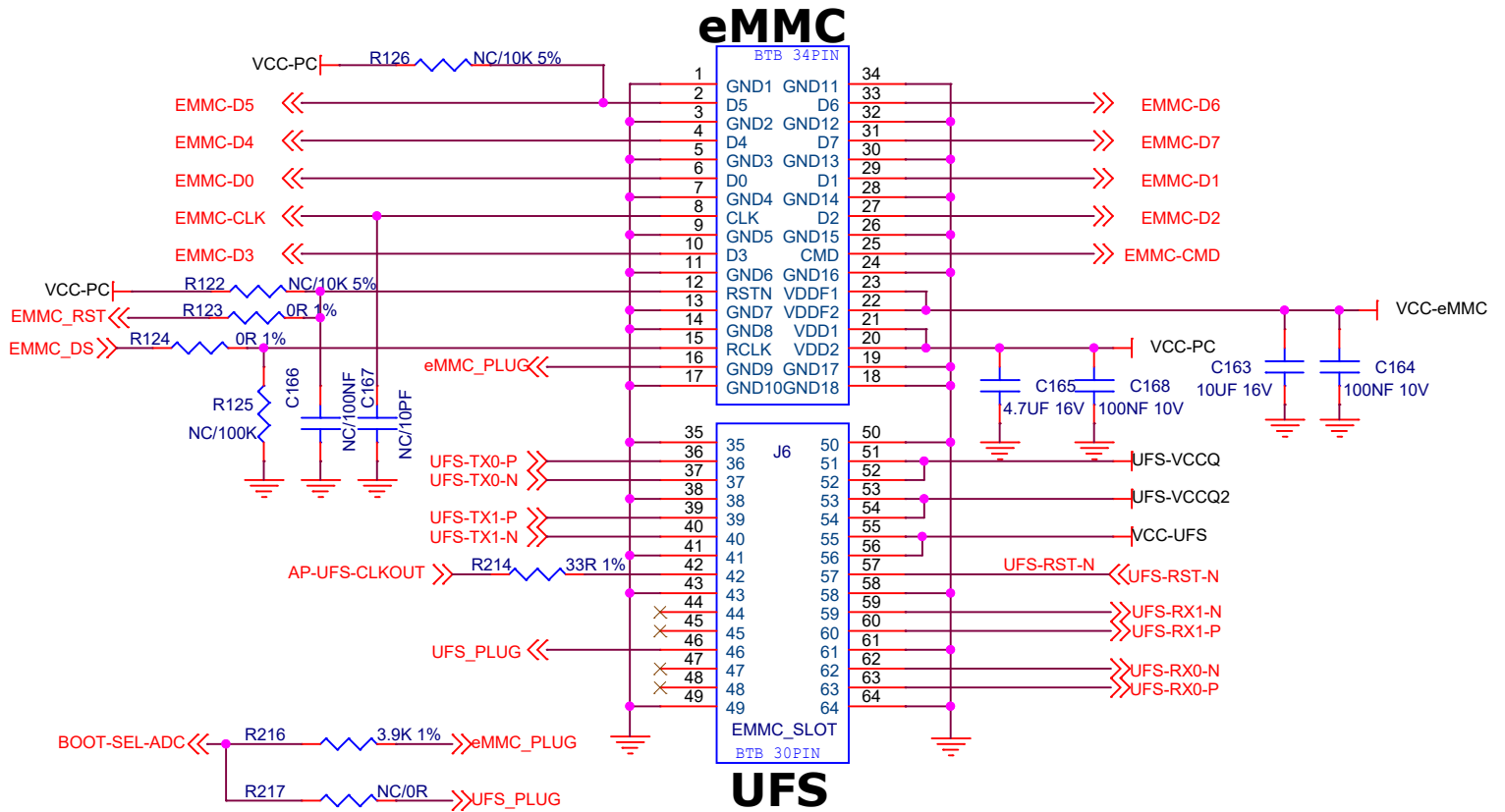
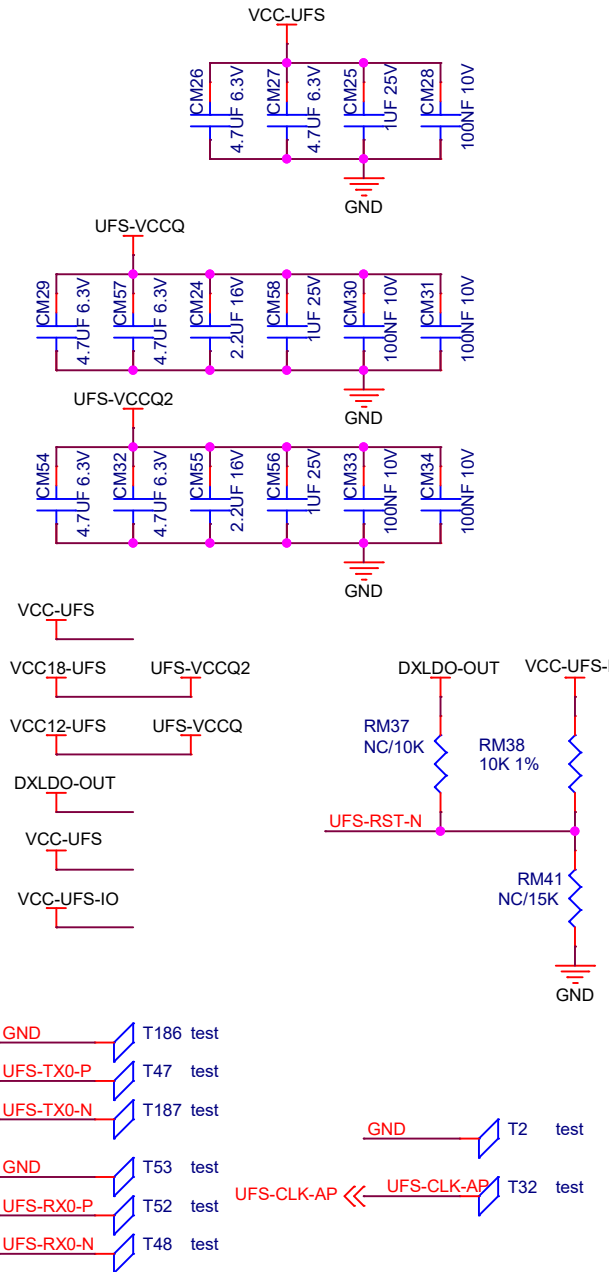
CLK-SEL		
default 24M	SEL1	SEL0
SOC=19.2M	0	1
SOC=26M	1	0
SOC=24M, UFS=19.2M	1	1
SOC=24M, UFS=26M	0	0

CORE POWER

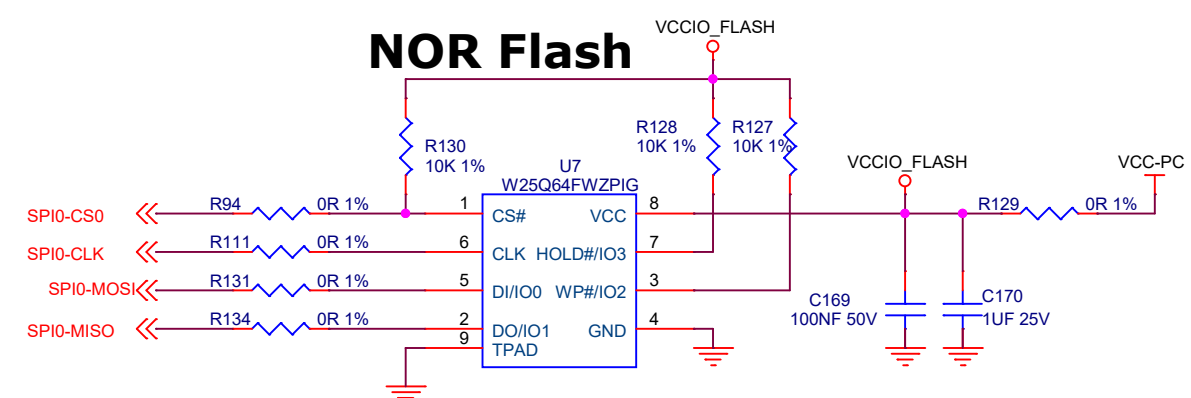


Size:	Title: RS501	REV
A3	Page Name: 08 A733 SYS	V1.10
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eMMC/UFS/NOR FLASH

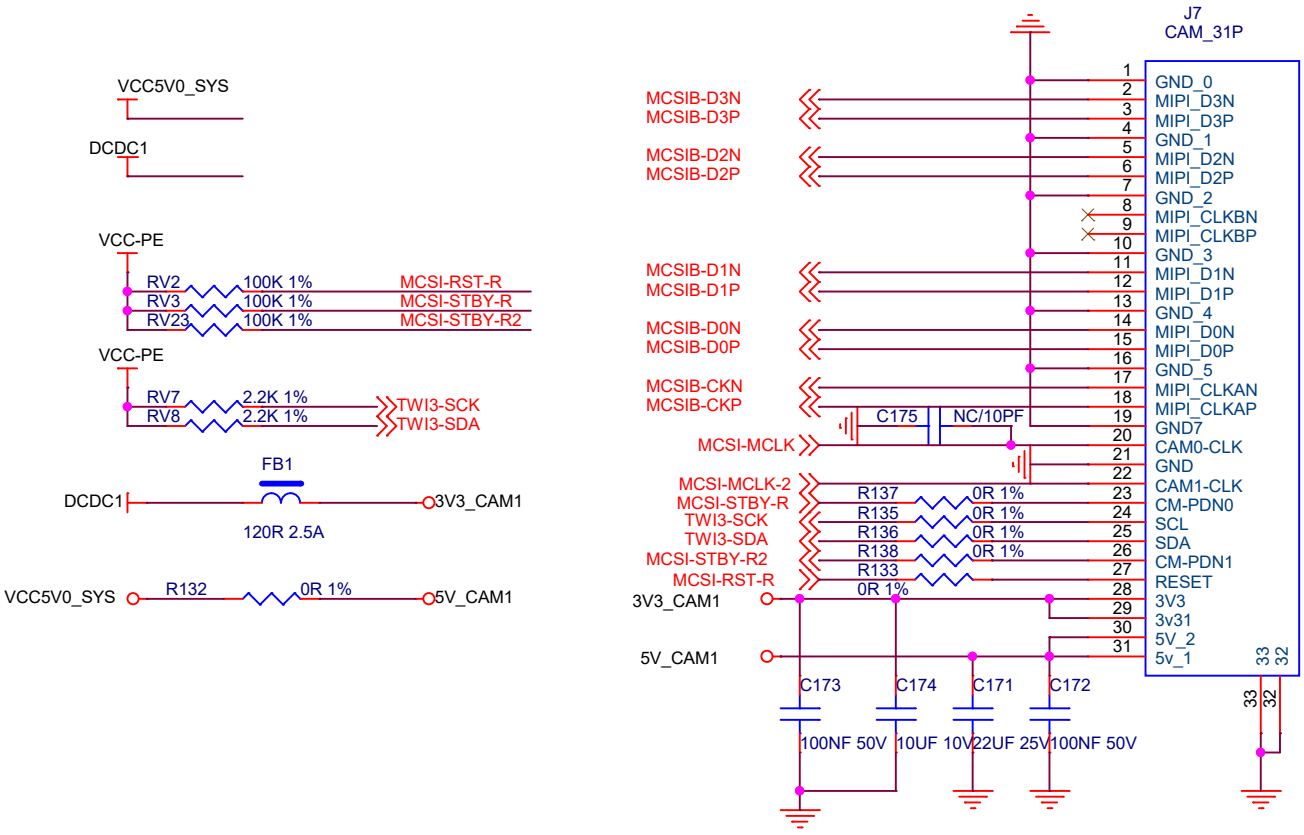


NOR Flash

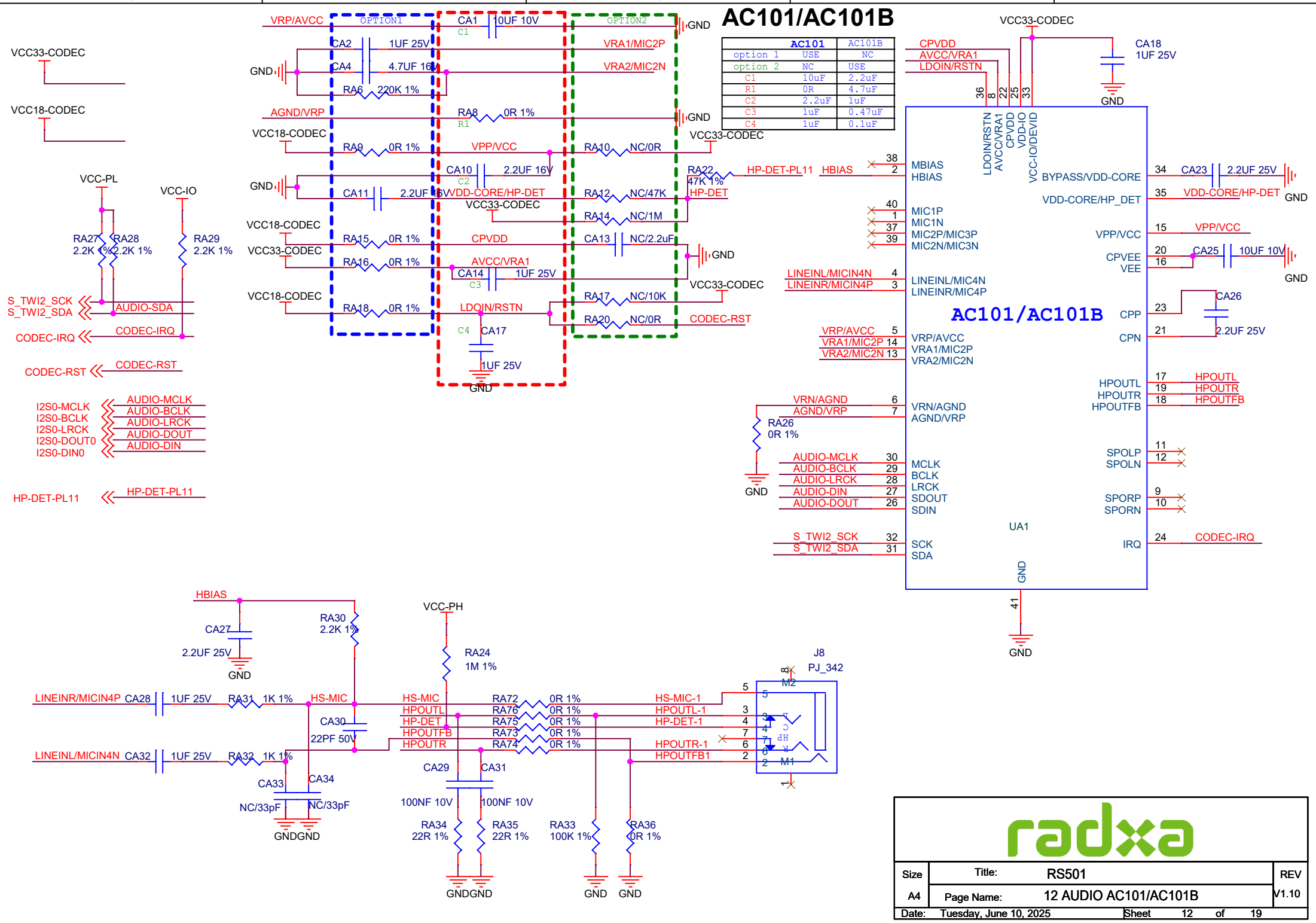


Size	Title: RS501	REV
A4	Page Name: 09 EMMC_UFS_NOR	V1.10
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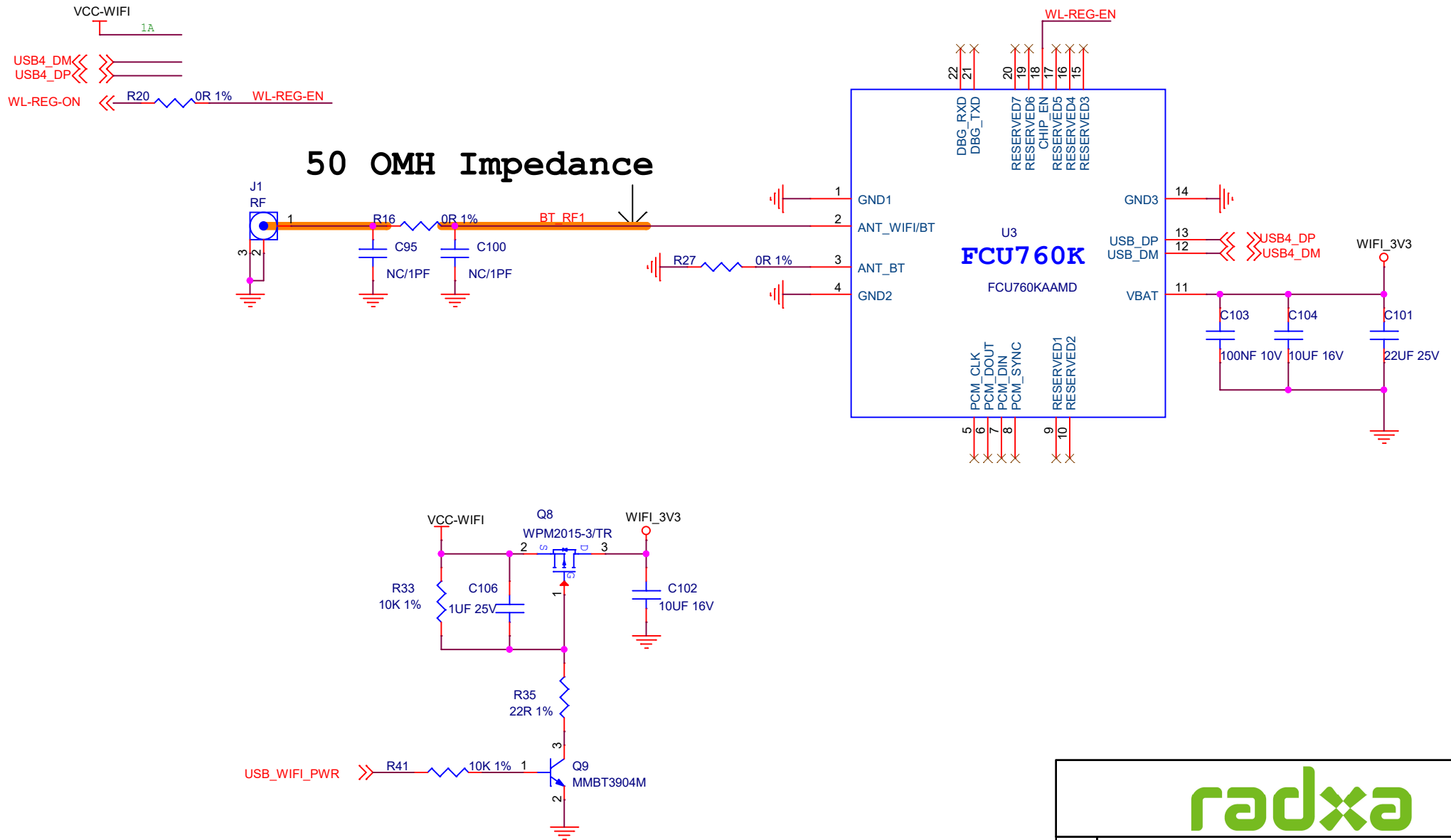
CAMERA



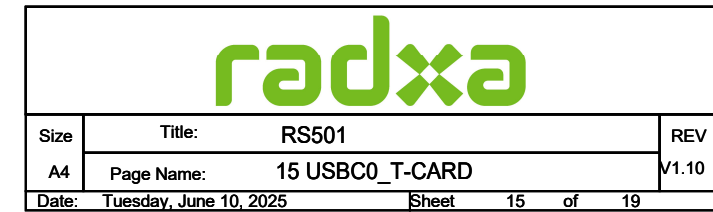
Size	Title: RS501	REV
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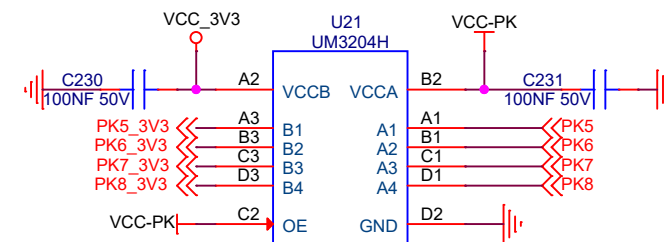
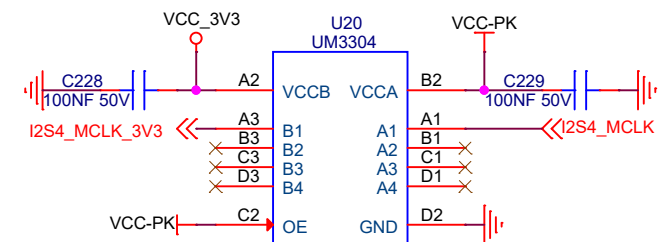
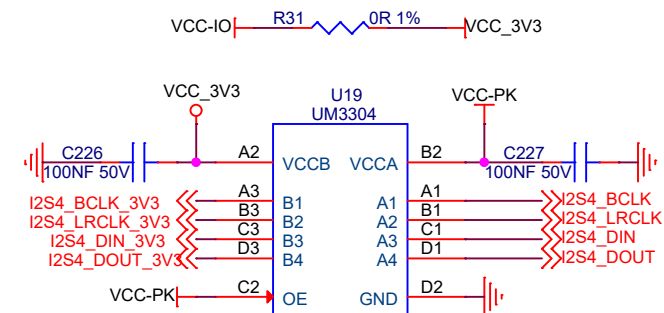
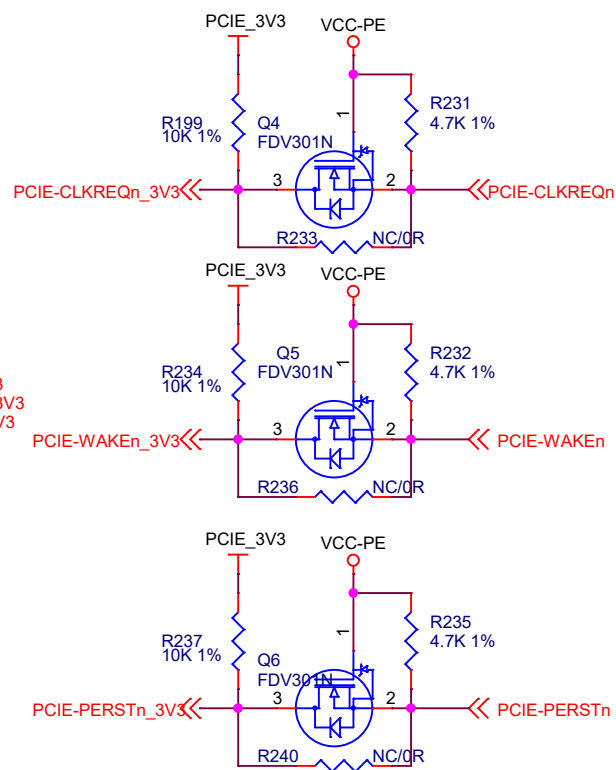
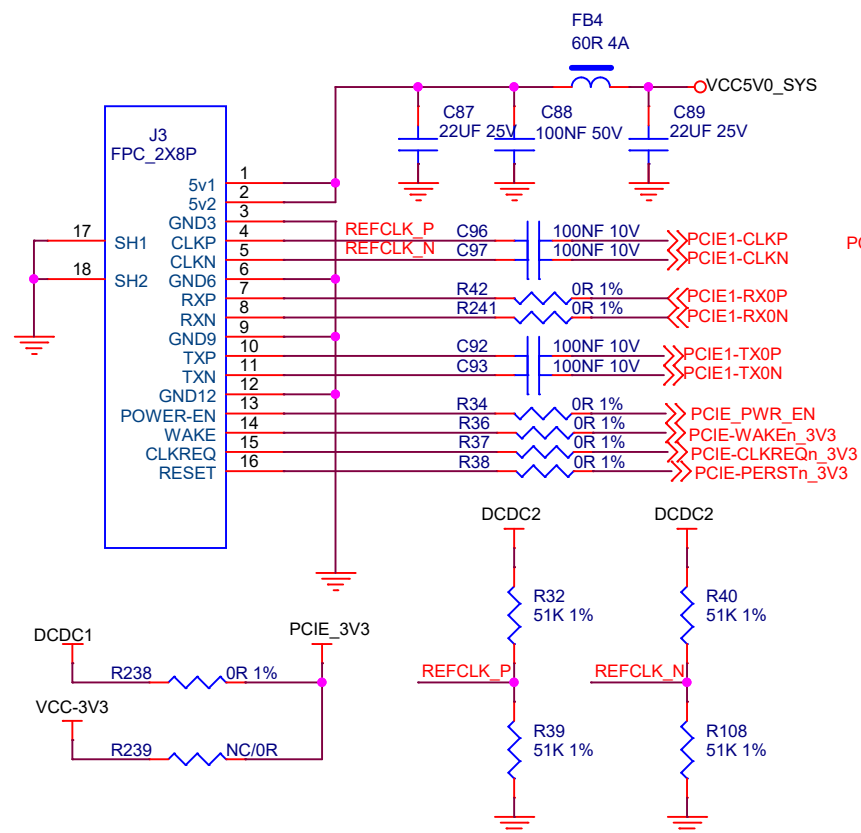
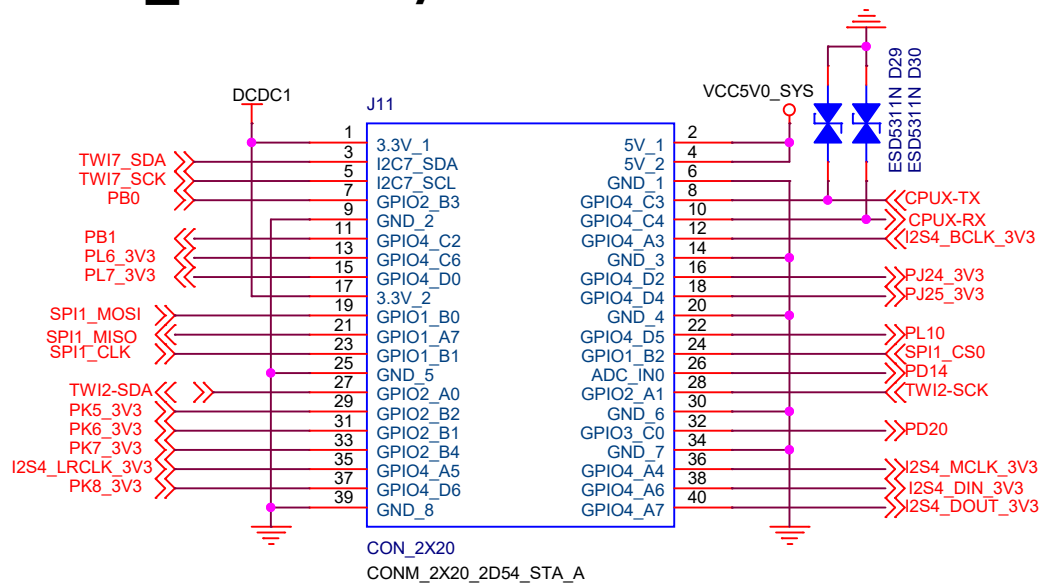
WIFI/BT



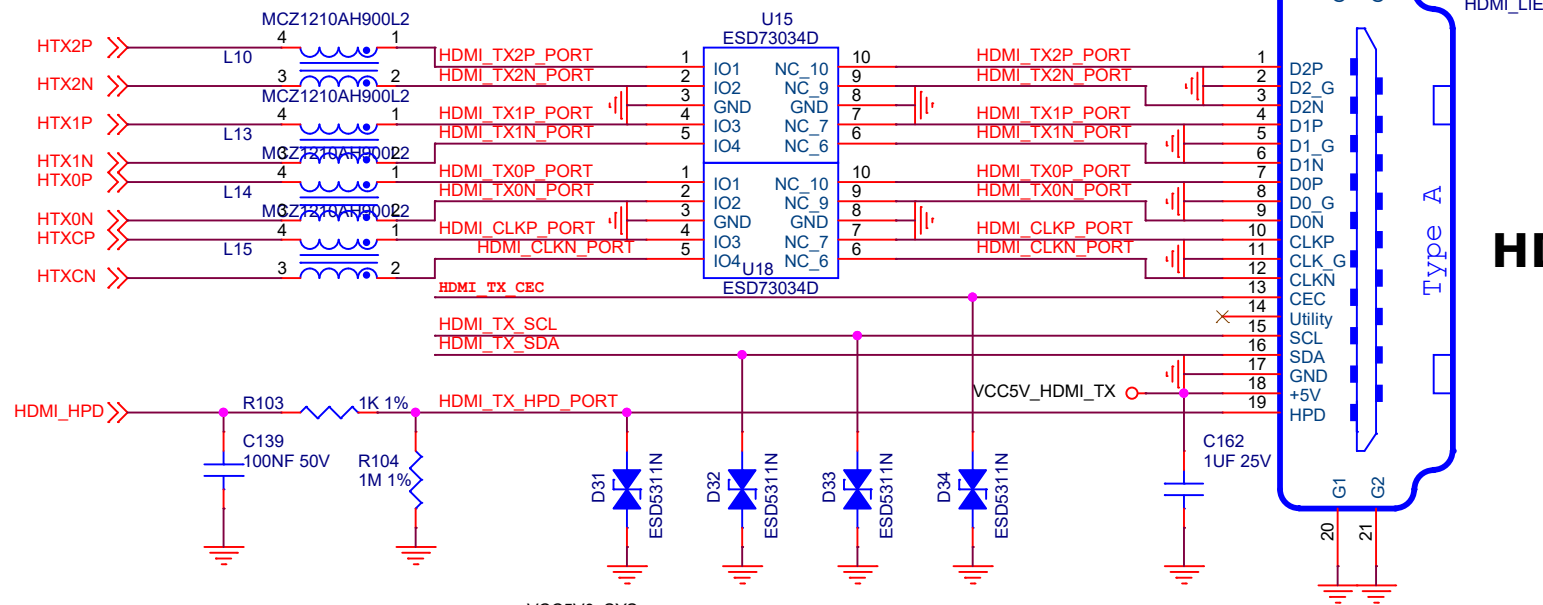
Size:	Title: RS501	REV
A4	Page Name: 13 WIFI/BT	V1.10
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[illegible][illegible]

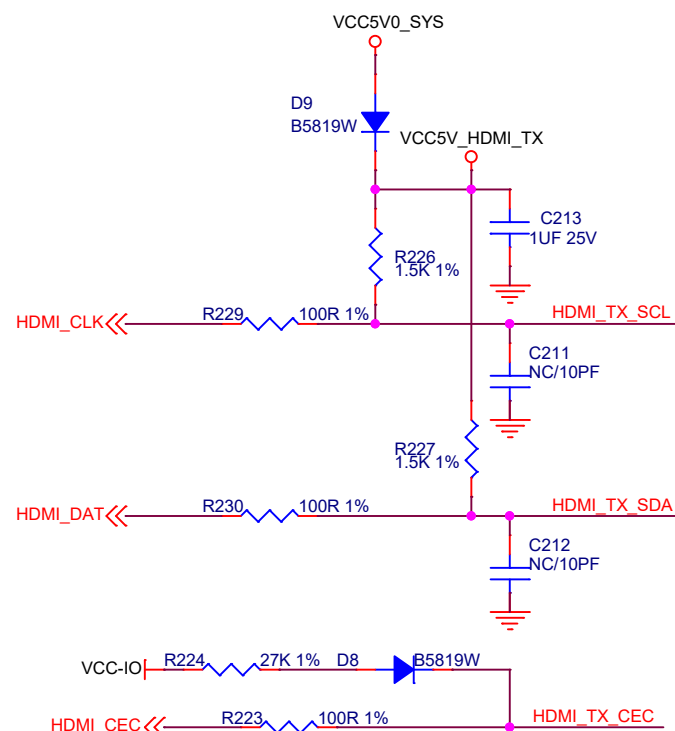
40PIN_Connctor/PCIE interface



Size:	Title: RS501	REV:
A4	Page Name: 16 PCIE_40PIN_CONNECT	V1.10
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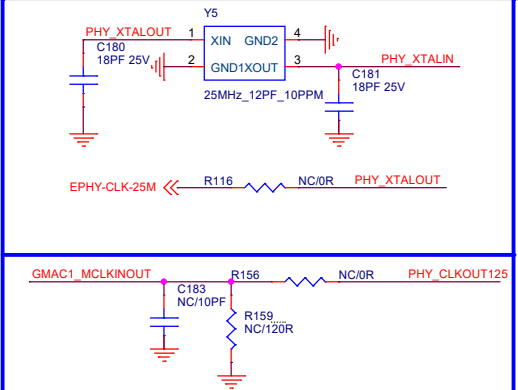
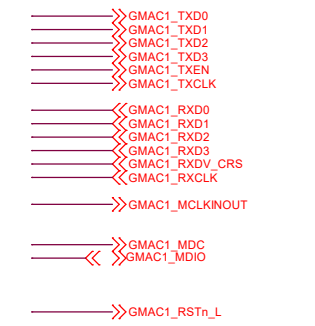


HDMI TX

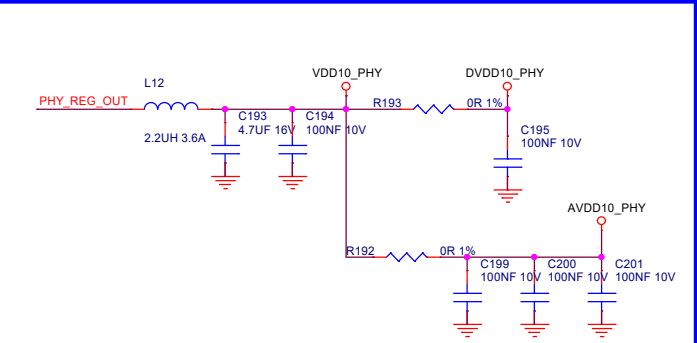
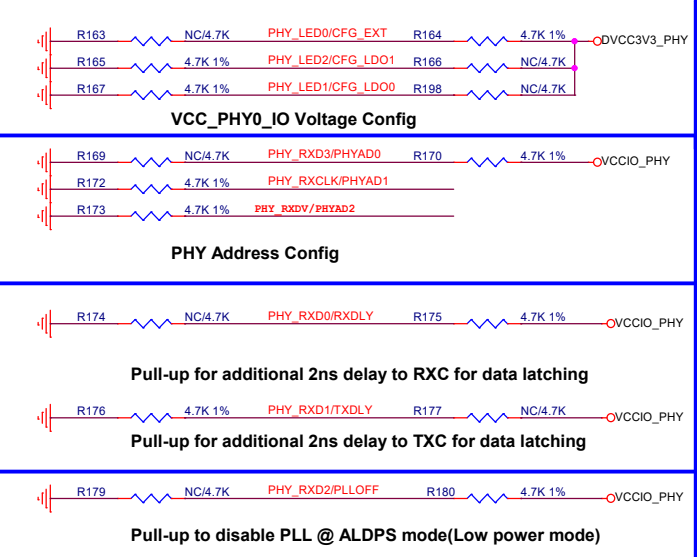
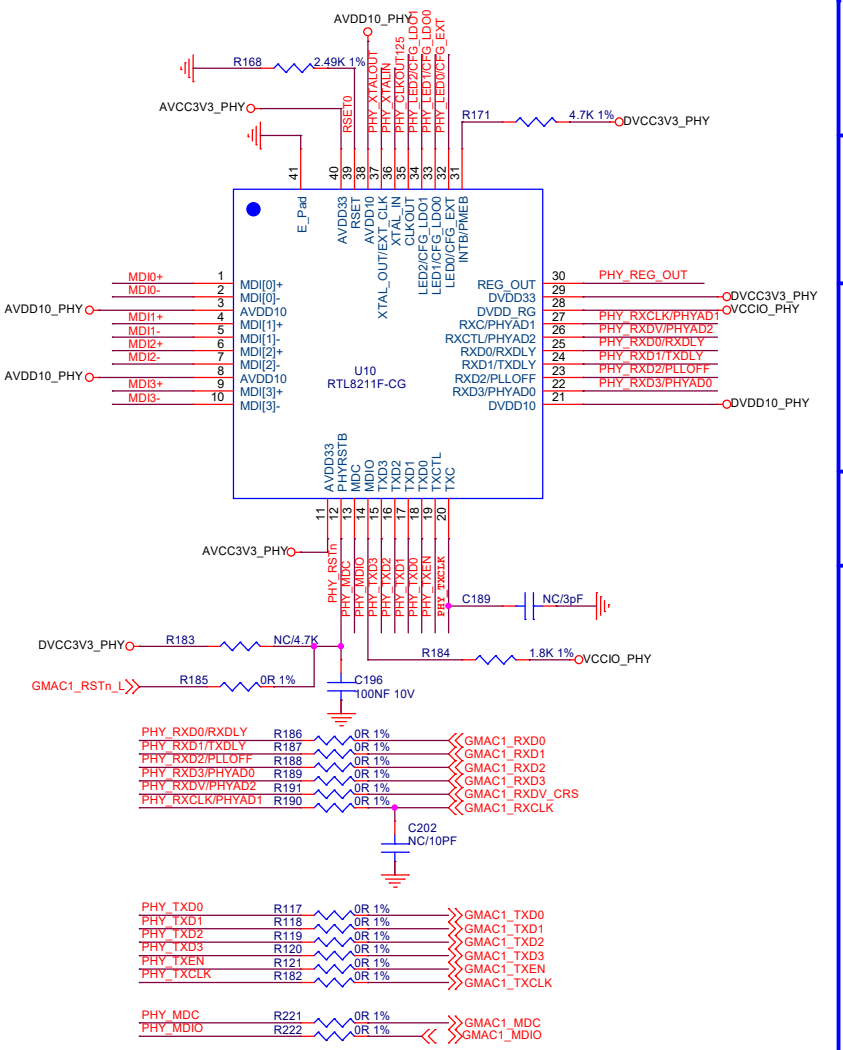
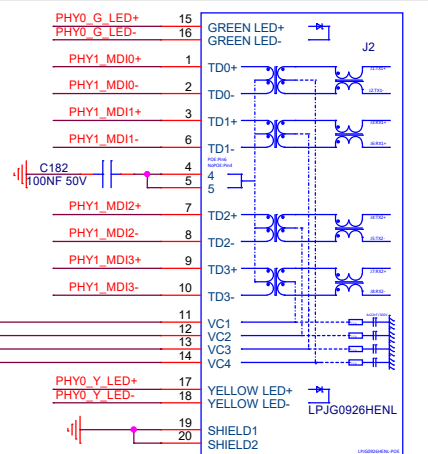
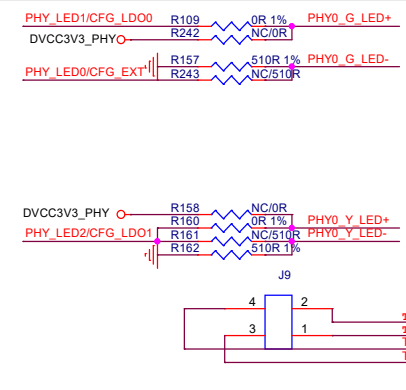
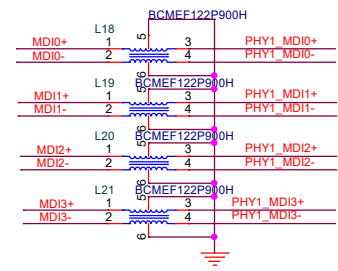


Size	Title: RS501	REV
A4	Page Name: 17.HDMI	V1.10
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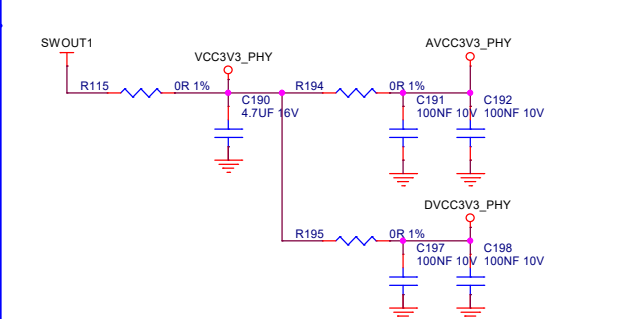
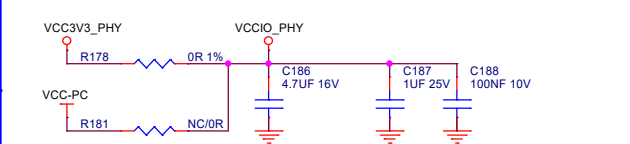
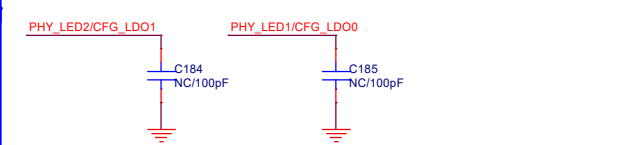
RGMII



RJ45

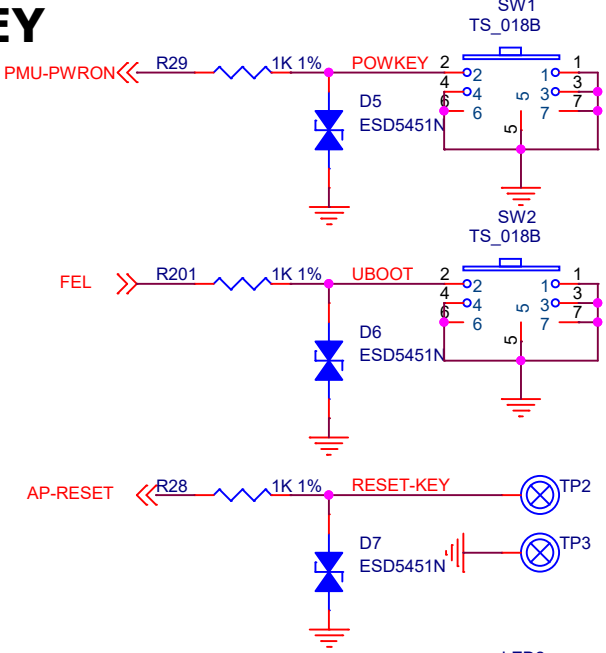


RGMII Power Source	CFG_EXT	CFG_LDO[1:0]	
External 3.3V	1'b1	2'b00	
External 1.8V	1'b1	2'b10	
Internal 1.8V	1'b0	2'b10	

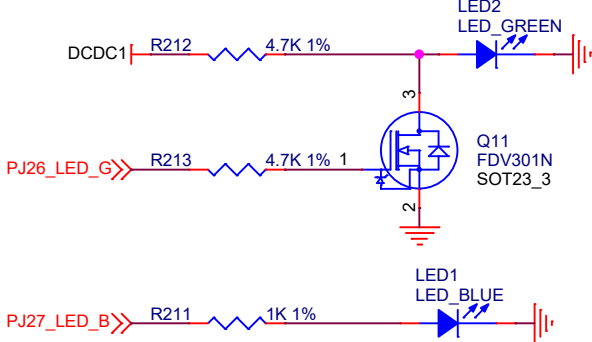


Size	Title:	RS501	REV
A3	Page Name:	18 ETHERNET	V1.10
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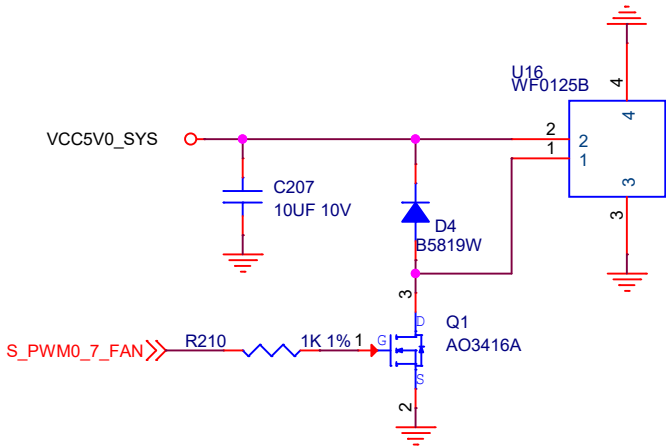
KEY



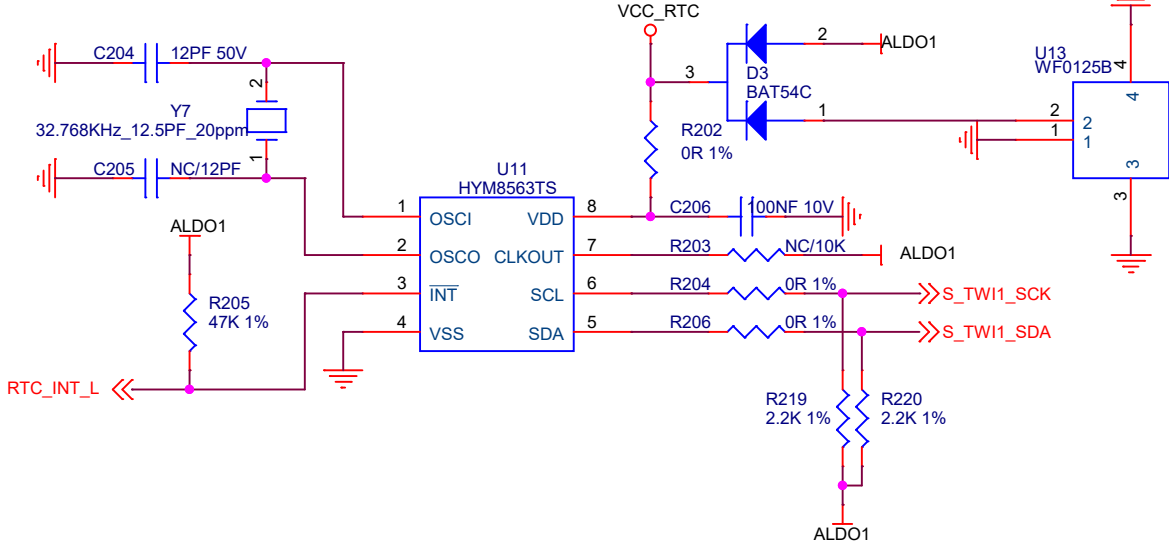
LED



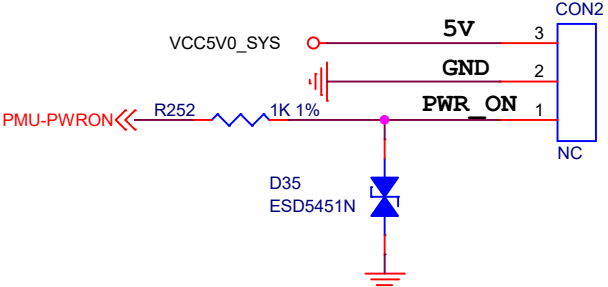
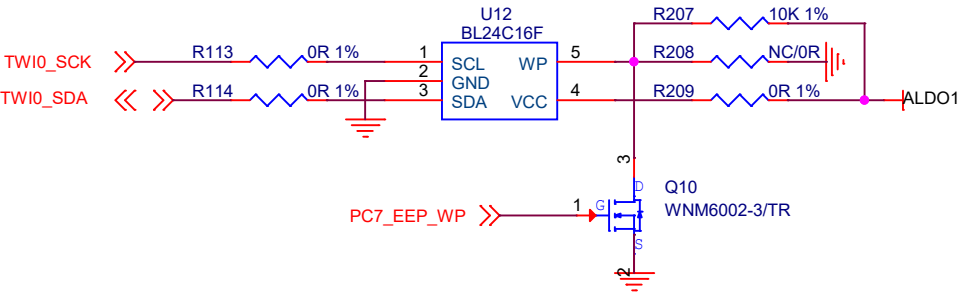
FAN



RTC



EEPROM



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