



Radxa Cubie A7Z

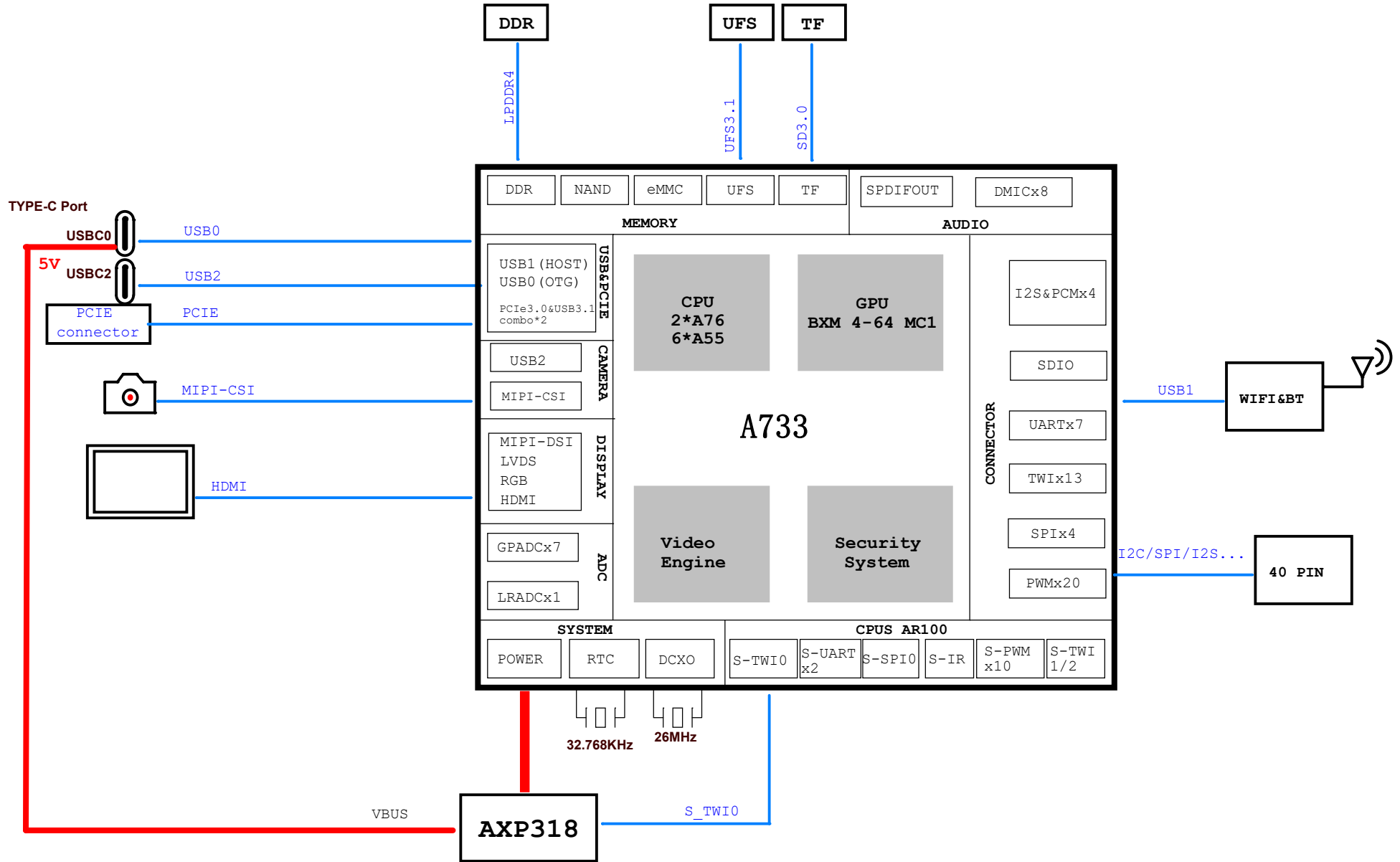
Schematic

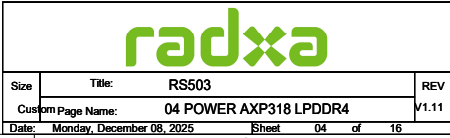
V1.11

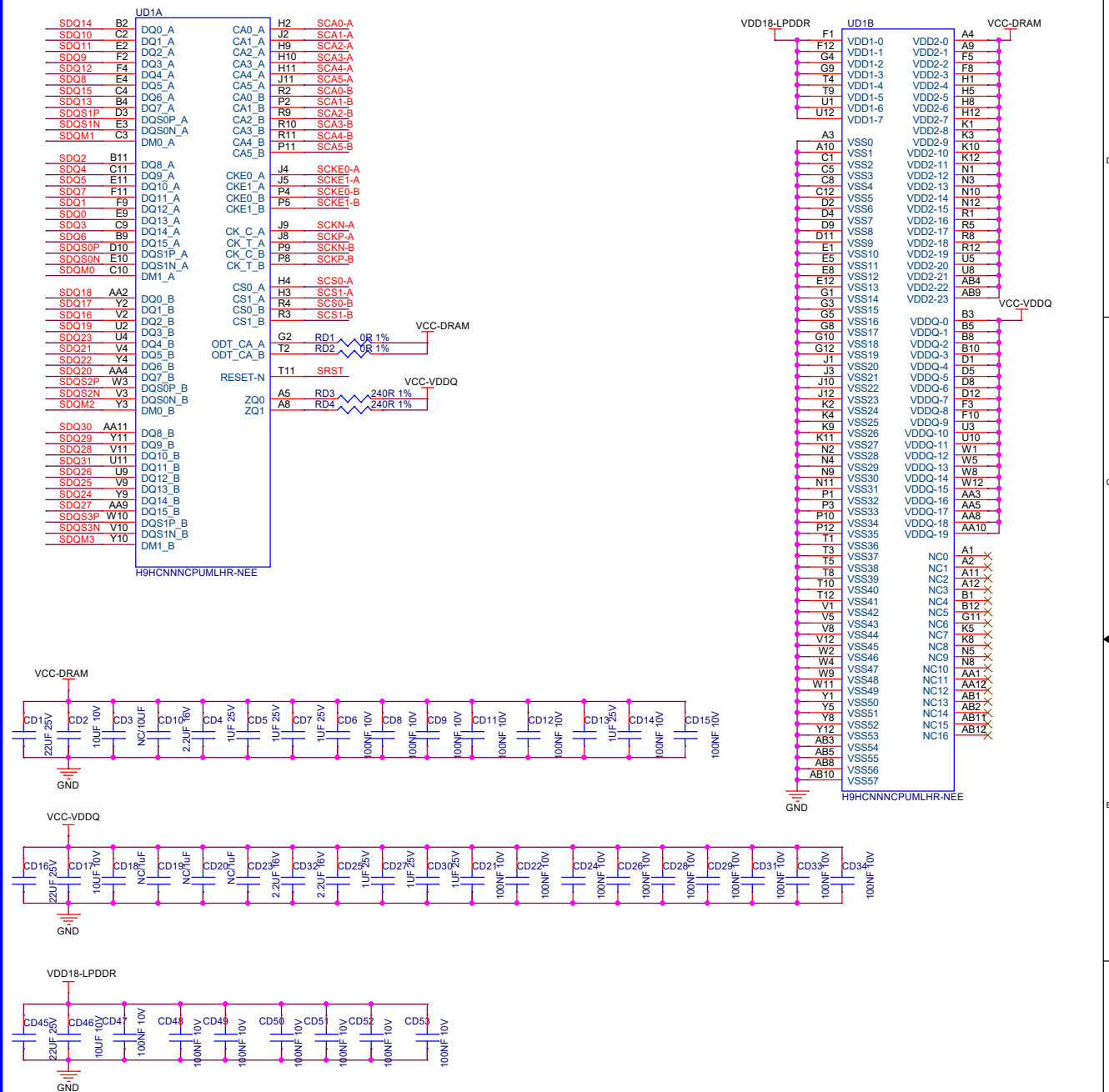
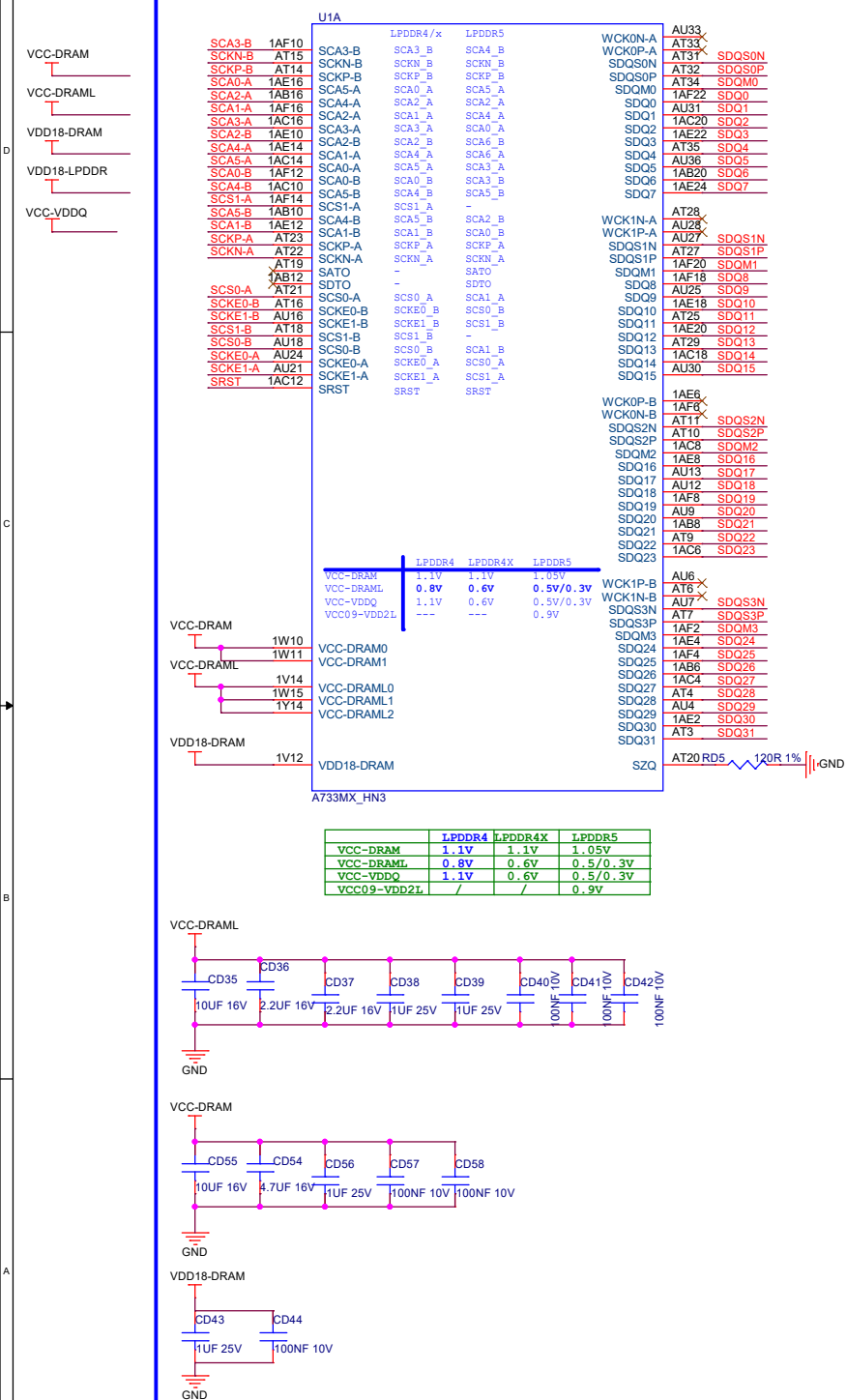
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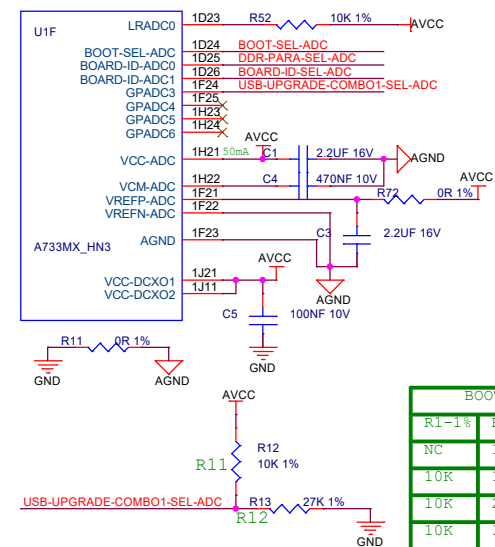
BLOCK





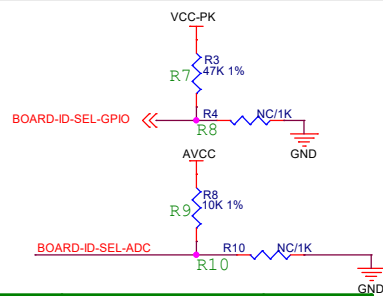
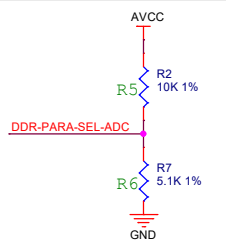
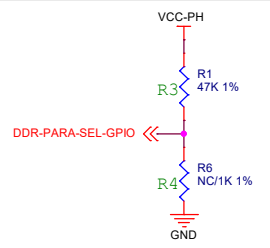
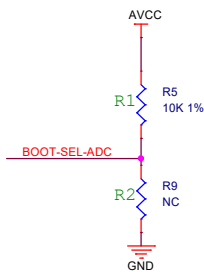


ADC



R11	R12	Volt	Upgrade	COMBO1
10K -1%	1K-1%	163mV		PCIE
	2K7-1%	382mV		USB
	5K1-1%	608mV		Floating
	27K-1%	1315mV	USB0	PCIE
	68K-1%	1569mV	USB0	USB
	NC	1800mV	USB0	Floating

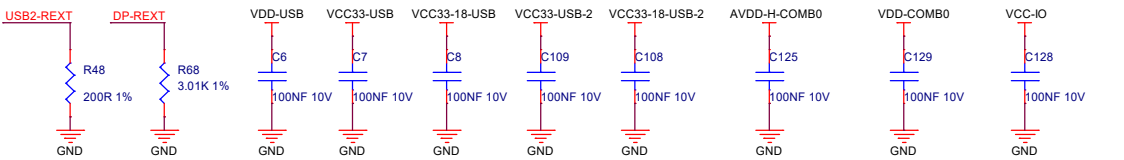
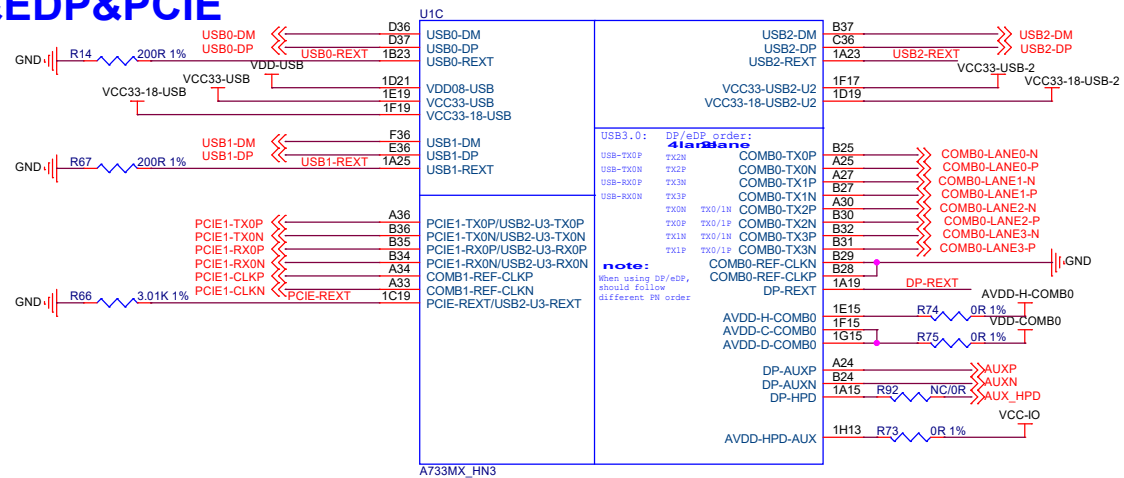
BOOT-SEL-ADC			Boot Media Type Select	
R1-1%	R2-1%	Volt		
NC	10K	0mV	SMHC0->MLC NAND->SLC NAND->TRY(except SPI4 in PI)	
10K	1K	163mV	SMHC0->SLC NAND->MLC NAND->TRY(except SPI4 in PI)	
10K	2K2	325mV	SMHC0->EMMC_USER->EMMC_BOOT->TRY(except SPI4 in PI)	
10K	3K9	505mV	SMHC0->EMMC_BOOT->EMMC_USER->TRY(except SPI4 in PI)	
6K8	4K7	736mV	SMHC0->SPI0 NOR->TRY(except SPI4 in PI)	
6K8	6K8	900mV	SMHC0->SPI0 NAND->TRY(except SPI4 in PI)	
4K3	6K8	1102mV	SMHC0->SPI4 NOR in PI->TRY(except SPI4 in PI)	
2K2	10K	1475mV	SMHC0->SPI4 NAND in PI->TRY(except SPI4 in PI)	
10K	NC	1800mV	SMHC0->UFS->TRY(except SPI4 in PI)	



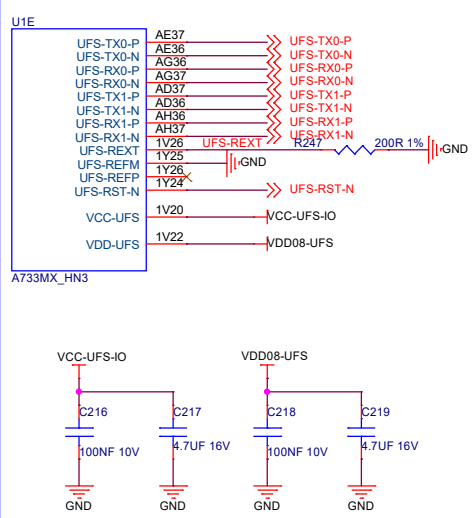
GPIO Level(Set by the R3 pull-up and R4 pull-down resistance of PH16 GPIO)	GPADC Voltage(Fixed pull-up R5 is 10K-1%,Set the voltage by adjusting pull-down resistor R6)	DDR PARA
0	163mV (1K-1%)	DDR PARA 1
0	382mV (2.7K-1%)	DDR PARA 2
0	608mV (5.1K-1%)	DDR PARA 3
0	811mV (8.2K-1%)	DDR PARA 4
0	1050mV (14K-1%)	DDR PARA 5
0	1315mV (27K-1%)	DDR PARA 6
0	1569mV (68K-1%)	DDR PARA 7
0	1800mV (NC)	DDR PARA 8
1	163mV (1K-1%)	DDR PARA 9
1	382mV (2.7K-1%)	DDR PARA 10
1	608mV (5.1K-1%)	DDR PARA 11
1	811mV (8.2K-1%)	DDR PARA 12
1	1050mV (14K-1%)	DDR PARA 13
1	1315mV (27K-1%)	DDR PARA 14
1	1569mV (68K-1%)	DDR PARA 15
1	1800mV (NC)	DDR PARA 16

GPIO-SET		BOARD-ID-SEL-ADC		BoardID Config	
R7	R8	R9	R10	Volt	
NC	1K pull down	10K -1%	1K-1%	163mV	Config-1
			2K7-1%	382mV	Config-2
			5K1-1%	608mV	Config-3
			8K2-1%	811mV	Config-4
			14K-1%	1050mV	Config-5
			27K-1%	1315mV	Config-6
			68K-1%	1569mV	Config-7
			NC	1800mV	Config-8
47K pull up	NC	10K -1%	1K-1%	163mV	Config-9
			2K7-1%	382mV	Config-10
			5K1-1%	608mV	Config-11
			8K2-1%	811mV	Config-12
			14K-1%	1050mV	Config-13
			27K-1%	1315mV	Config-14
			68K-1%	1569mV	Config-15
			NC	1800mV	Config-16

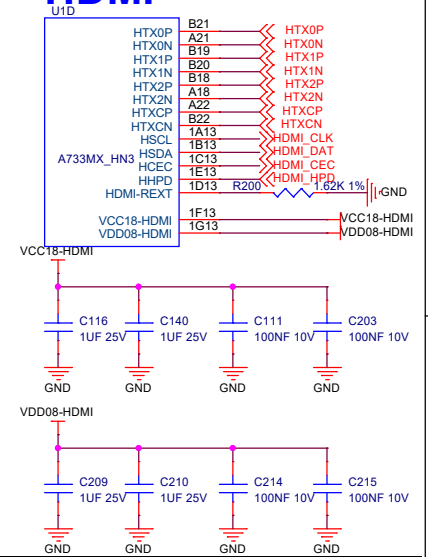
USB&EDP&PCIE



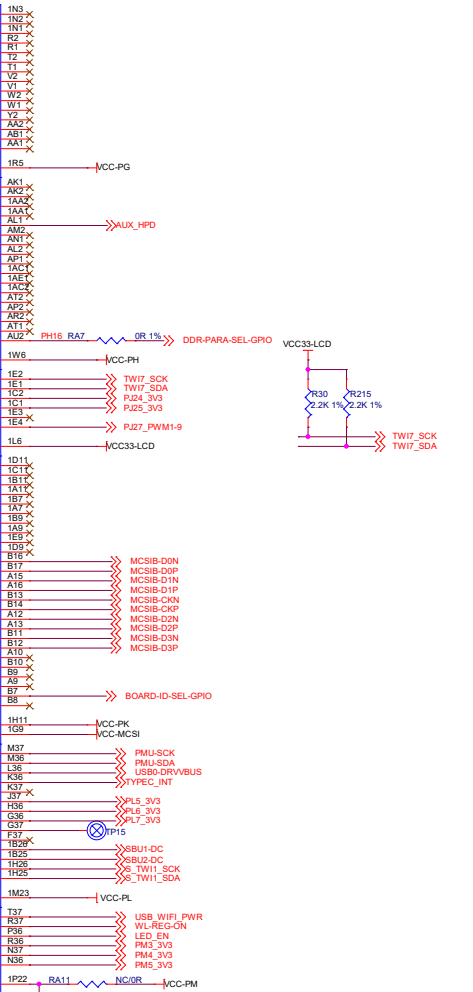
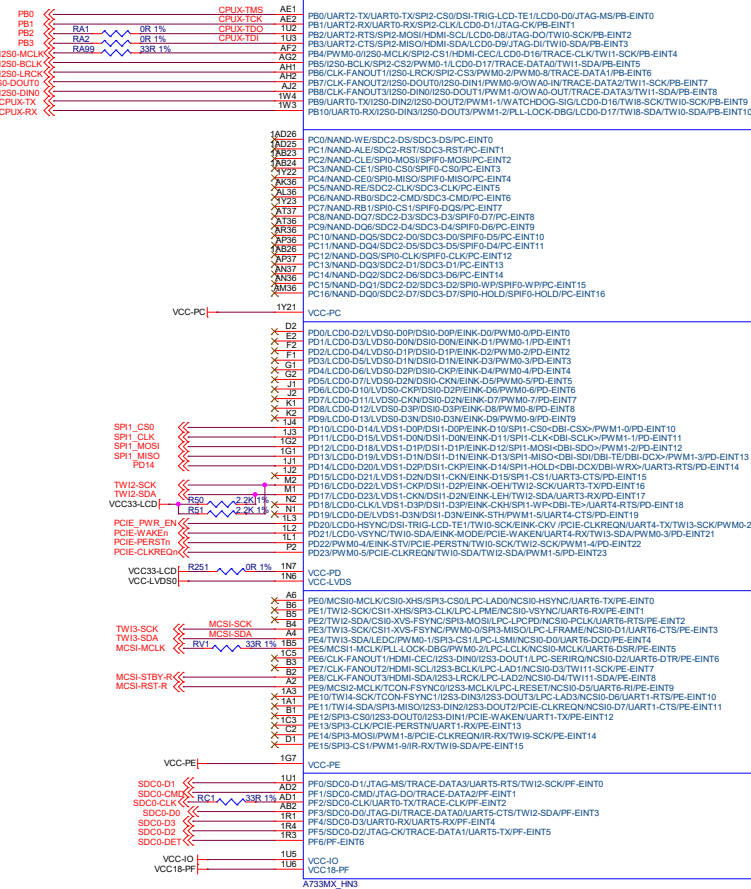
UFS



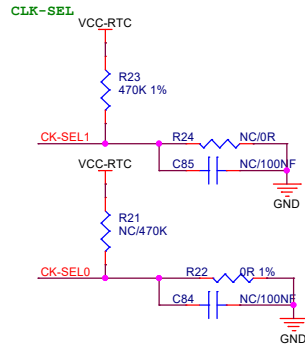
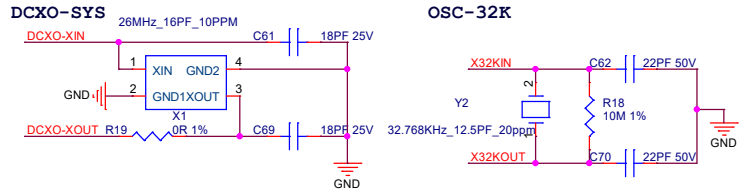
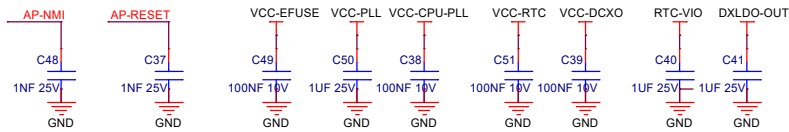
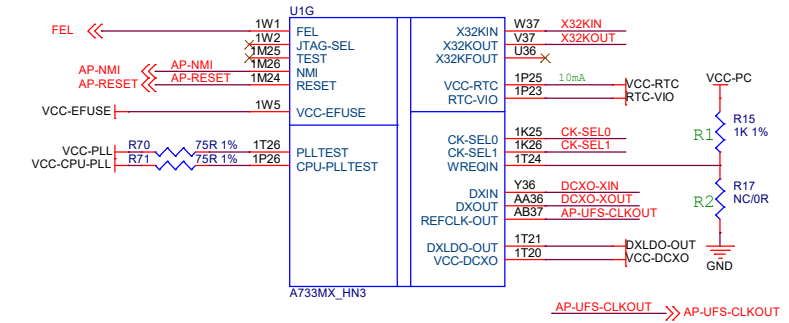
HDMI



GPIO

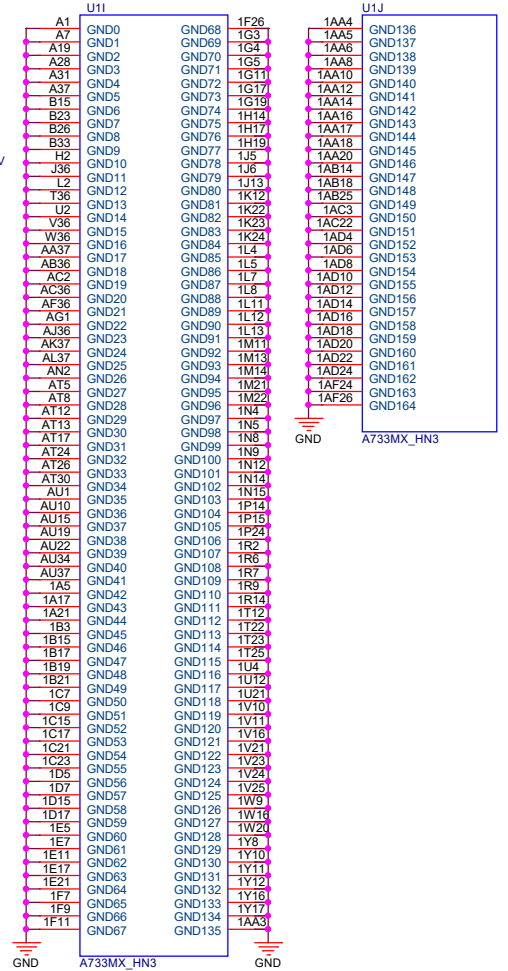
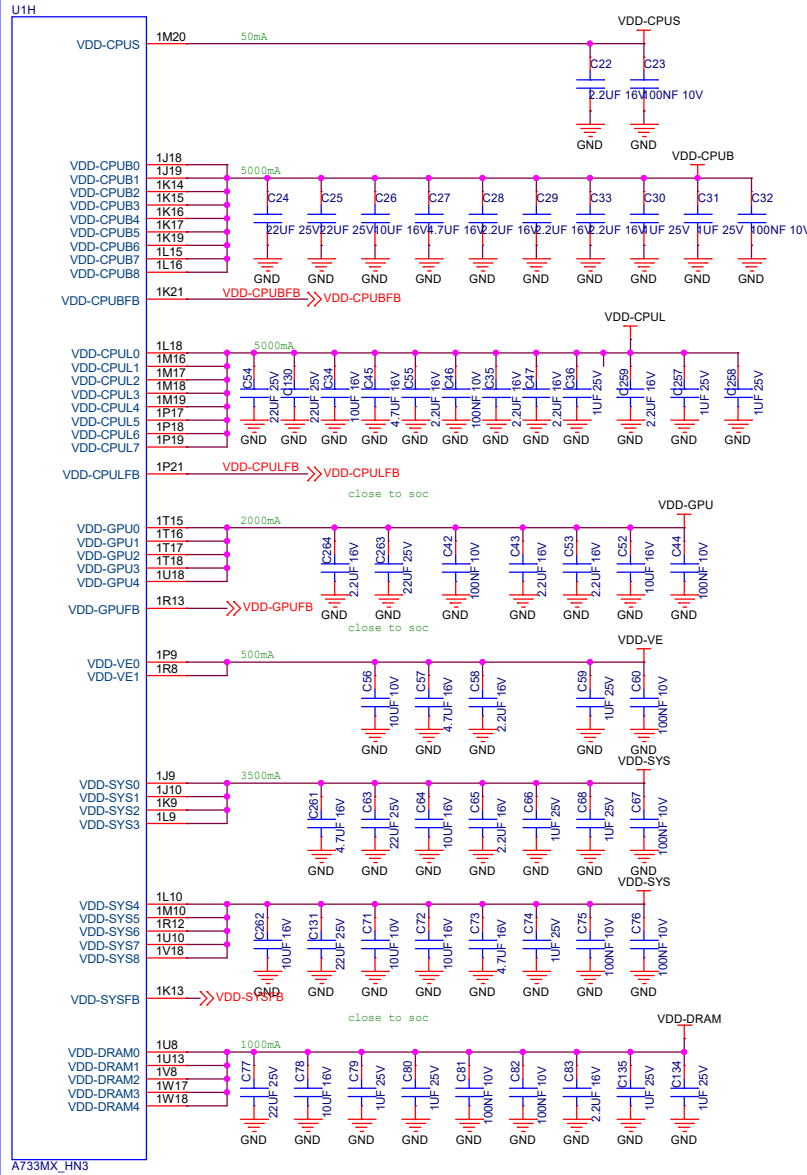


SYS



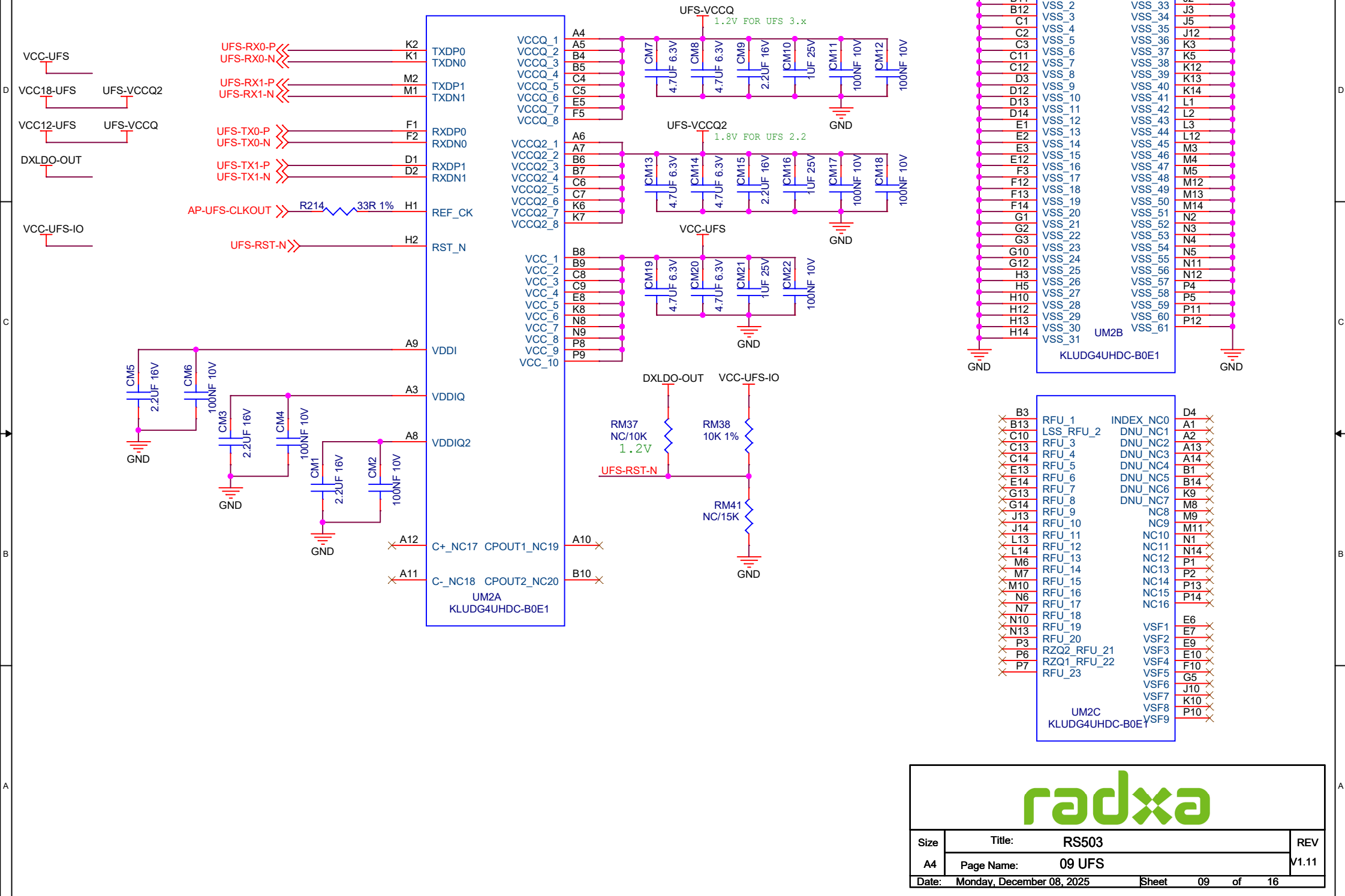
default 24M	SEL1	SEL0
SOC=19.2M	0	1
SOC=26M	1	0
SOC=24M, UFS=19.2M	1	1
SOC=24M, UFS=26M	0	0

CORE POWER



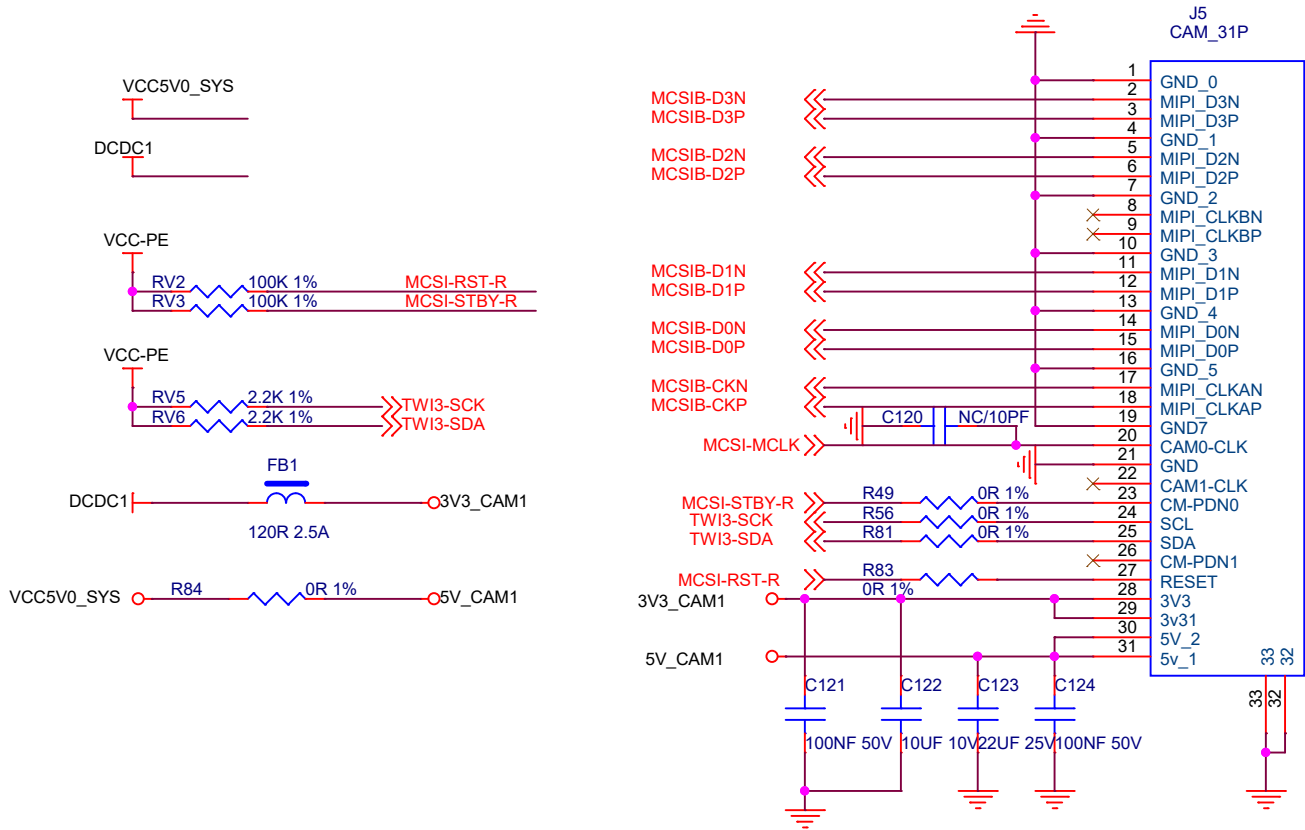
radxa

UFS



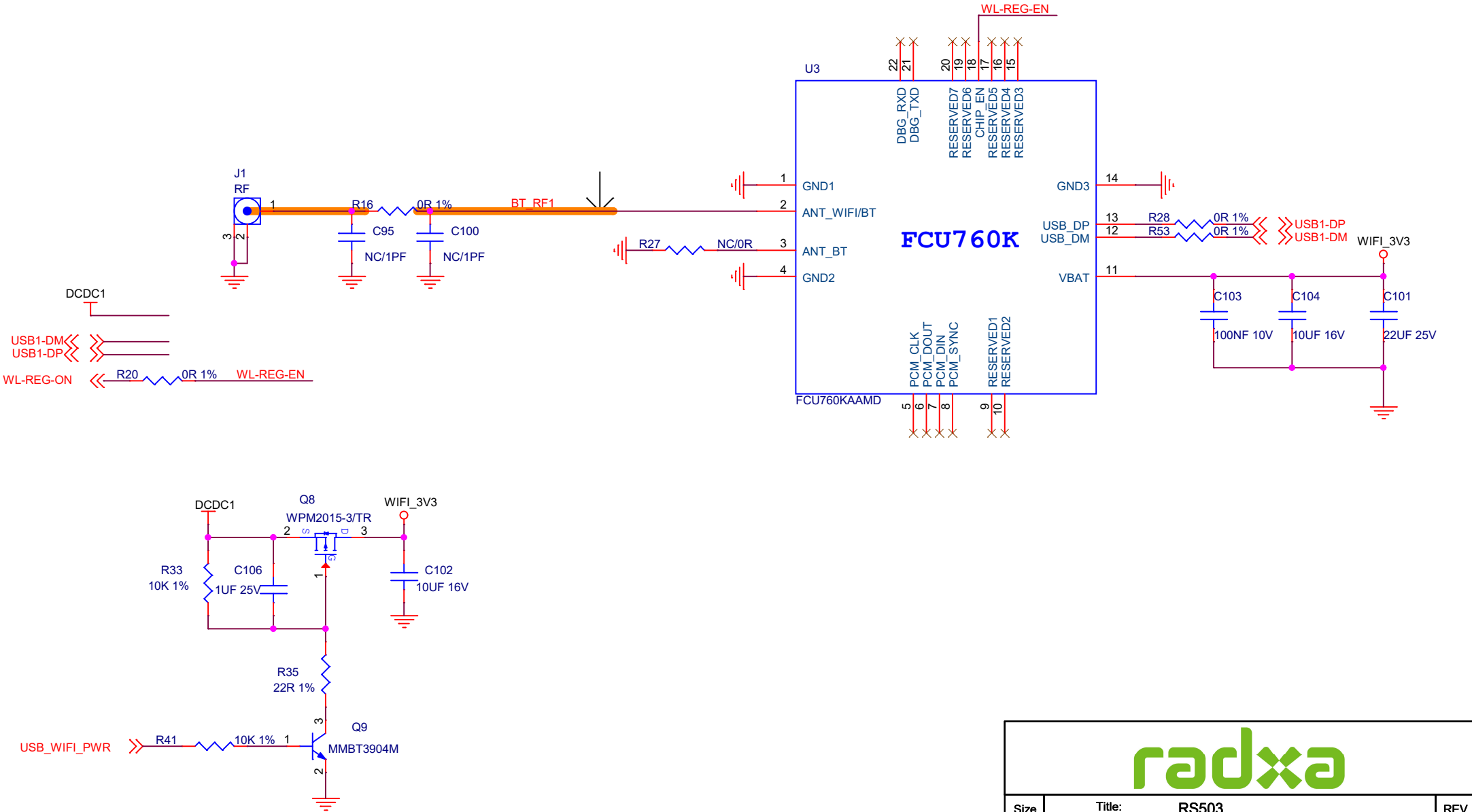
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Date:	Monday, December 08, 2025	Sheet 09 of 16

CAMERA



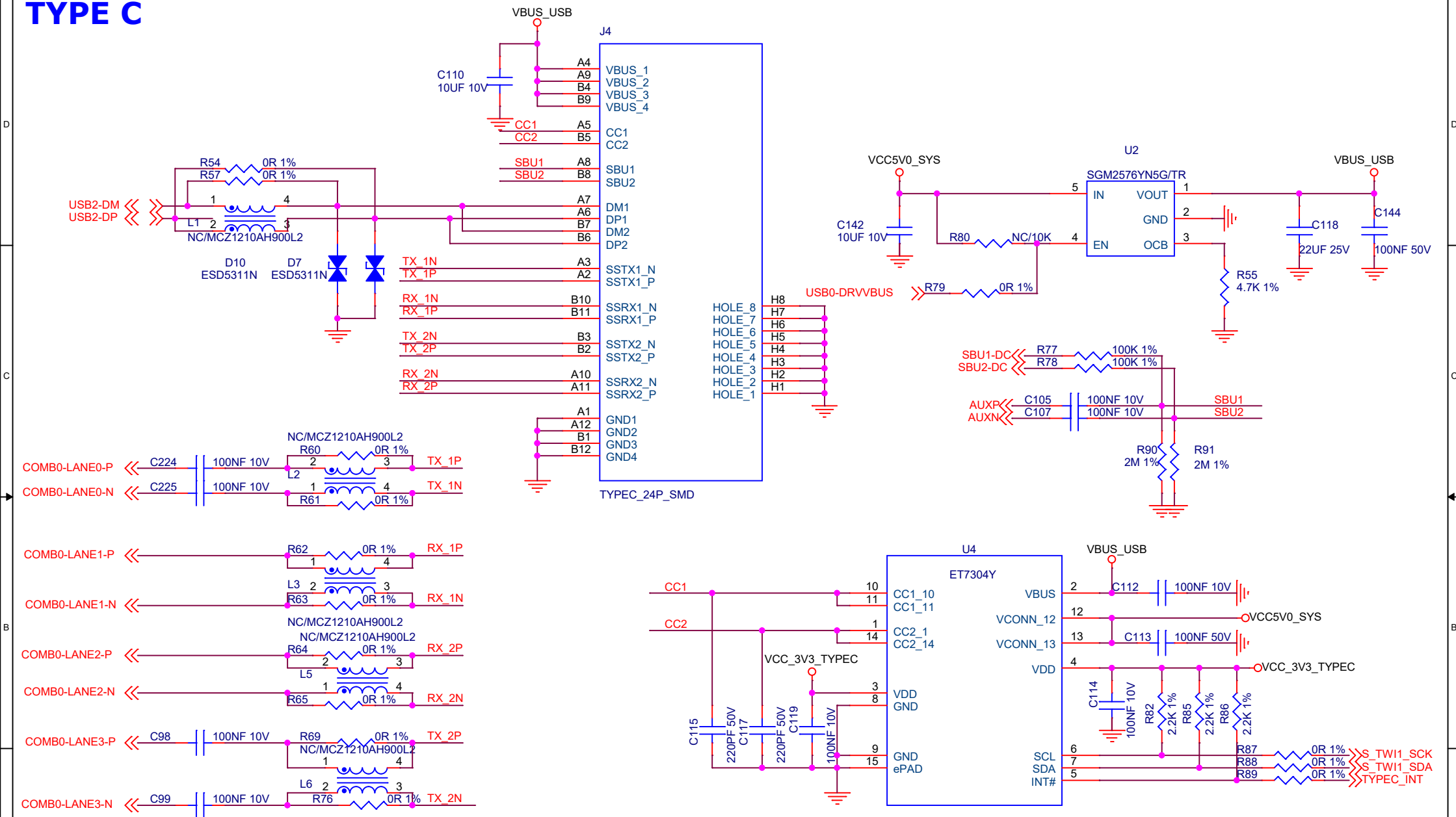
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A4	Page Name: 10 CAMERA	V1.11
Date:	Monday, December 08, 2025	Sheet 10 of 16

WIFI/BT

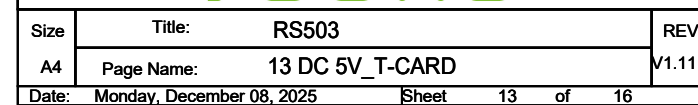
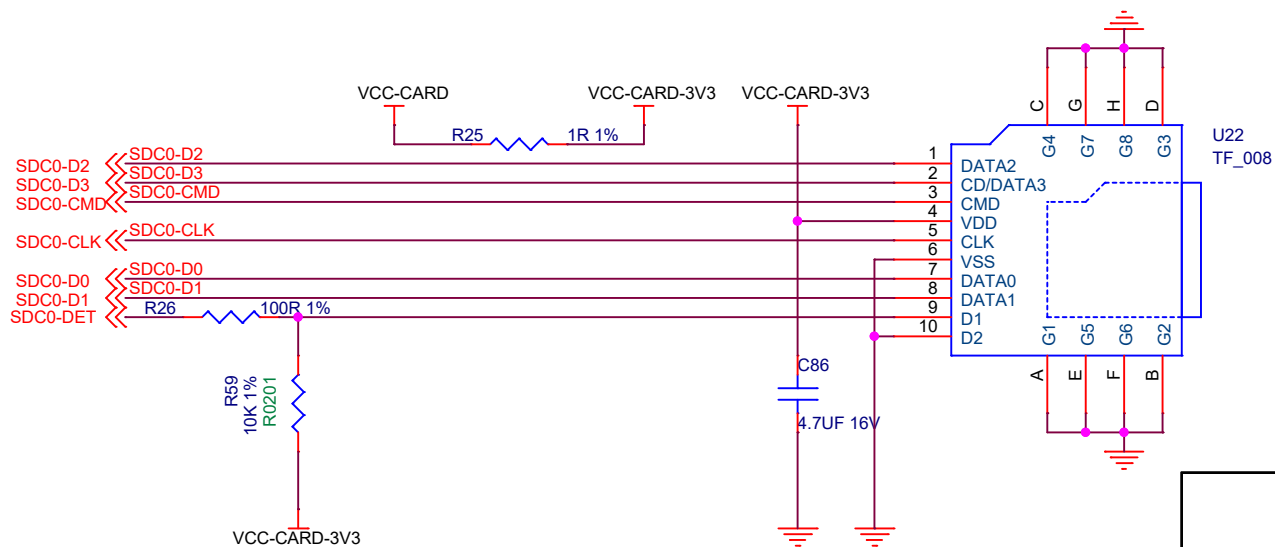


Size	Title: RS503	REV
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Date: Monday, December 08, 2025		
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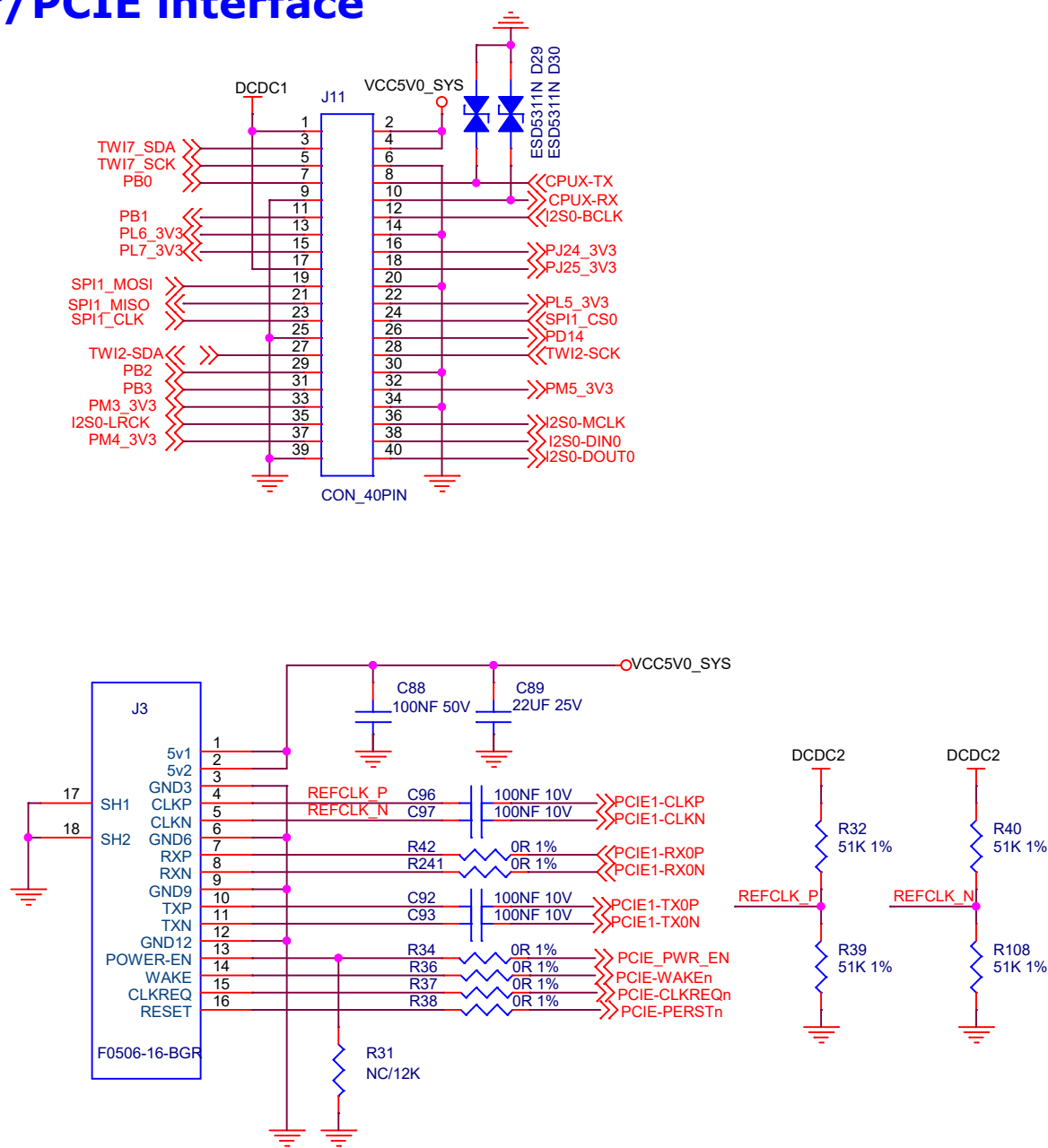
TYPE C



Size	Title: RS503	REV
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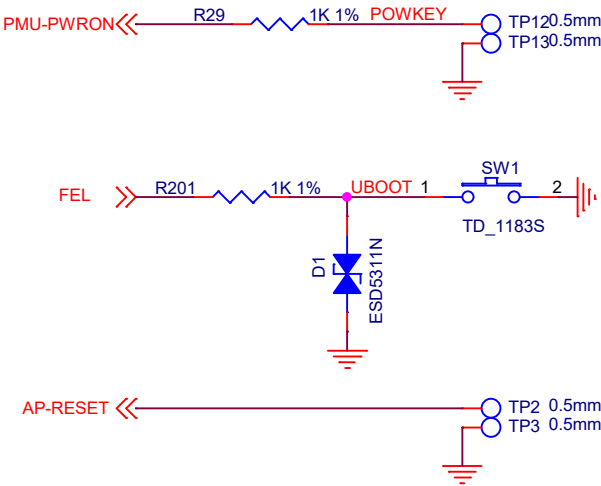


40PIN Connctor/PCIE interface

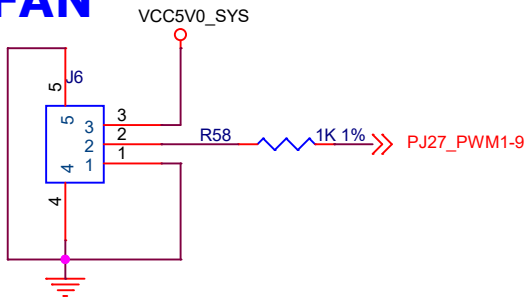


Size	Title: RS503	REV
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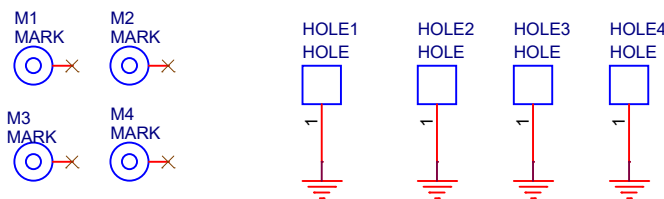
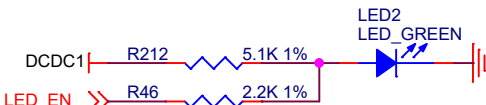
KEY



FAN



LED



Size	Title: RS503	REV
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