



Radxa ROCK 4D

Schematic

V1.12

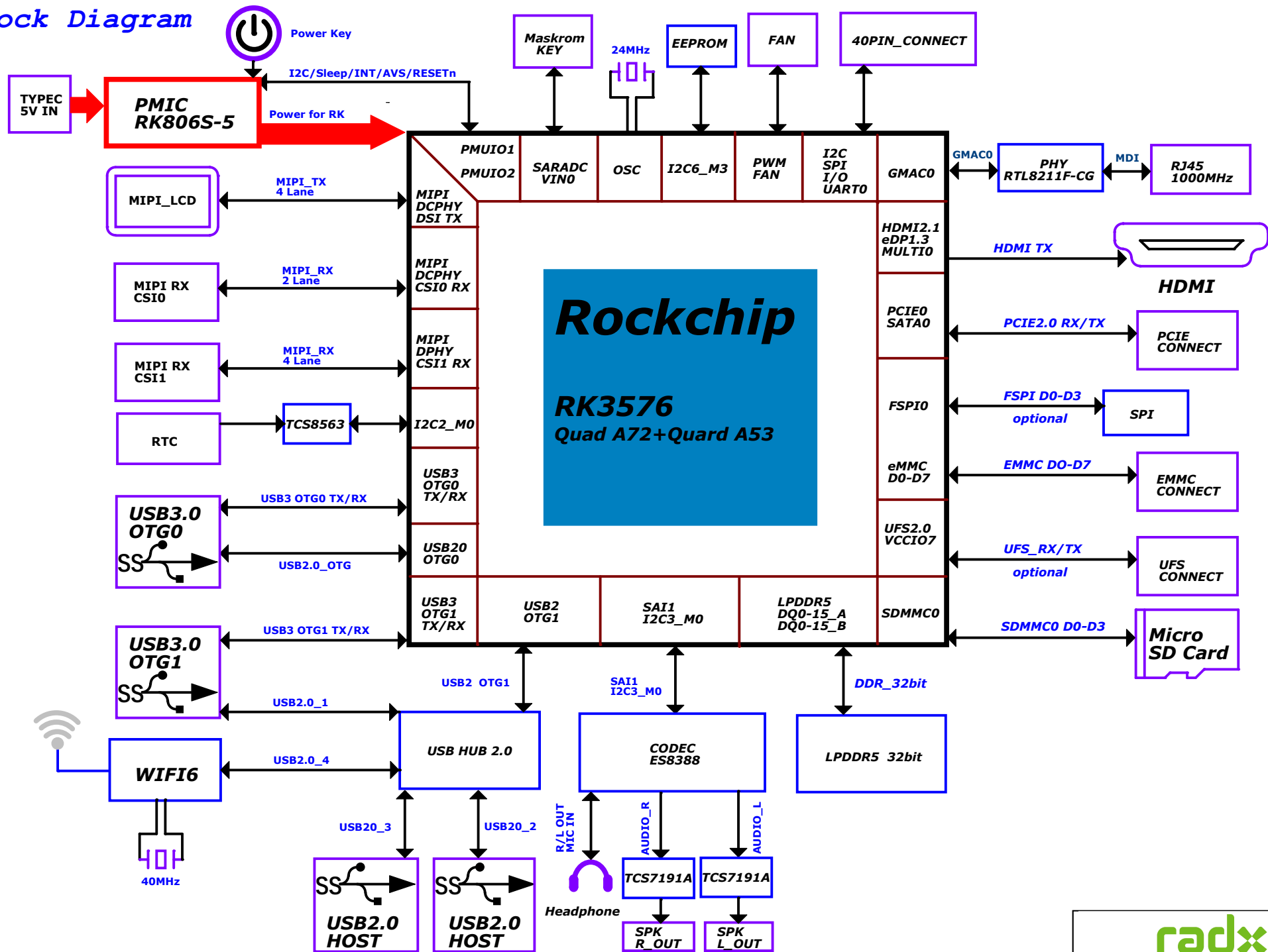
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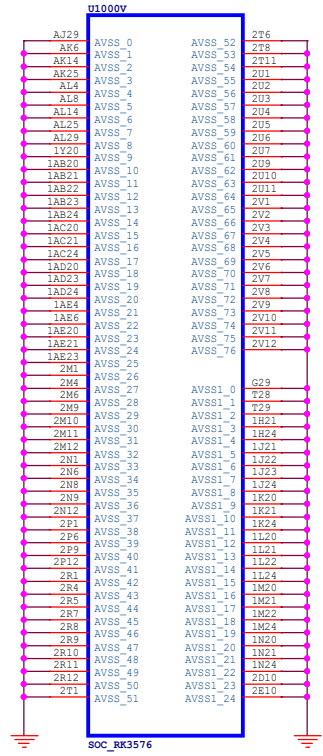
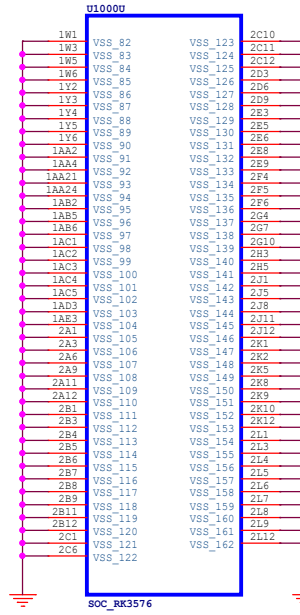
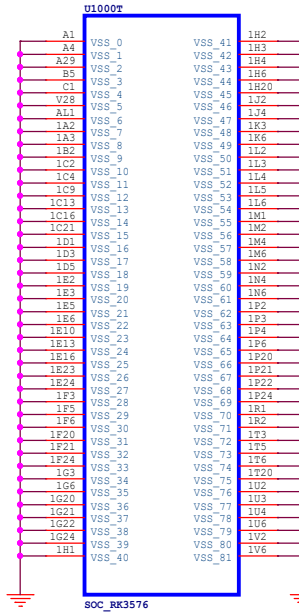
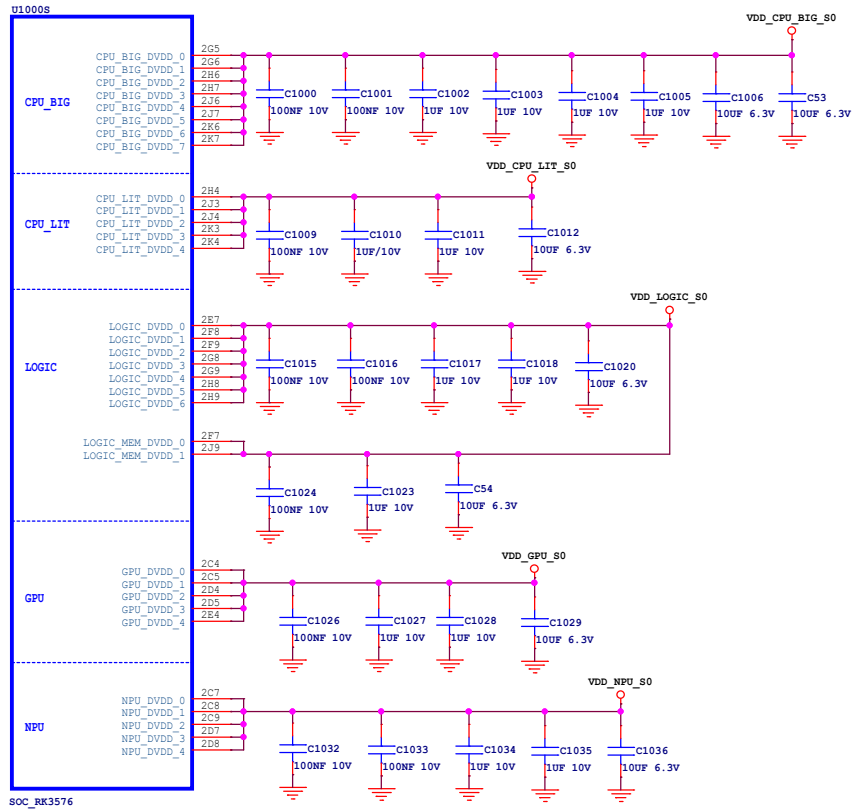


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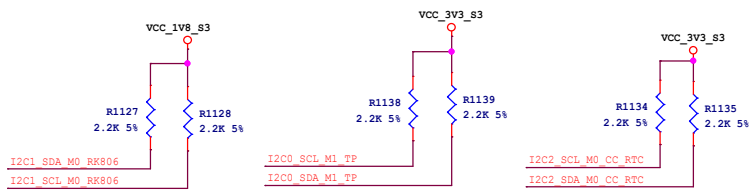
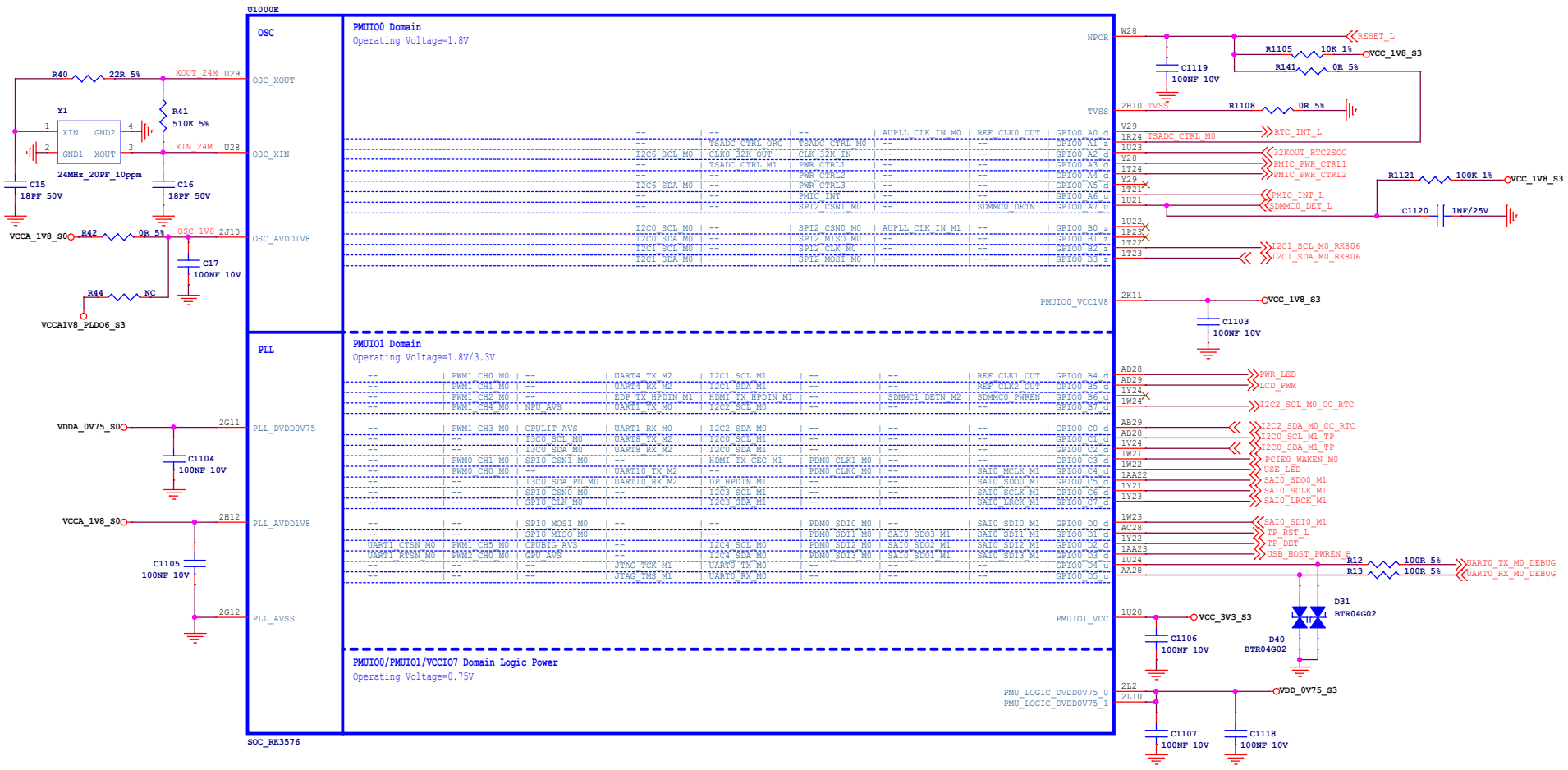
Block Diagram

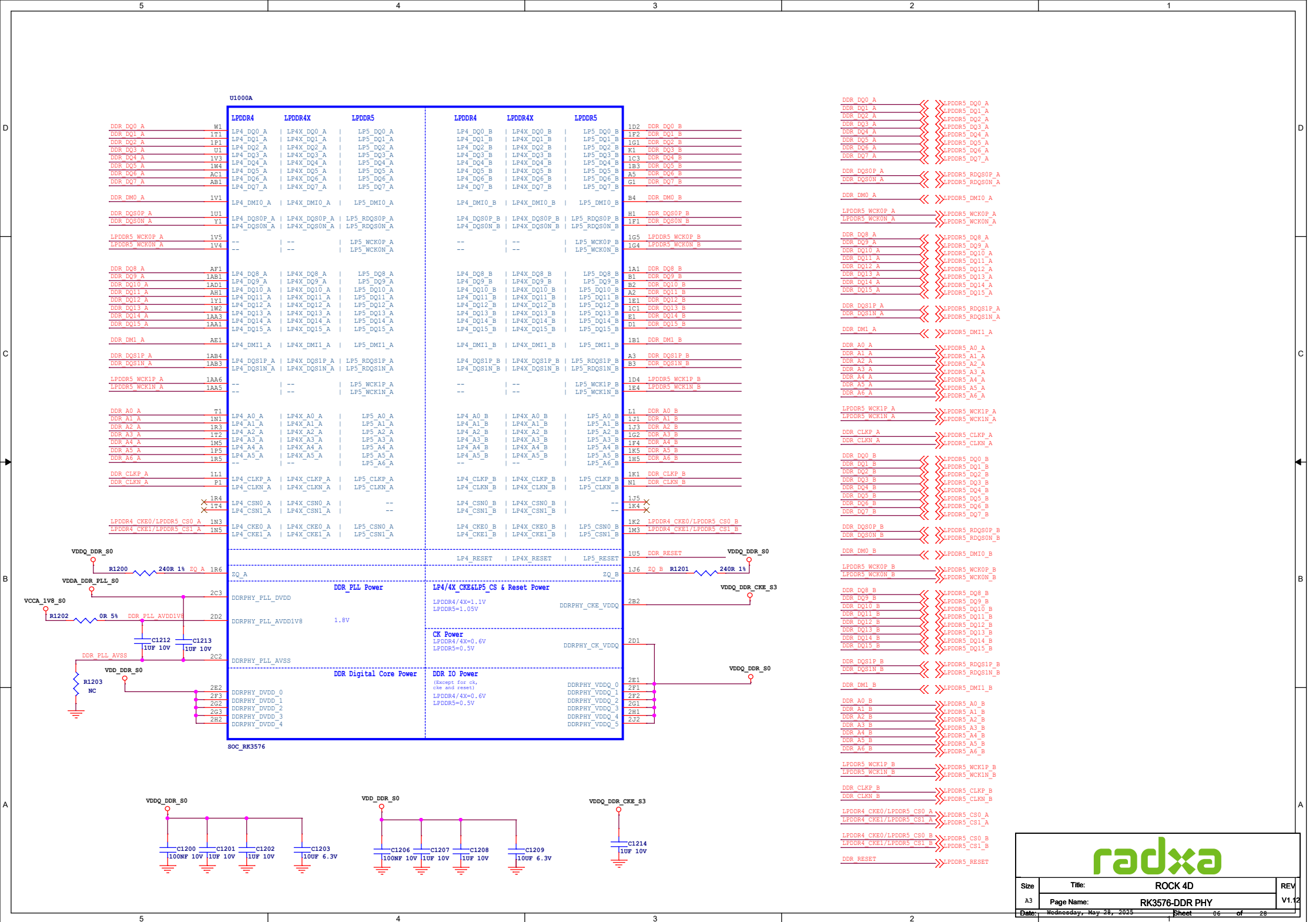


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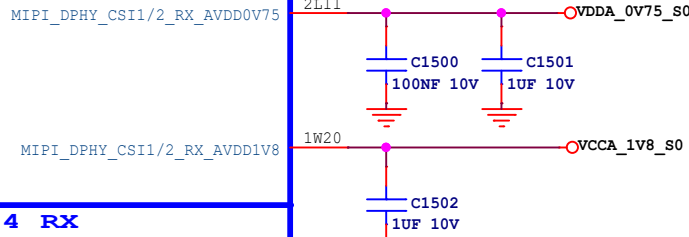


U1000P

MIPI DPHY CSI1/2 RX

MIPI V1.2/2.5Gbps

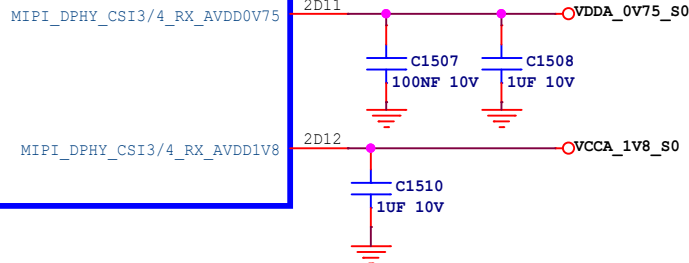
MIPI_DPHY_CSI1_RX_D0N	AE28	MIPI_DPHY_CSI1_RX_D0N
MIPI_DPHY_CSI1_RX_D0P	AE29	MIPI_DPHY_CSI1_RX_D0P
MIPI_DPHY_CSI1_RX_D1N	AF28	MIPI_DPHY_CSI1_RX_D1N
MIPI_DPHY_CSI1_RX_D1P	AF29	MIPI_DPHY_CSI1_RX_D1P
MIPI_DPHY_CSI1_RX_CLKN	1AC23	MIPI_DPHY_CSI1_RX_CLKN
MIPI_DPHY_CSI1_RX_CLKP	1AC22	MIPI_DPHY_CSI1_RX_CLKP
MIPI_DPHY_CSI1_RX_D2N	AG28	MIPI_DPHY_CSI1_RX_D2N
MIPI_DPHY_CSI1_RX_D2P	AG29	MIPI_DPHY_CSI1_RX_D2P
MIPI_DPHY_CSI1_RX_D3N	AH28	MIPI_DPHY_CSI1_RX_D3N
MIPI_DPHY_CSI1_RX_D3P	AH29	MIPI_DPHY_CSI1_RX_D3P
--	1AD22	MIPI_DPHY_CSI2_RX_CLKN
--	1AD21	MIPI_DPHY_CSI2_RX_CLKP



MIPI DPHY CSI3/4 RX

MIPI V1.2/2.5Gbps

MIPI_DPHY_CSI3_RX_D0N	H29	MIPI_DPHY_CSI3_RX_D0N
MIPI_DPHY_CSI3_RX_D0P	H28	MIPI_DPHY_CSI3_RX_D0P
MIPI_DPHY_CSI3_RX_D1N	J29	MIPI_DPHY_CSI3_RX_D1N
MIPI_DPHY_CSI3_RX_D1P	J28	MIPI_DPHY_CSI3_RX_D1P
MIPI_DPHY_CSI3_RX_CLKN	1H23	MIPI_DPHY_CSI3_RX_CLKN
MIPI_DPHY_CSI3_RX_CLKP	1H22	MIPI_DPHY_CSI3_RX_CLKP
MIPI_DPHY_CSI3_RX_D2N	K29	MIPI_DPHY_CSI4_RX_D0N
MIPI_DPHY_CSI3_RX_D2P	K28	MIPI_DPHY_CSI4_RX_D0P
MIPI_DPHY_CSI3_RX_D3N	L29	MIPI_DPHY_CSI4_RX_D1N
MIPI_DPHY_CSI3_RX_D3P	L28	MIPI_DPHY_CSI4_RX_D1P
--	1K23	MIPI_DPHY_CSI4_RX_CLKN
--	1K22	MIPI_DPHY_CSI4_RX_CLKP



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MIPI DCPHY DSI TX

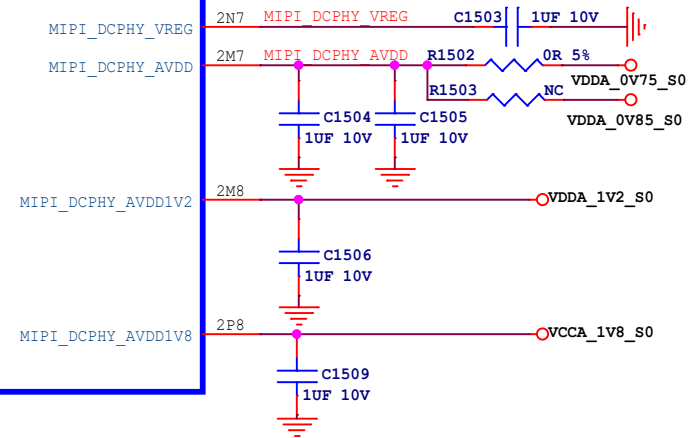
D-PHY:V2.0 2.5Gbps/Lane
C-PHY:V1.1 1.7Gsps/Trio

MIPI_DPHY_DSI_TX_D0N	AK15	MIPI_DPHY_DSI_TX_D0N
MIPI_DPHY_DSI_TX_D0P	AL15	MIPI_DPHY_DSI_TX_D0P
MIPI_DPHY_DSI_TX_D1N	AK16	MIPI_DPHY_DSI_TX_D1N
MIPI_DPHY_DSI_TX_D1P	AL16	MIPI_DPHY_DSI_TX_D1P
MIPI_DPHY_DSI_TX_CLKN	AK17	MIPI_DPHY_DSI_TX_CLKN
MIPI_DPHY_DSI_TX_CLKP	AL17	MIPI_DPHY_DSI_TX_CLKP
MIPI_DPHY_DSI_TX_D2N	AK18	MIPI_DPHY_DSI_TX_D2N
MIPI_DPHY_DSI_TX_D2P	AL18	MIPI_DPHY_DSI_TX_D2P
MIPI_DPHY_DSI_TX_D3N	AK19	MIPI_DPHY_DSI_TX_D3N
MIPI_DPHY_DSI_TX_D3P	AL19	MIPI_DPHY_DSI_TX_D3P

MIPI DCPHY CSI RX

D-PHY:V2.0 4.5Gbps/Lane
C-PHY:V1.1 2.5Gsps/Trio

MIPI_DPHY_CSI0_RX_D0N	AL20	MIPI_DPHY_CSI0_RX_D0N
MIPI_DPHY_CSI0_RX_D0P	AK20	MIPI_DPHY_CSI0_RX_D0P
MIPI_DPHY_CSI0_RX_D1N	AL21	MIPI_DPHY_CSI0_RX_D1N
MIPI_DPHY_CSI0_RX_D1P	AK21	MIPI_DPHY_CSI0_RX_D1P
MIPI_DPHY_CSI0_RX_CLKN	AL22	MIPI_DPHY_CSI0_RX_CLKON
MIPI_DPHY_CSI0_RX_CLKP	AK22	MIPI_DPHY_CSI0_RX_CLKOP
MIPI_DPHY_CSI0_RX_D2N	AL23	MIPI_DPHY_CSI0_RX_D2N
MIPI_DPHY_CSI0_RX_D2P	AK23	MIPI_DPHY_CSI0_RX_D2P
MIPI_DPHY_CSI0_RX_D3N	AL24	MIPI_DPHY_CSI0_RX_D3N
MIPI_DPHY_CSI0_RX_D3P	AK24	MIPI_DPHY_CSI0_RX_D3P



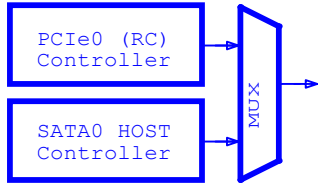
SOC_RK3576

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PCIE0/SATA0 Combo PHY0



PCIE0:Gen1/Gen2
SATA0:Gen1/Gen2/Gen3

PCIE0_REFCLKP		--	
PCIE0_REFCLKN		--	
PCIE0_TXP		SATA0_TXP	
PCIE0_TXN		SATA0_TXN	
PCIE0_RXP		SATA0_RXP	
PCIE0_RXN		SATA0_RXN	

PCIE0_SATA0_AVDD0V85

PCIE0_SATA0_AVDD1V8

1N22	>>>	PCIE0_SOC_REFCLKP
1N23	>>>	PCIE0_SOC_REFCLKN
P29	>>>	PCIE0_SOC_TXP
P28	>>>	PCIE0_SOC_TXN
R28	>>>	PCIE0_SOC_RXP
R29	>>>	PCIE0_SOC_RXN

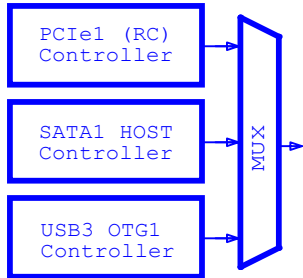
2F11

C1700
1UF 10V

2F12

C1701
100NF 10V

PCIE1/SATA1/USB3_OTG1 Combo PHY1



PCIE1:Gen1/Gen2
SATA1:Gen1/Gen2/Gen3
USB :USB3.2 Gen1x1 OTG1

PCIE1_REFCLKP		--	
PCIE1_REFCLKN		--	
PCIE1_TXP		SATA1_TXP	USB3_OTG1_SSTXP
PCIE1_TXN		SATA1_TXN	USB3_OTG1_SSTXN
PCIE1_RXP		SATA1_RXP	USB3_OTG1_SSRXP
PCIE1_RXN		SATA1_RXN	USB3_OTG1_SSRXN

PCIE1_SATA1_USB3_OTG1_AVDD0V85

PCIE1_SATA1_USB3_OTG1_AVDD1V8

1L23	>>>	USB3_OTG1_SSTXP
1M23	>>>	USB3_OTG1_SSTXN
N28	>>>	USB3_OTG1_SSRXP
N29	>>>	USB3_OTG1_SSRXN
M28	>>>	USB3_OTG1_SSRXP
M29	>>>	USB3_OTG1_SSRXN

2E11

C1702
4.7UF 10V

2E12

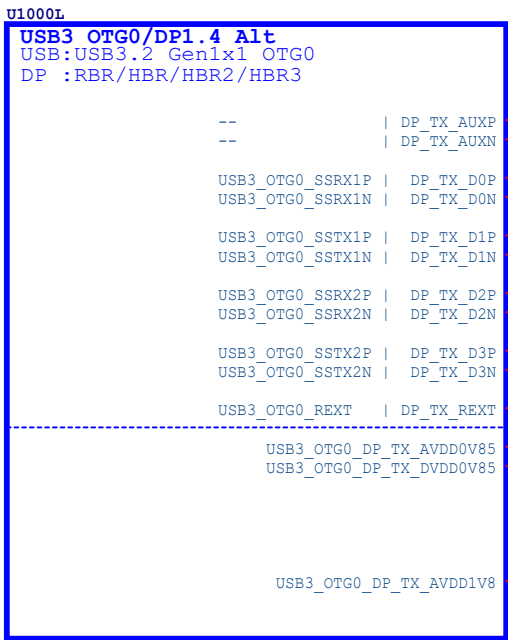
C1703
1UF 10V

C1704
4.7UF 10V

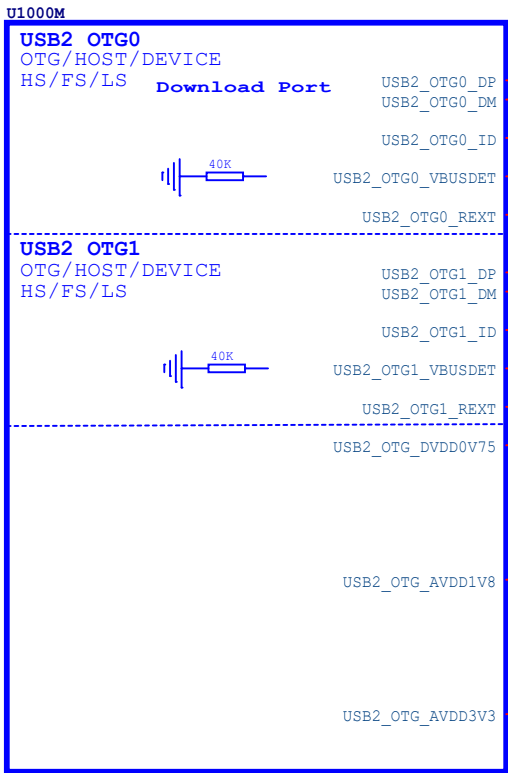
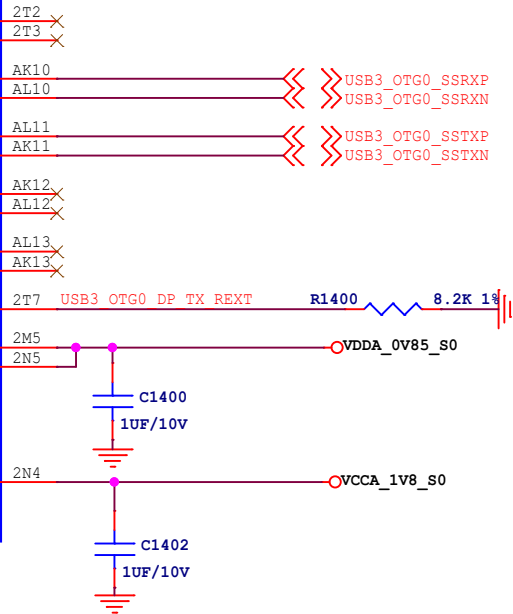
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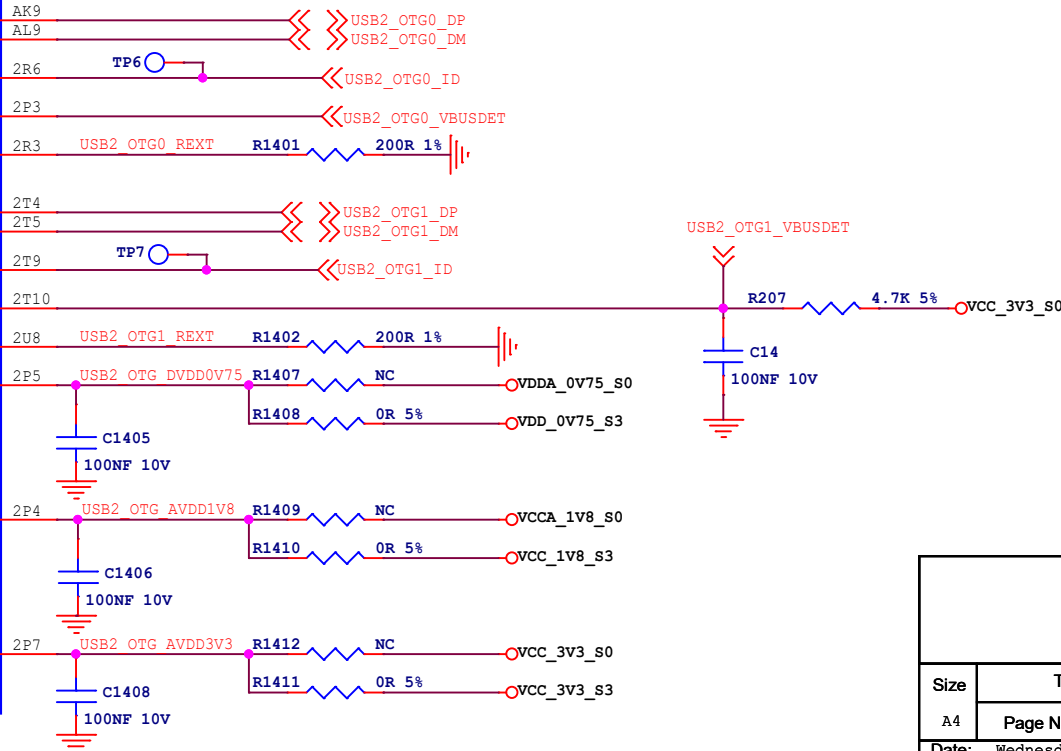
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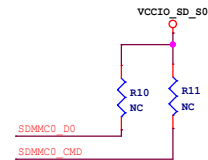
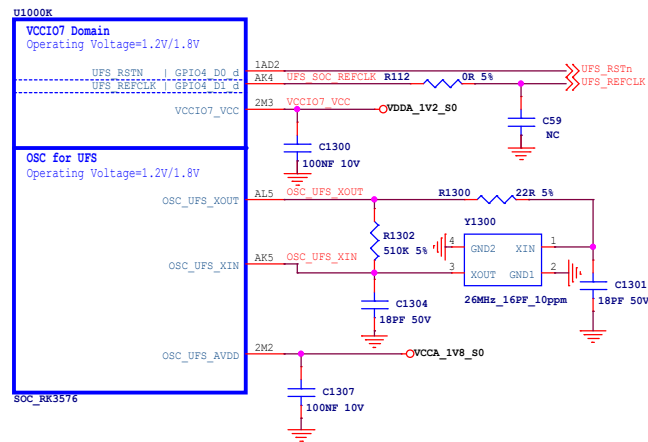
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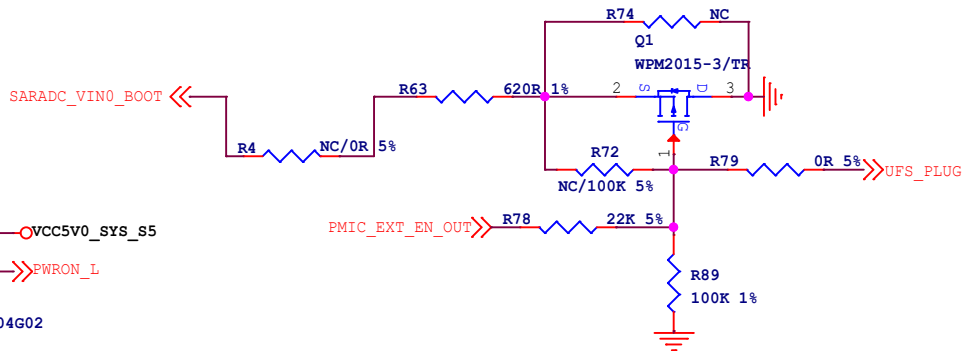
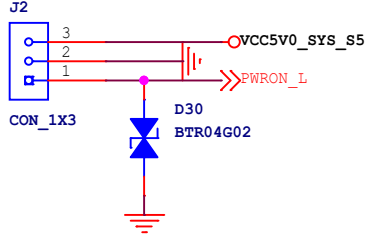
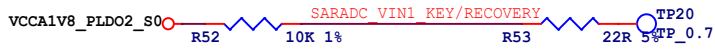
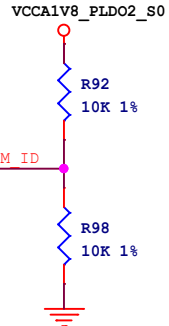
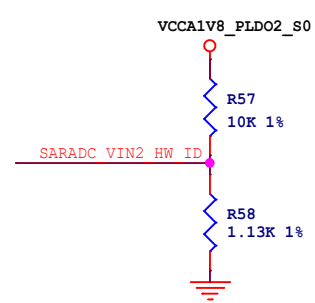
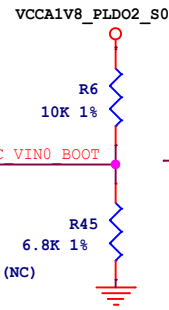
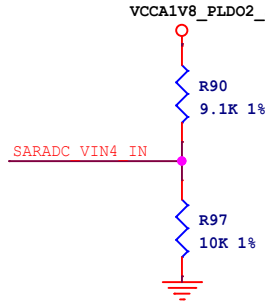
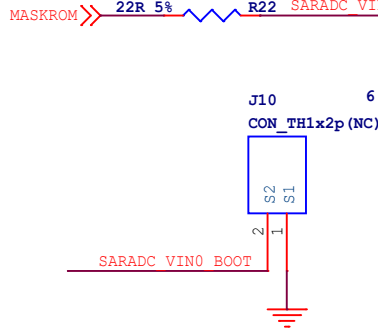
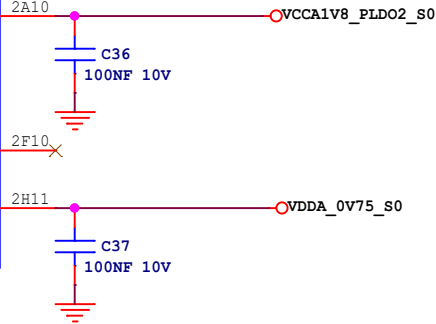
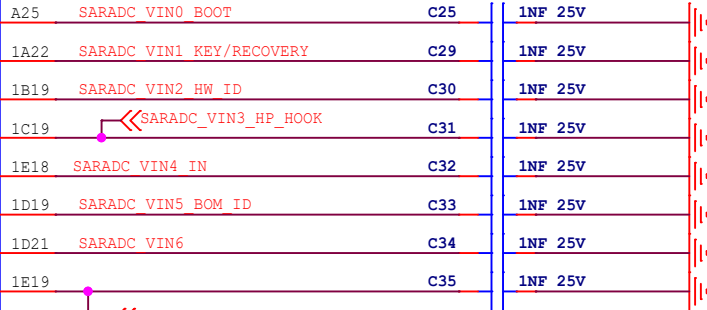
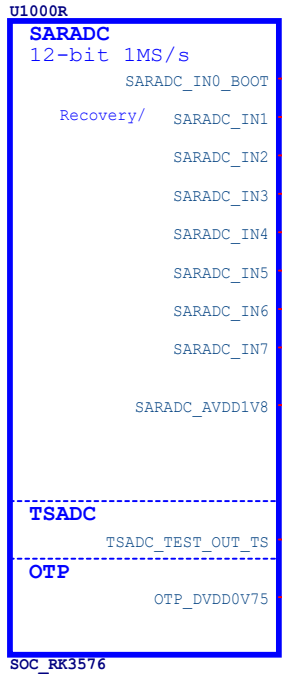


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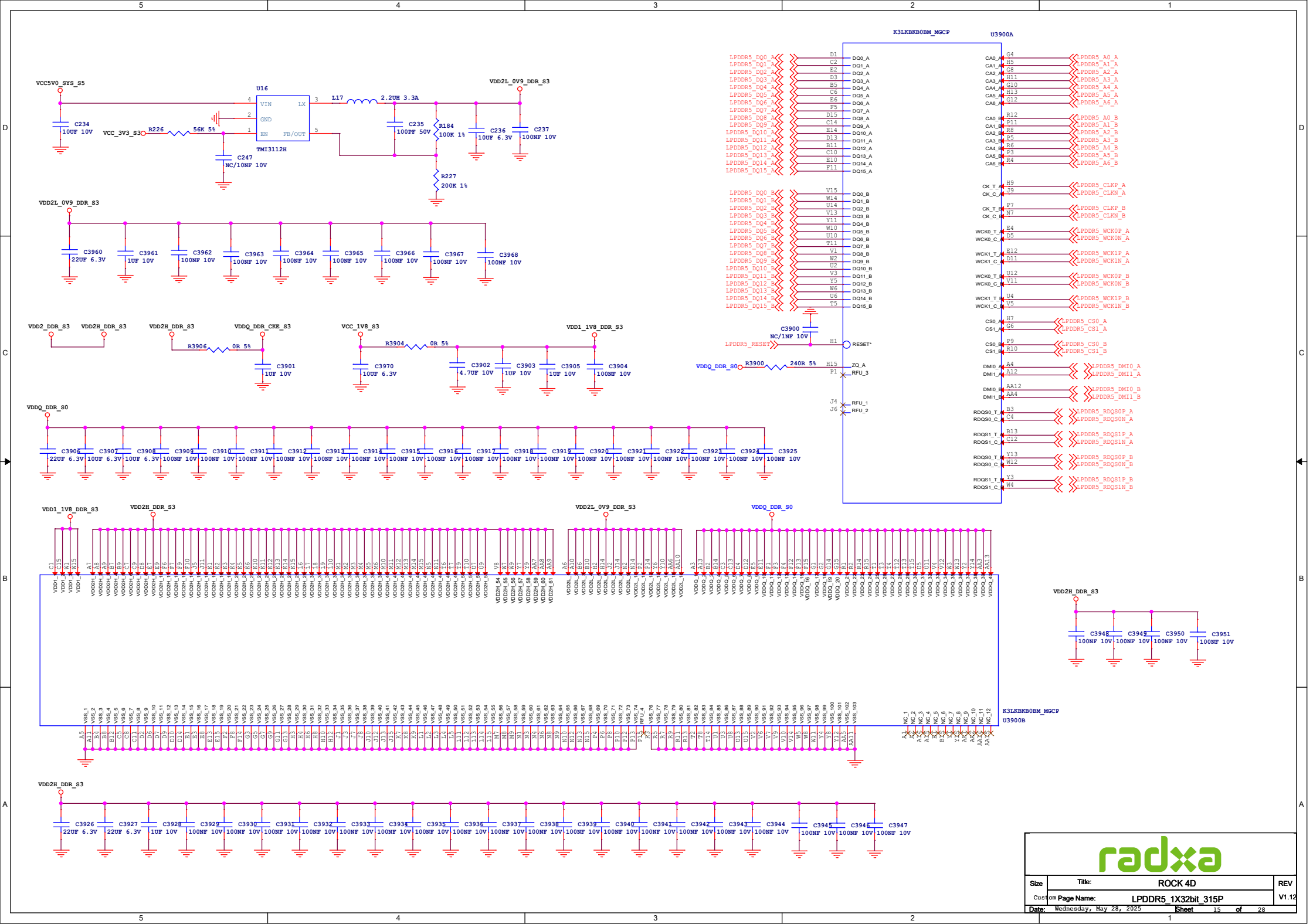


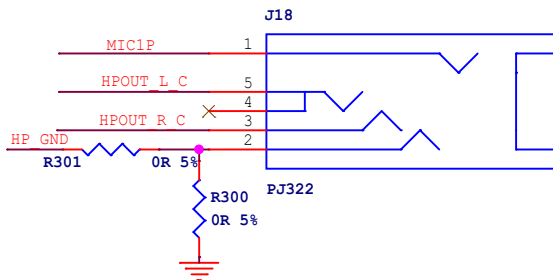
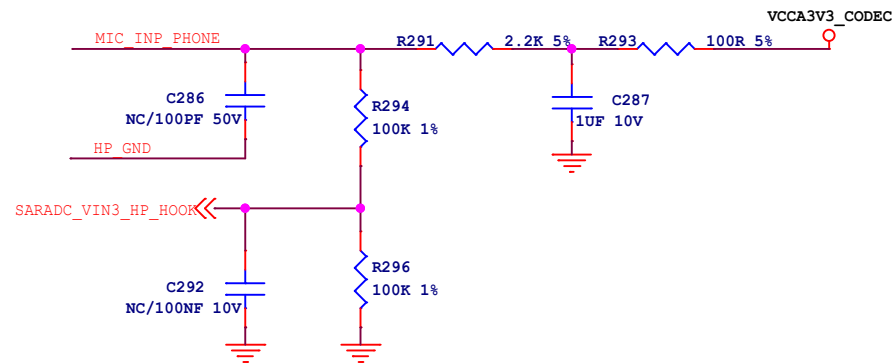
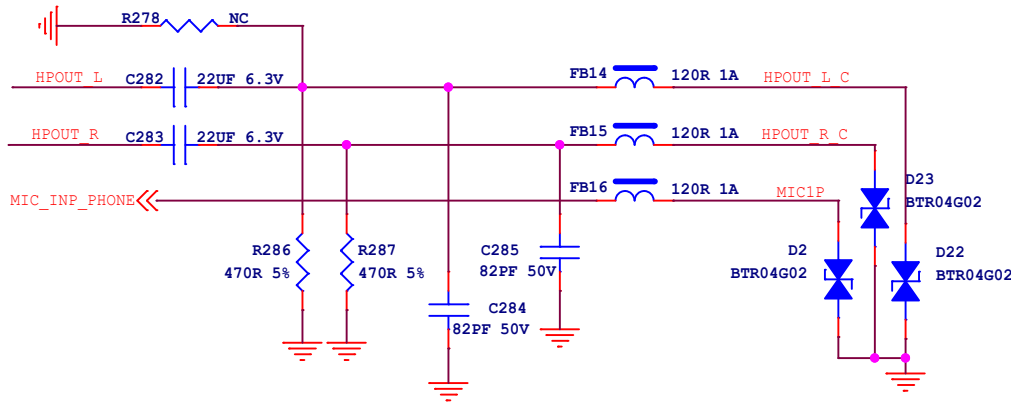
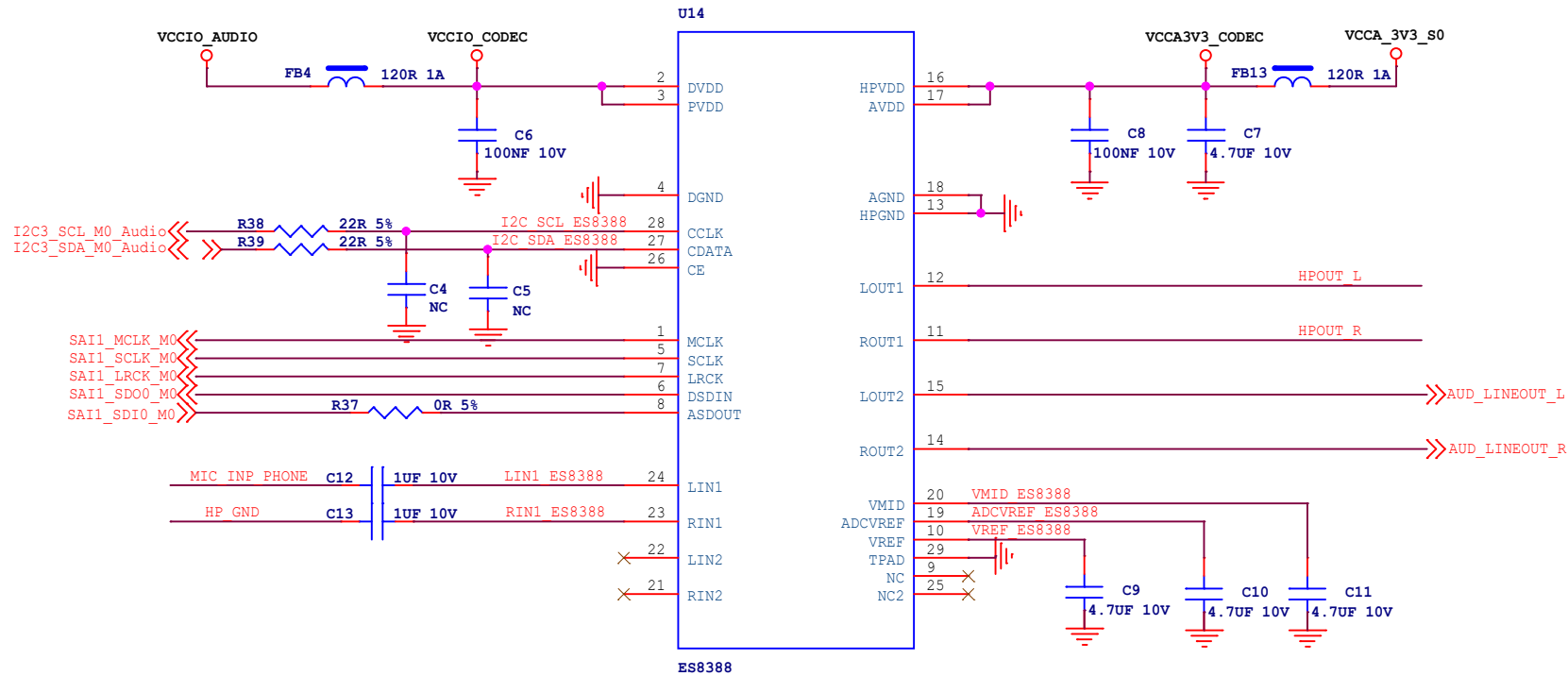
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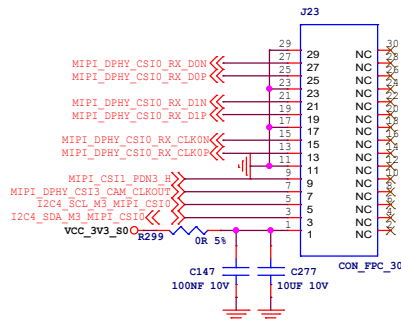
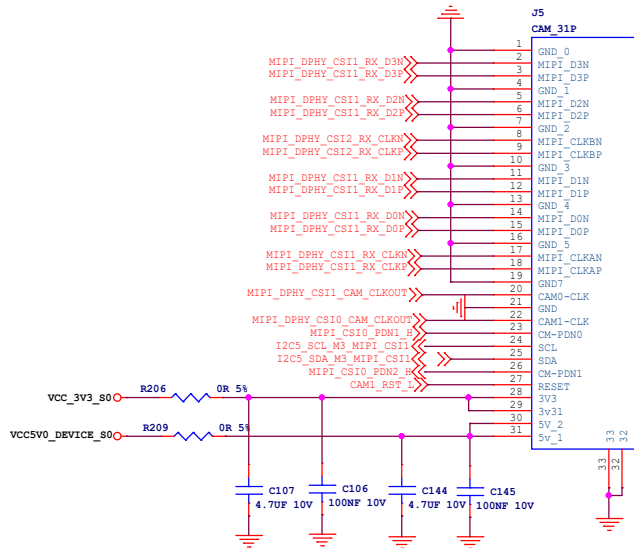
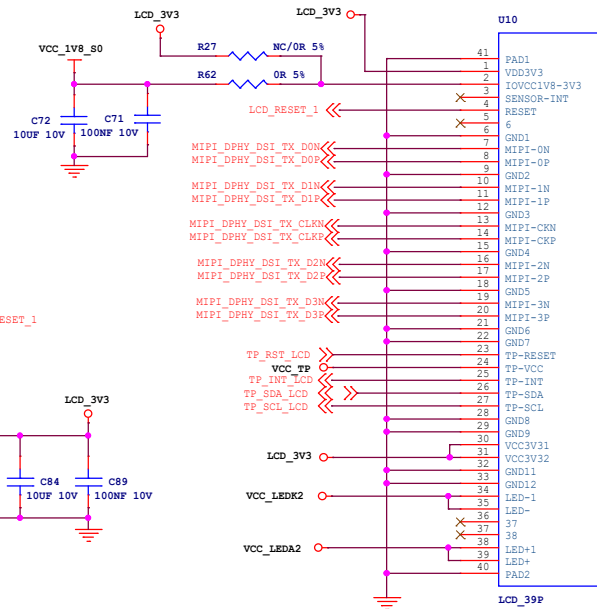
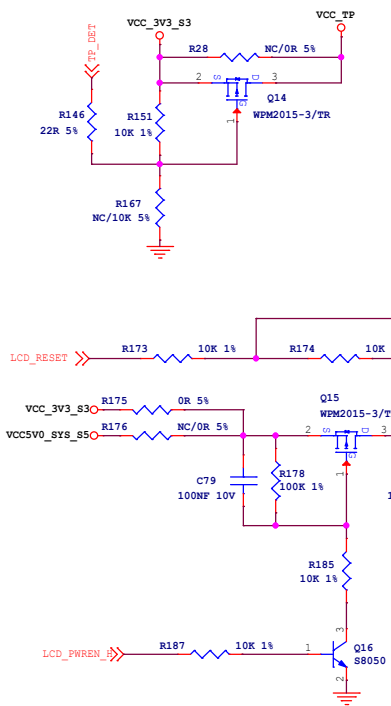
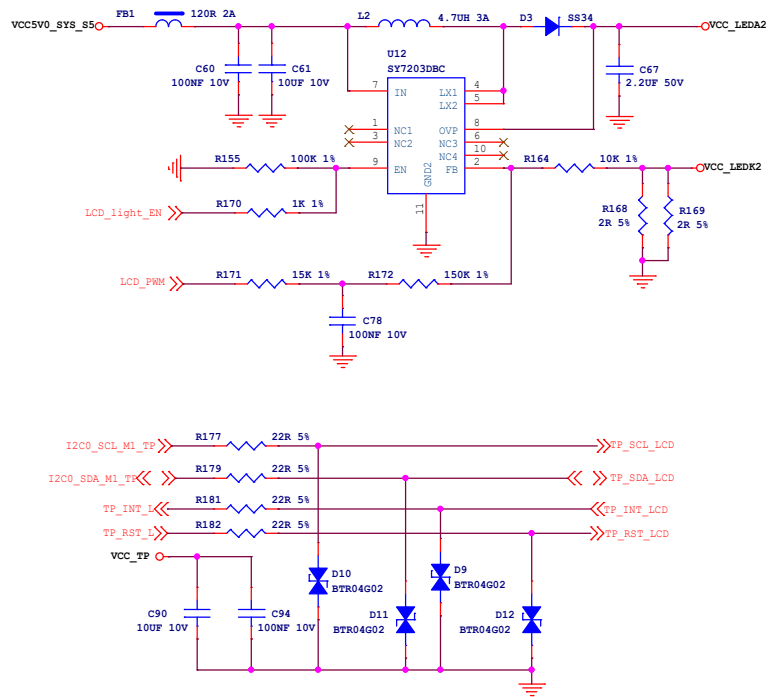
VCCIO4 Domain
Operating Voltage=1.8V/3.3V

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--	--	I2C4_SDA_M2	--	UART8_RX_M1	--	SA10_SDT0_M0	ETH0_RXD1_M1	SDMMC1_D1_M1	VI_CIF_D14	GPIO2_A7_D	B20
--	--	--	--	UART1_TX_M1	PD00_SDI3_M3	SA10_SDI0_M0	ETH0_TXD0_M1	SDMMC1_D2_M1	VI_CIF_D13	GPIO2_B0_D	B19
--	--	--	--	UART1_RX_M1	PD00_SDI2_M3	SA10_SDI1_M0	ETH0_TXD1_M1	SDMMC1_D3_M1	VI_CIF_D12	GPIO2_B1_D	IA18
--	--	PCIE0_CLKREQ0_M0	SPI1_CS_N0_M2	UART1_CTSN_M1	PD00_SDI1_M3	SA10_SDT0_M0	ETH0_TXD2_M1	SDMMC1_D4_M1	VI_CIF_D11	GPIO2_B2_D	IA17
--	--	PCIE0_CLKREQ0_M0	SPI1_CLK_M3	UART1_RTSN_M1	PD00_CLK1_M3	SA10_SDT0_M0	ETH0_RXD0_M1	SDMMC1_D5_M1	VI_CIF_D10	GPIO2_B3_D	IB16
--	--	SATA0_ACMLED_M0	SPI1_MOSI_M1	UART1_CTSN_M1	PD00_SDI0_M3	SA10_SDT0_M0	ETH0_RXD1_M1	SDMMC1_D6_M1	VI_CIF_D9	GPIO2_B4_D	IC18
--	--	SATA1_ACMLED_M0	SPI1_MISO_M1	UART1_RTSN_M1	PD00_CLK0_M3	SA10_MCLK_M0	ETH0_RXD2_M1	SDMMC1_D7_M1	VI_CIF_D8	GPIO2_B5_D	A21
--	--	--	--	UART7_TX_M0	--	SA10_SCLK_M0	ETH0_RXD3_M1	ETH1_PTP_REFCLK_M1	VI_CIF_D7	GPIO2_B6_D	B21
--	--	--	--	UART7_RX_M0	--	SA10_LCK0_M0	ETH0_RXD4_M1	ETH1_PTP_REFCLK_M1	VI_CIF_D6	GPIO2_B7_D	IA17
--	--	--	--	UART9_RX_M0	PD01_SDI1_M0	--	ETH0_PTP_REFCLK_M1	ETH1_RXD2_M0	VI_CIF_D5	GPIO2_C0_D	IA15
--	--	--	--	UART9_TX_M0	PD01_CLK1_M0	SA12_MCLK_M0	ETH0_PTP_REFCLK_M1	ETH1_RXD3_M0	VI_CIF_D4	GPIO2_C1_D	ID15
--	--	--	--	UART11_CTSN_M1	PD01_SDI2_M0	SA12_SCLK_M0	ETH0_PTP_REFCLK_M1	ETH1_RXD4_M0	VI_CIF_D3	GPIO2_C2_D	IA15
--	--	--	--	UART11_RTSN_M1	PD01_SDI3_M0	SA12_LCK0_M1	ETH0_PTP_REFCLK_M1	ETH1_RXD5_M0	VI_CIF_D2	GPIO2_C3_D	IA13
--	--	--	--	SPI1_CS_N0_M1	PD01_SDI0_M0	SA12_SCLK_M0	ETH0_PTP_REFCLK_M1	ETH1_RXD6_M0	VI_CIF_D1	GPIO2_C4_D	IC15
--	--	--	--	SPI1_CLK_M1	PD01_CLK0_M0	SA12_SDI1_M0	ETH0_PTP_REFCLK_M1	ETH1_RXD7_M0	VI_CIF_D0	GPIO2_C5_D	IA14
--	--	--	--	UART4_CTSN_M0	--	SA12_SCLK_M0	ETH0_PTP_REFCLK_M1	ETH1_RXD8_M0	--	GPIO2_C6_D	B15
--	--	--	--	UART4_RTSN_M0	--	SA12_LCK0_M0	ETH0_PTP_REFCLK_M1	ETH1_RXD9_M0	--	GPIO2_C7_D	B16
--	--	--	--	UART4_TX_M0	--	SA14_SDI_M3	ETH1_TXD0_M0	--	--	GPIO2_D0_D	IA16
--	--	--	--	UART4_RX_M0	--	SA12_SDI0_M0	ETH1_TXD1_M0	--	--	GPIO2_D1_D	B17
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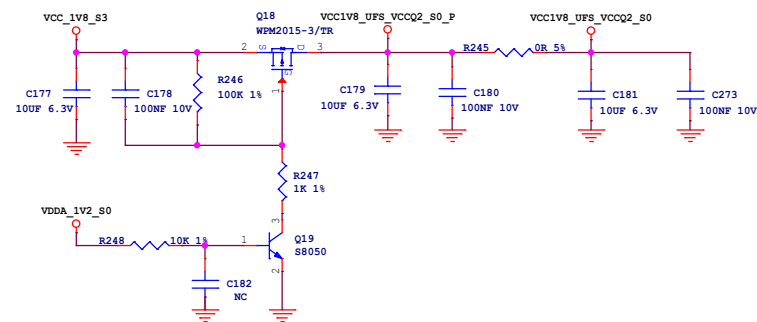
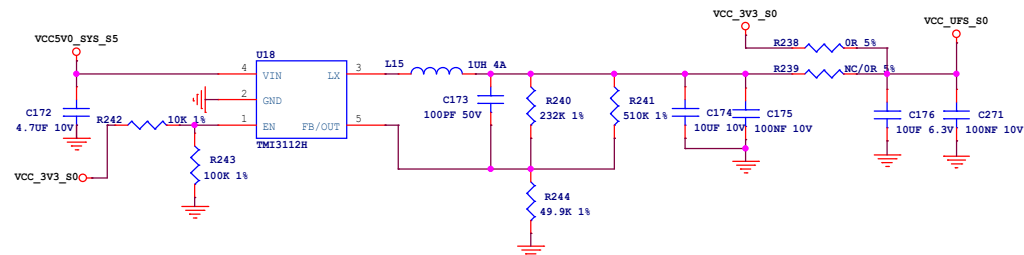


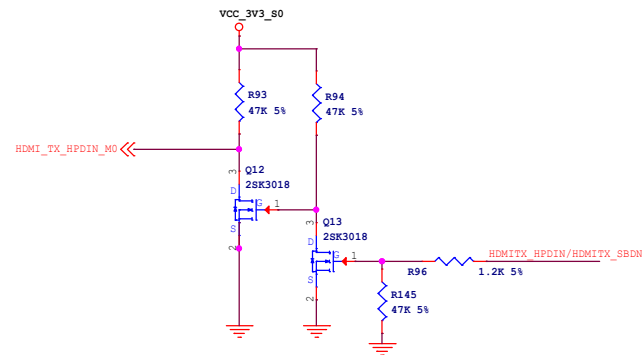
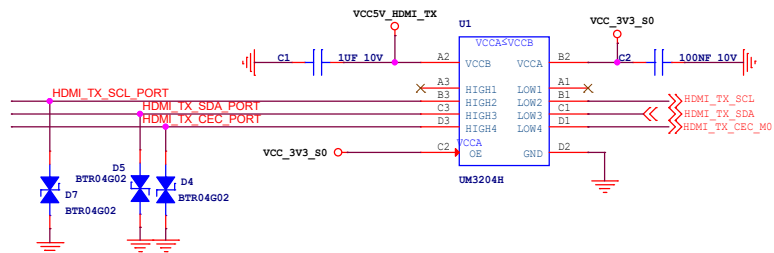
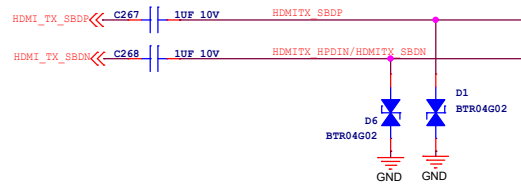
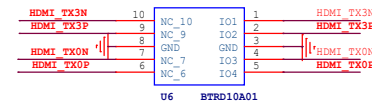
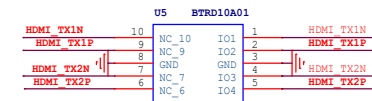
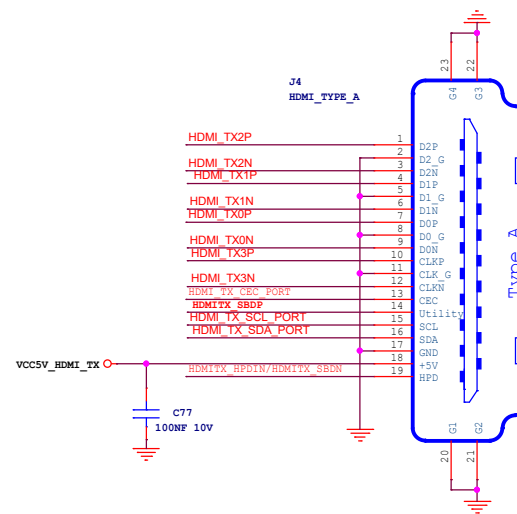
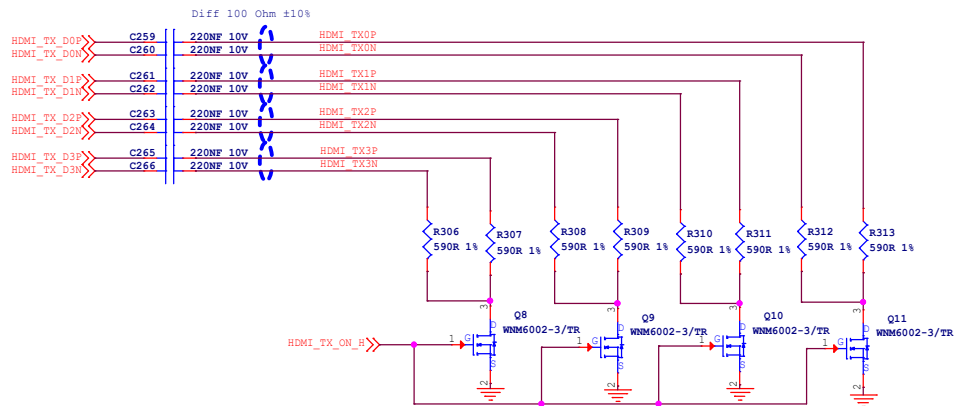


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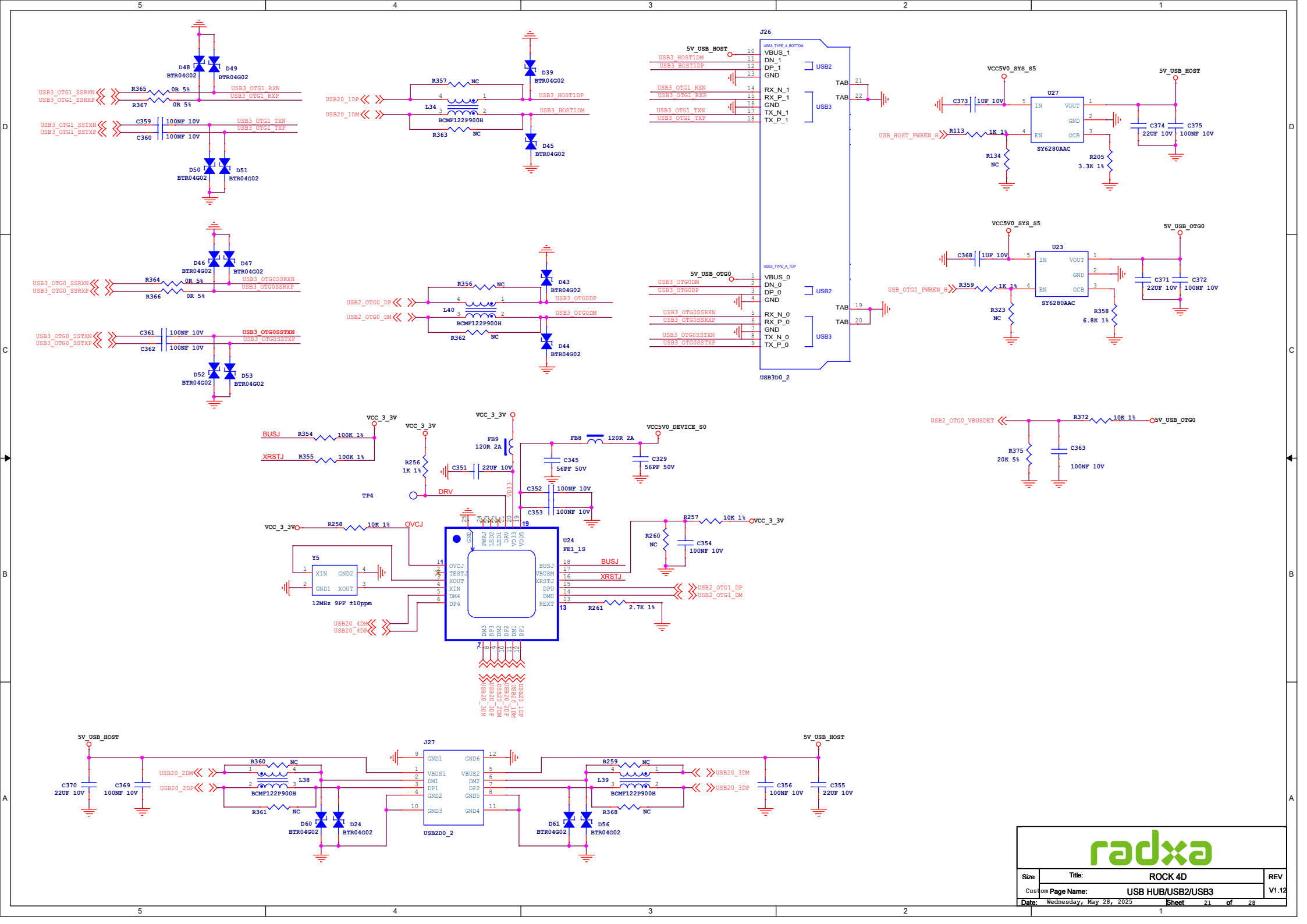


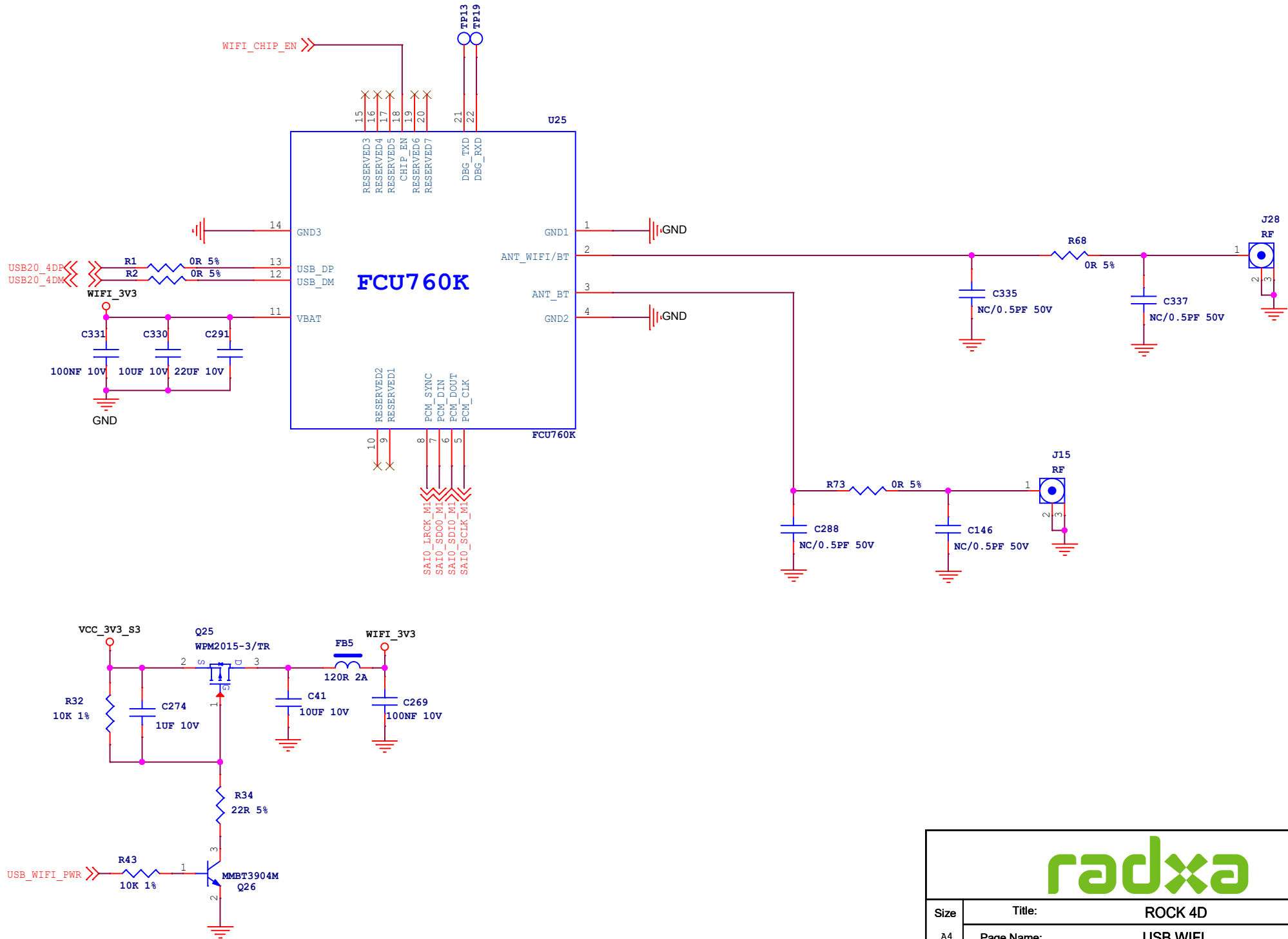
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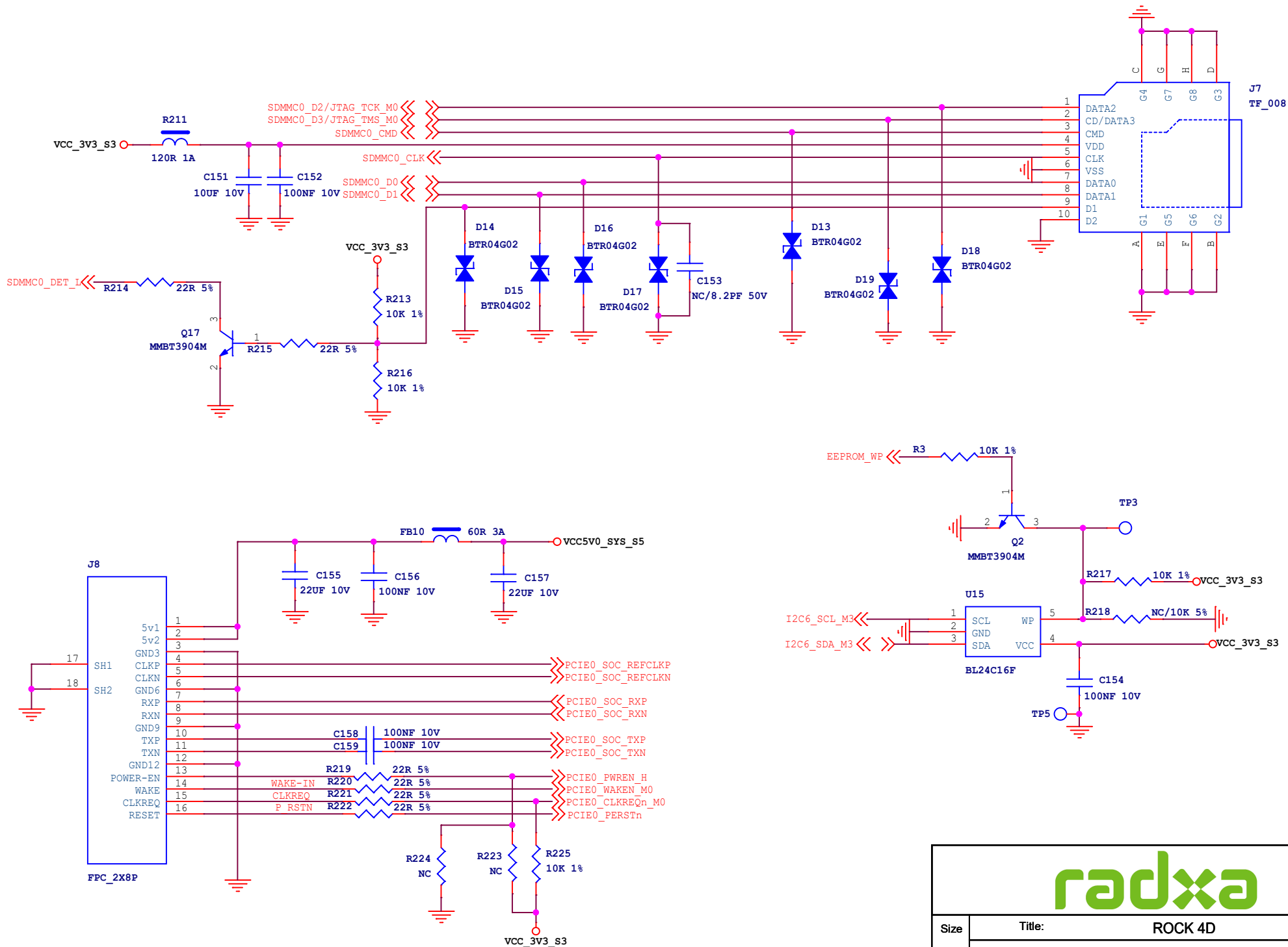
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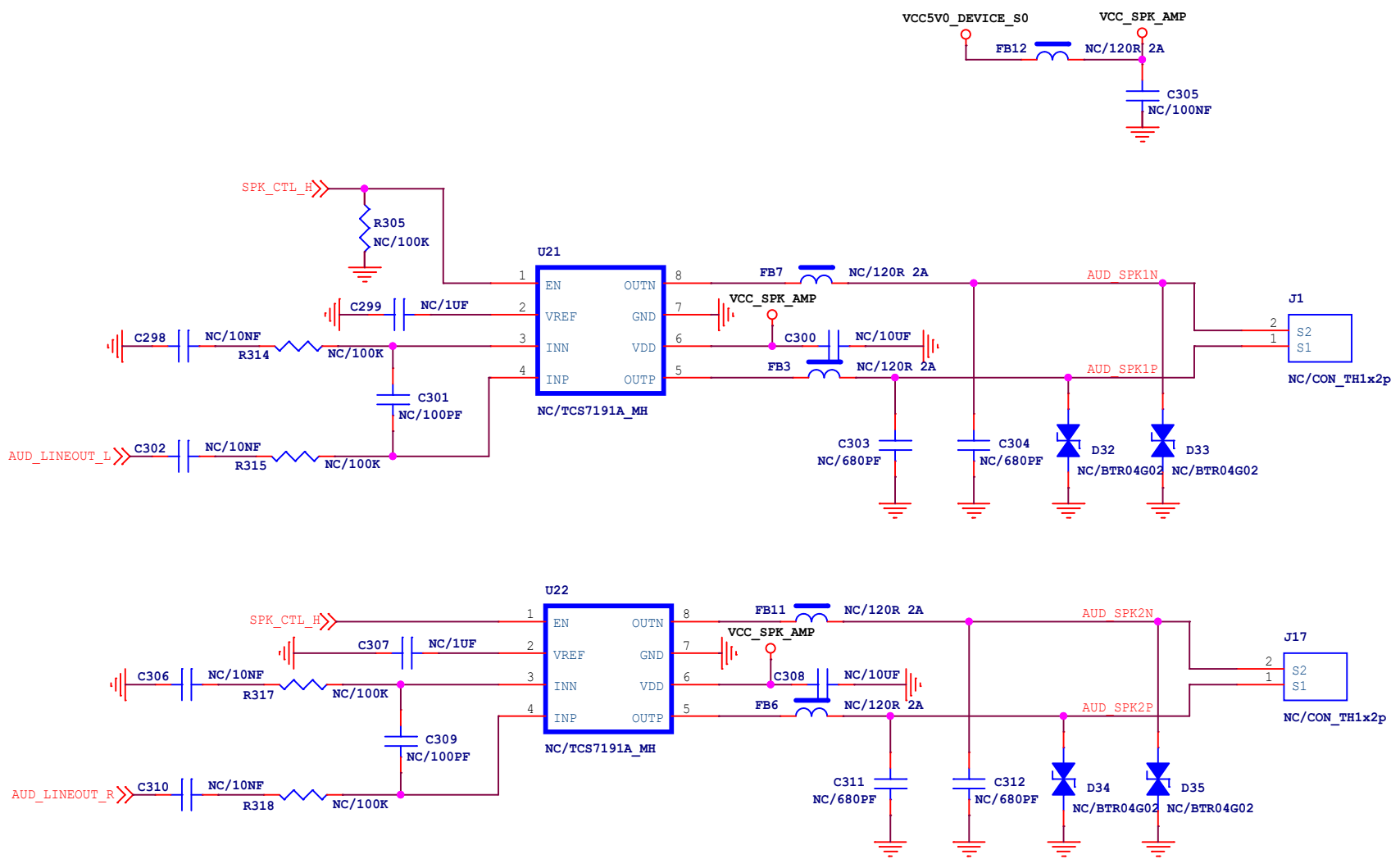
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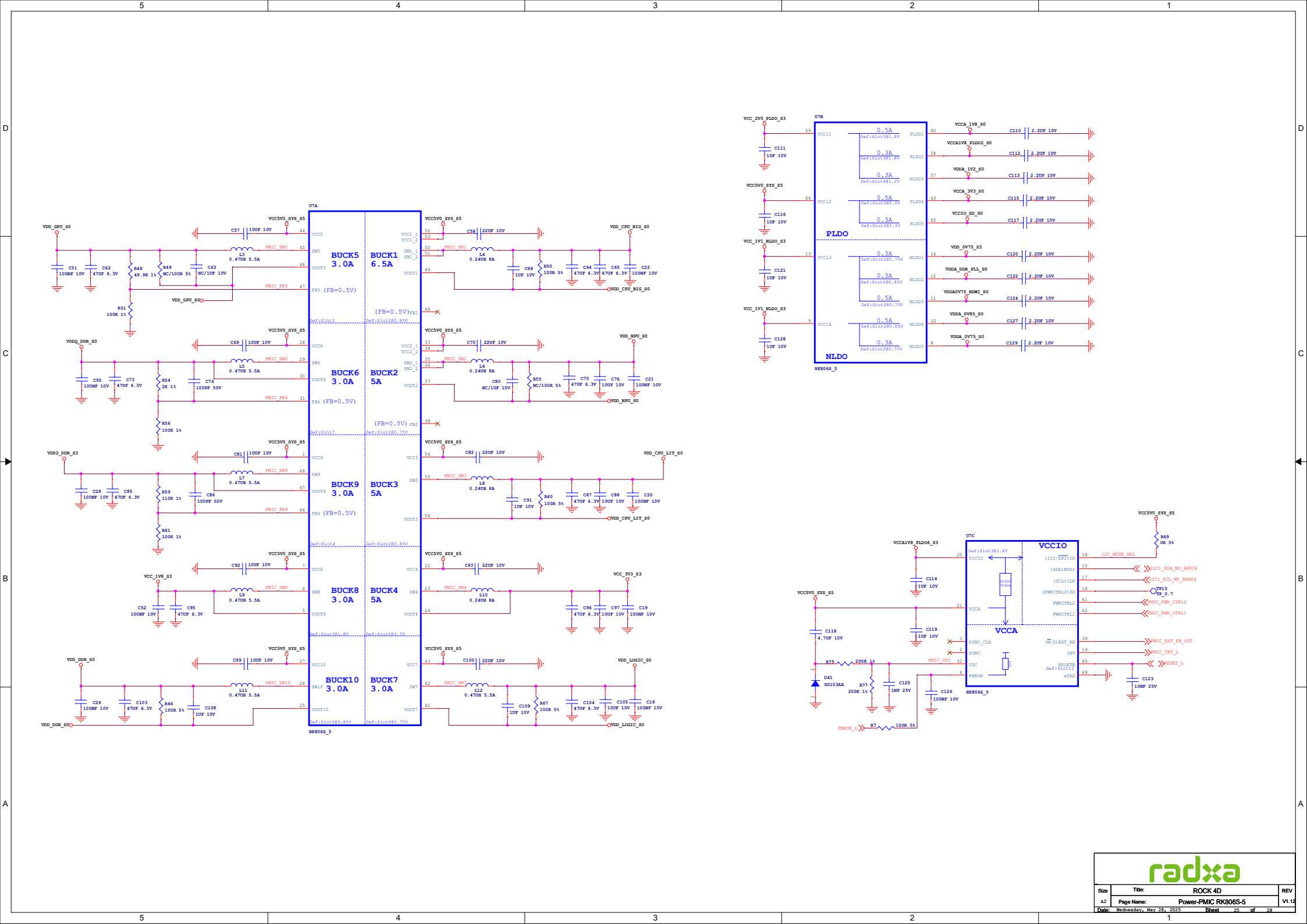
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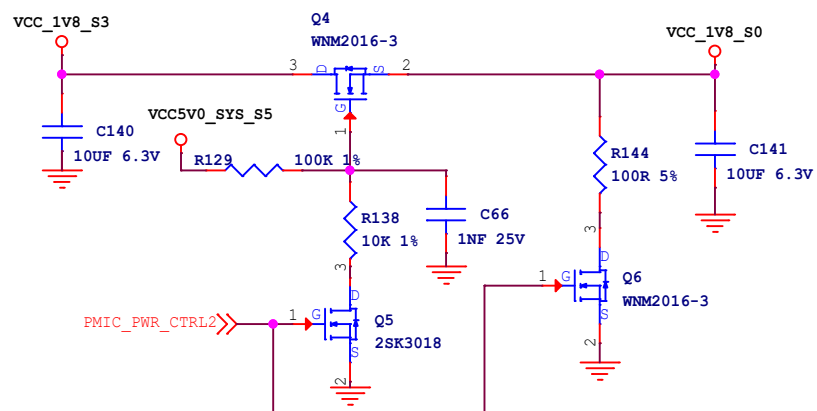
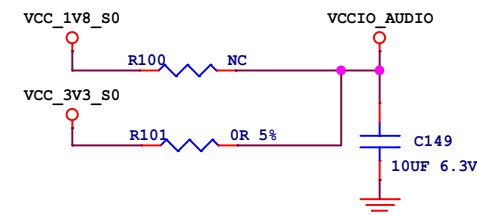
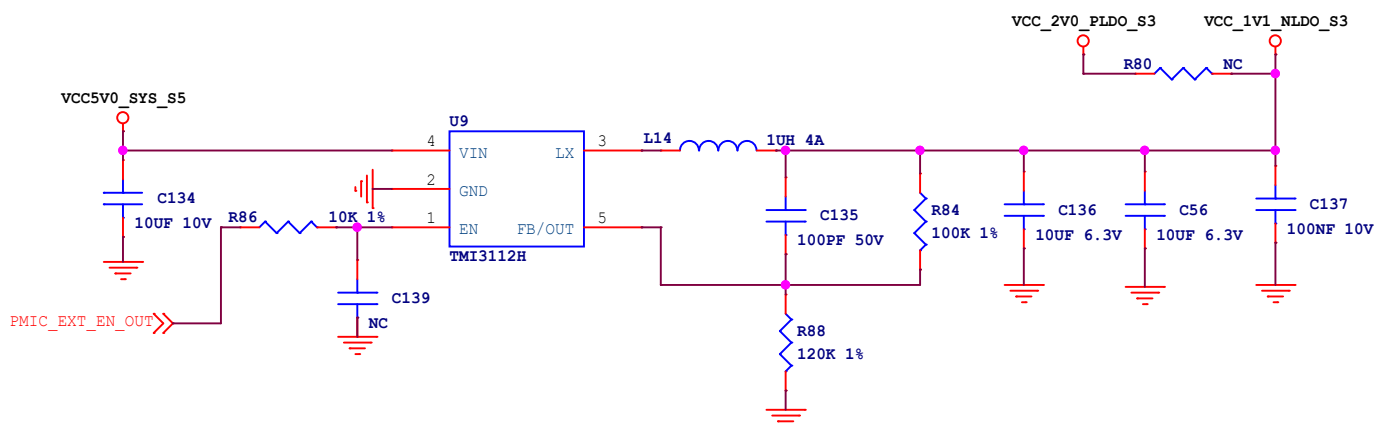
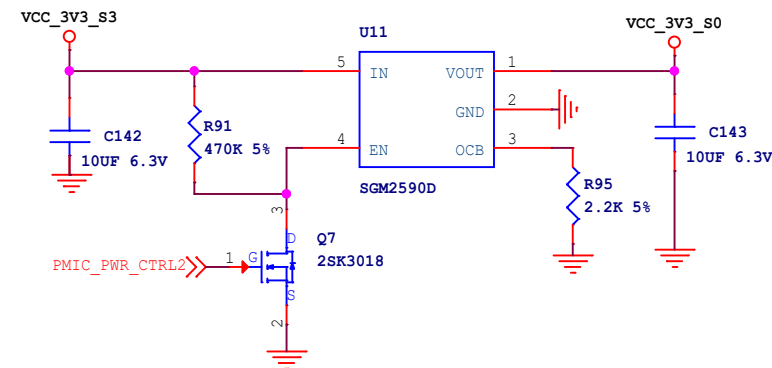
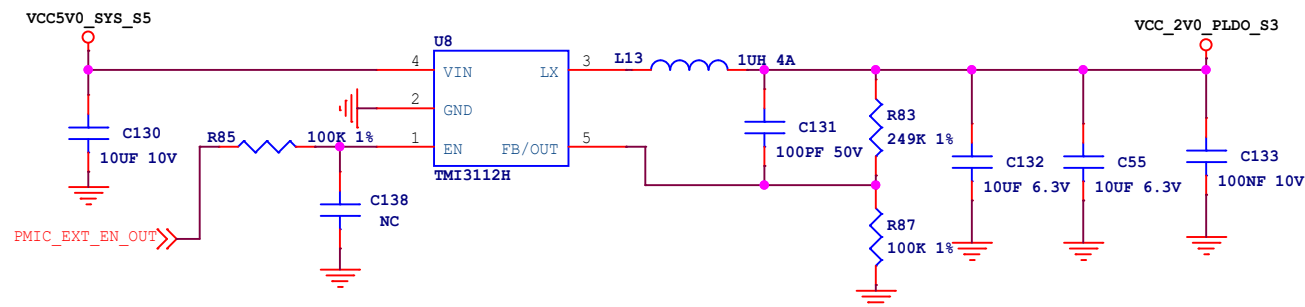


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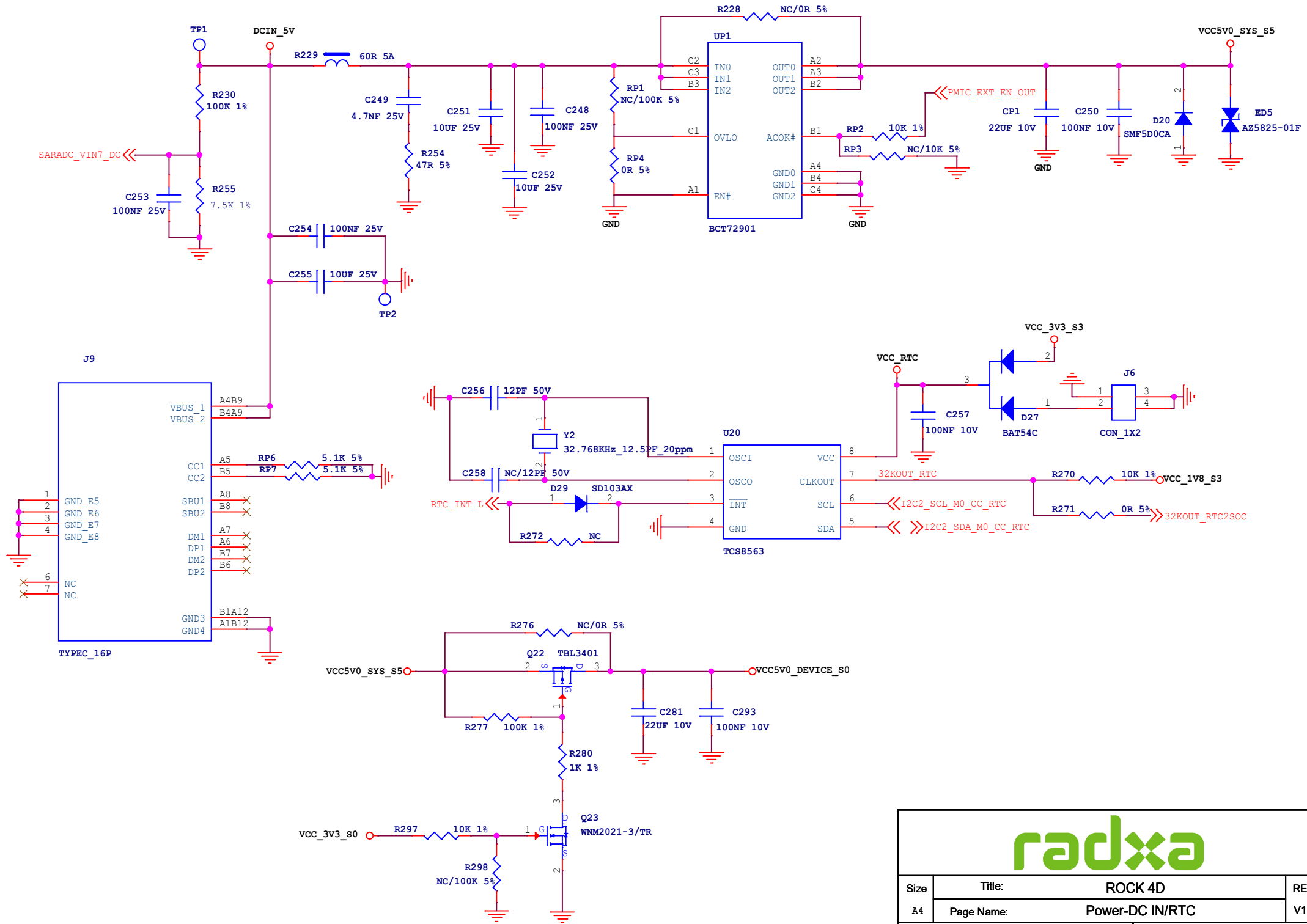


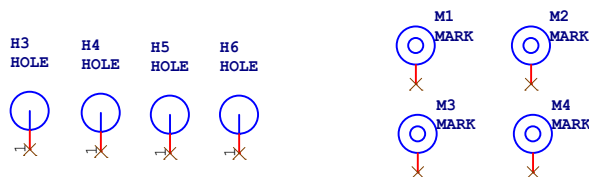
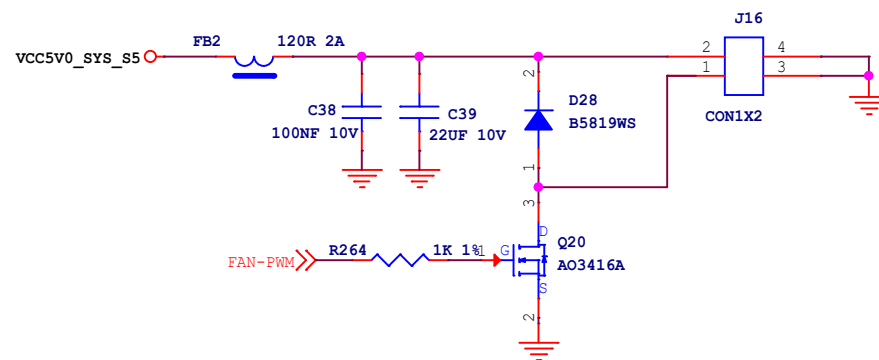
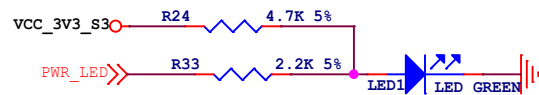
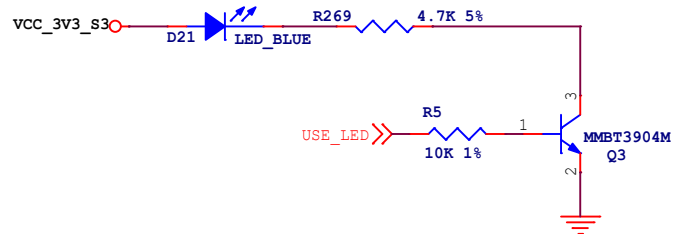
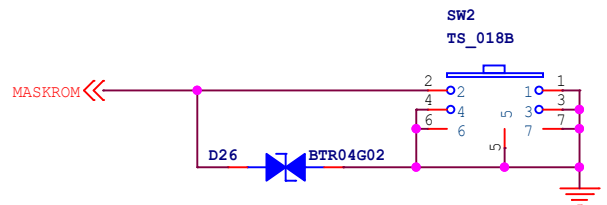
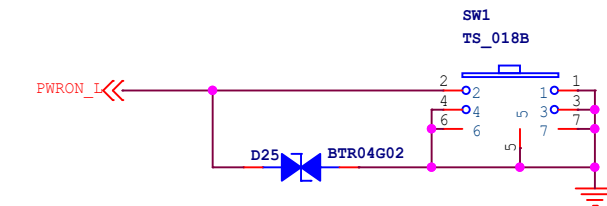
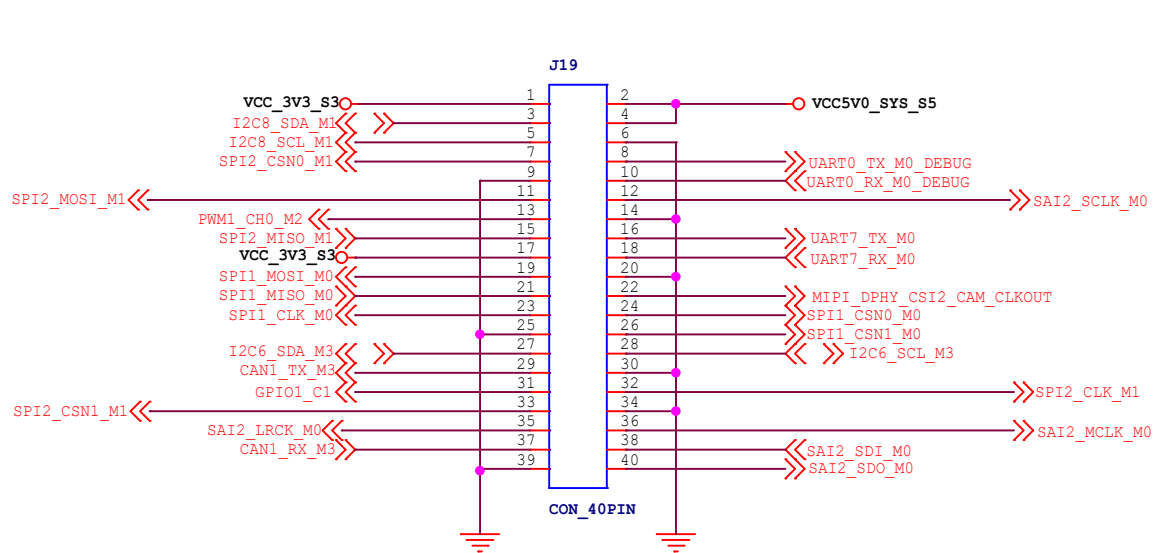




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