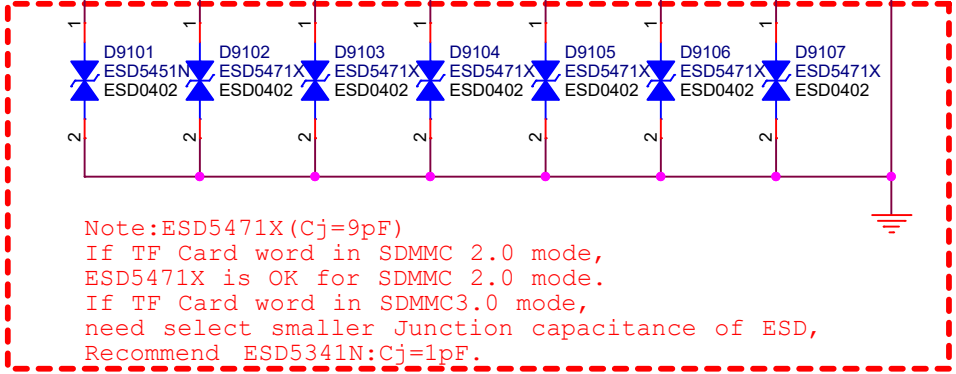
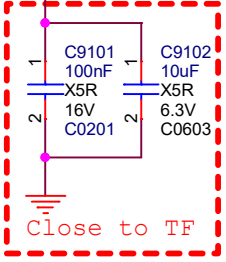
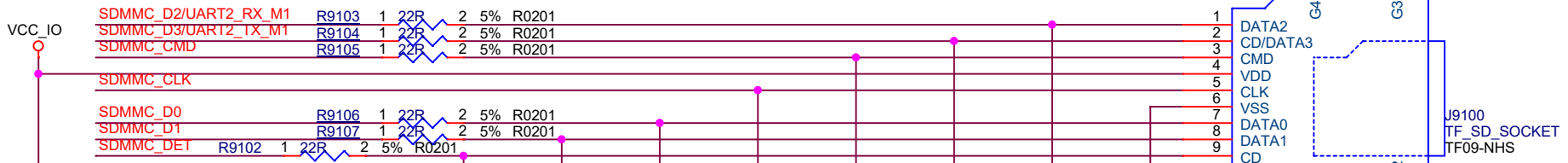
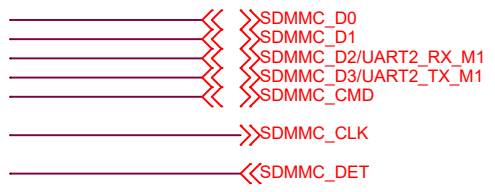


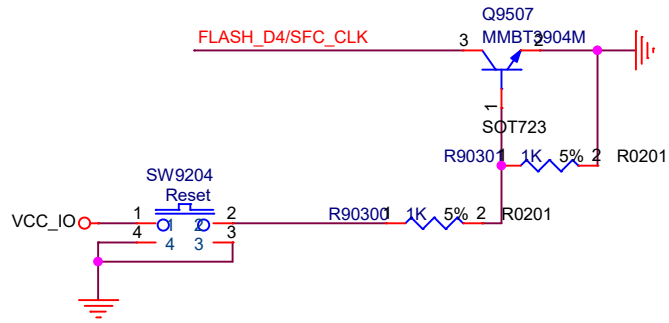
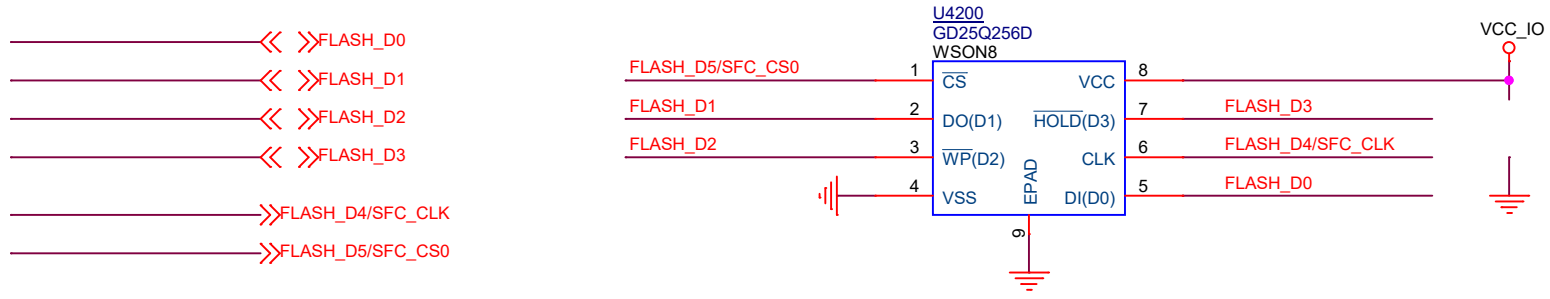
# TF Card



Note:ESD5471X(Cj=9pF)  
 If TF Card word in SDMMC 2.0 mode,  
 ESD5471X is OK for SDMMC 2.0 mode.  
 If TF Card word in SDMMC3.0 mode,  
 need select smaller Junction capacitance of ESD,  
 Recommend ESD5341N:Cj=1pF.

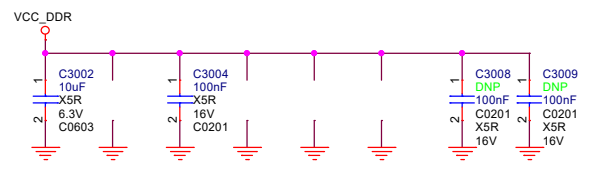
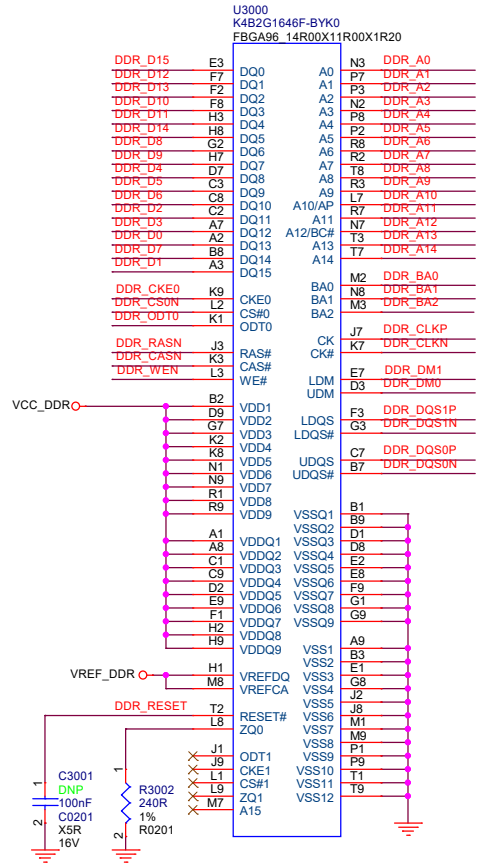
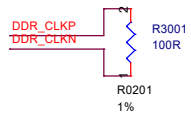
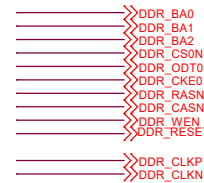
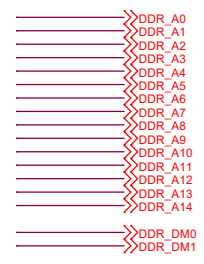
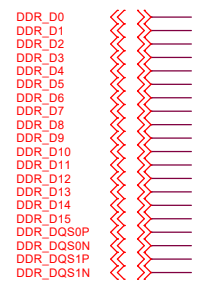
# SPI Flash

Remind: Refer to the latest AVL for parts selection.

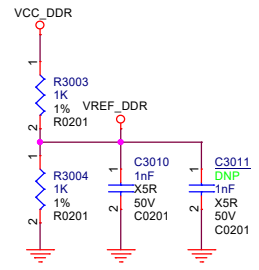


# DDR3 1x16bit

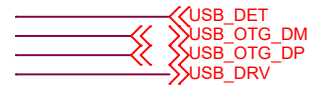
Remind: Refer to the latest AVL for parts selection.



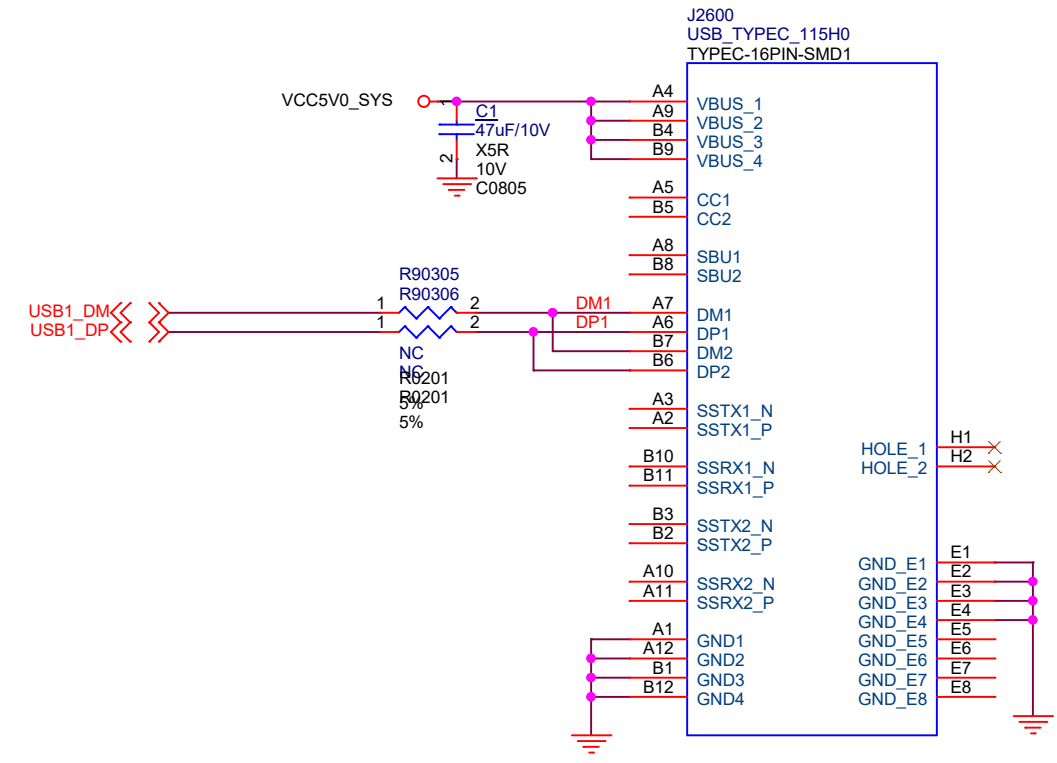
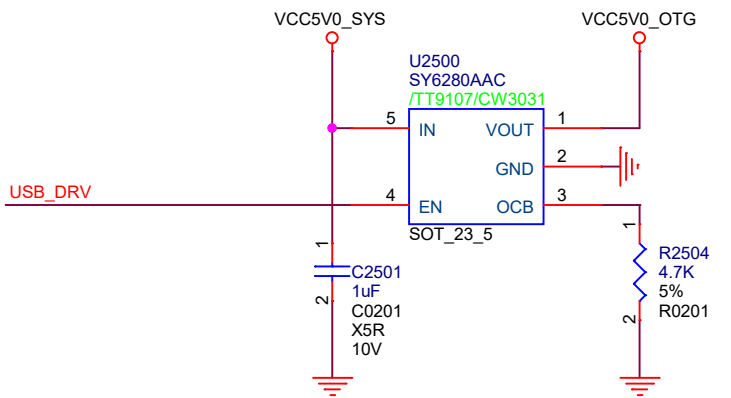
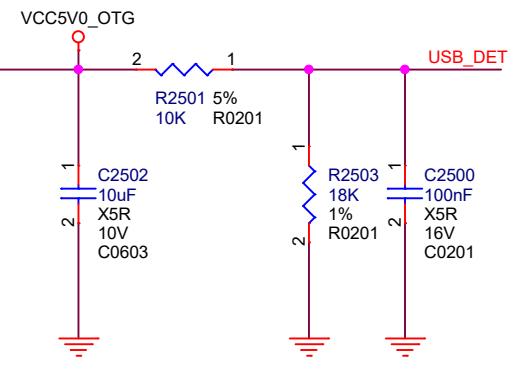
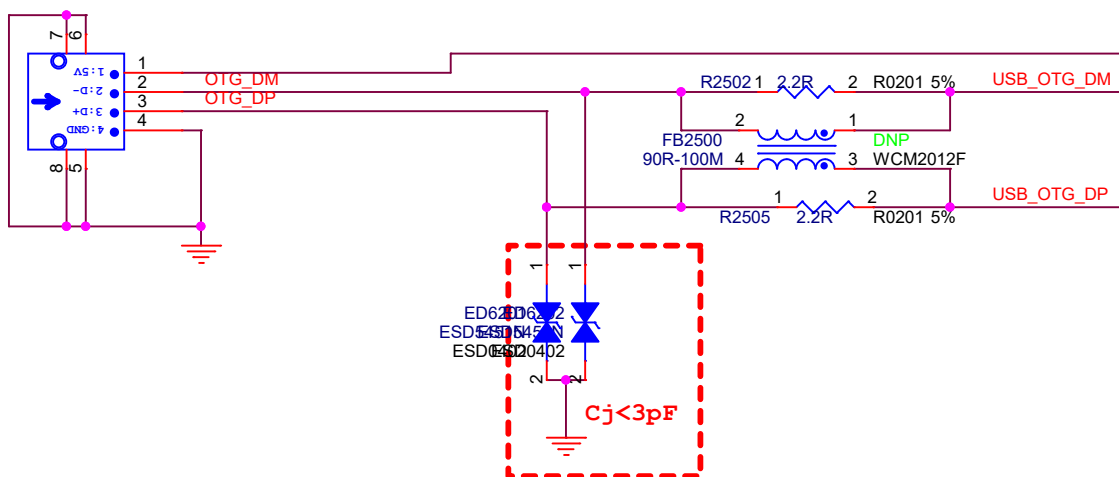
Note: All the Power filter capacitors should be placed close to the power pins of DDR3



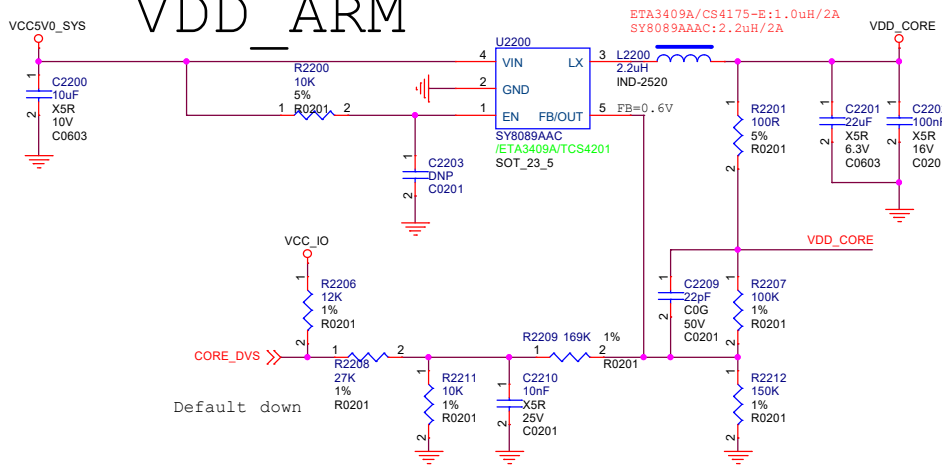
# USB OTG



USB-HUB\_PORTBA  
USB2500  
USB\_L



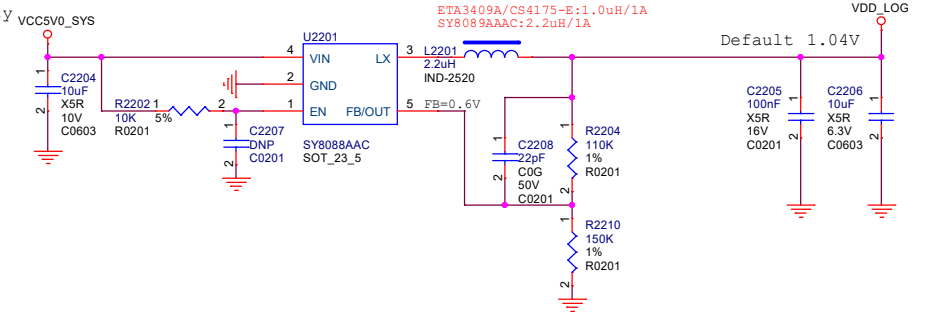
# VDD\_ARM



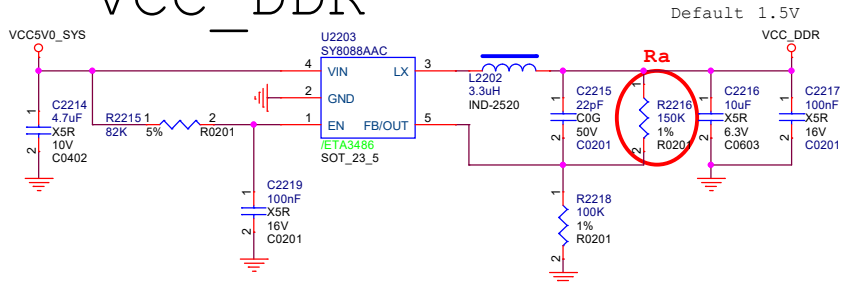
$$V = V_{max} - (V_{max} - V_{min}) * Duty$$

Default 1.015V  
0.827-1.34V

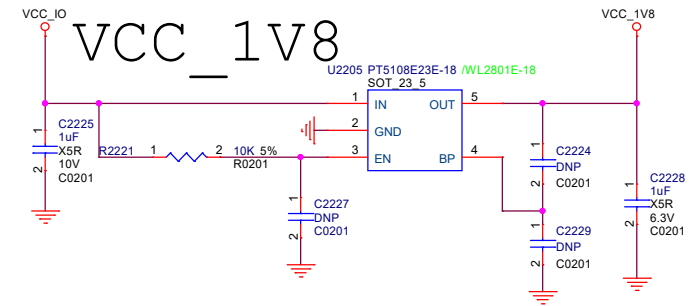
# VDD\_LOG



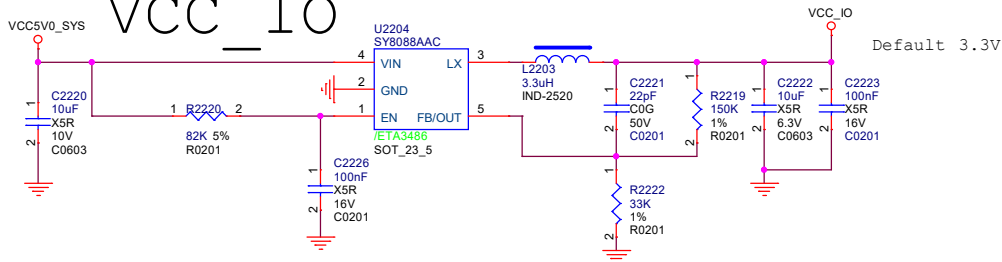
# VCC\_DDR



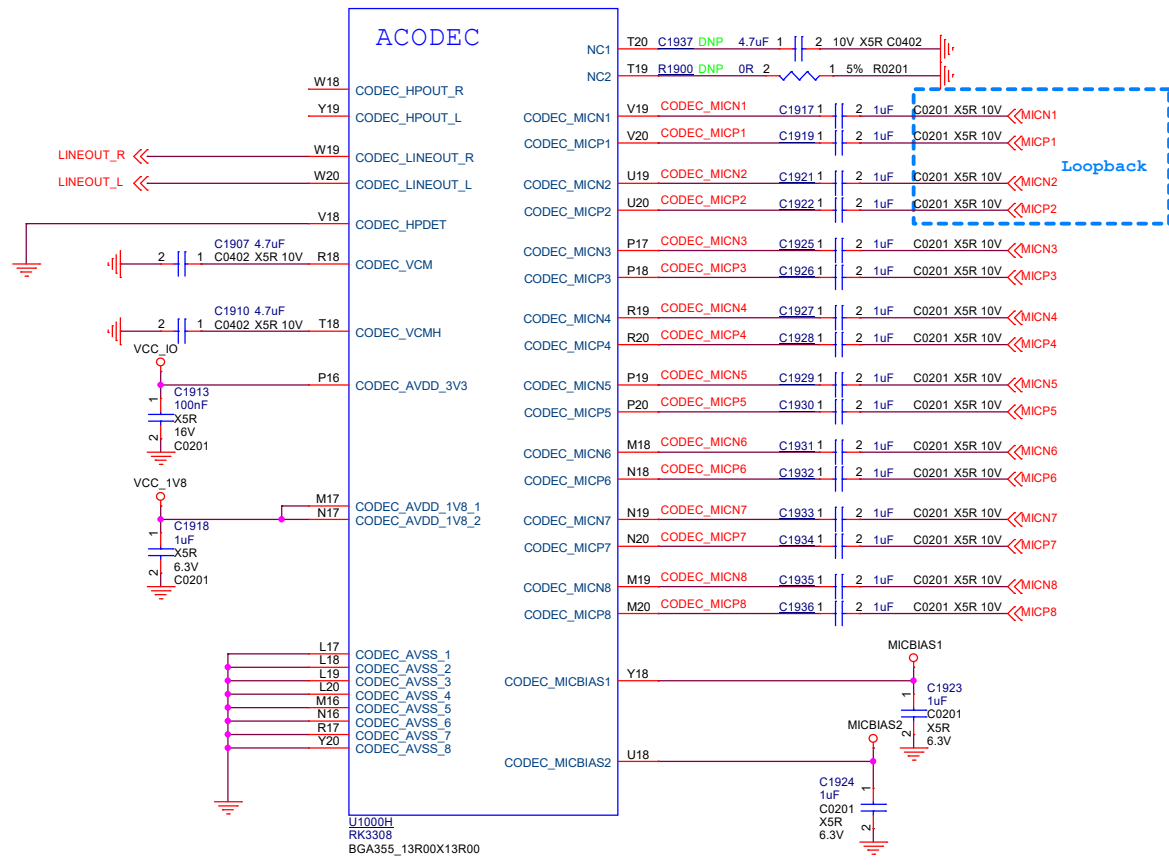
# VCC\_1V8



# VCC\_IO



# RK3308 Part-H

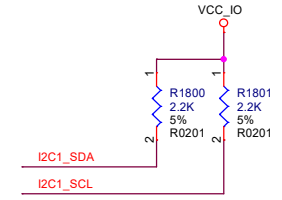
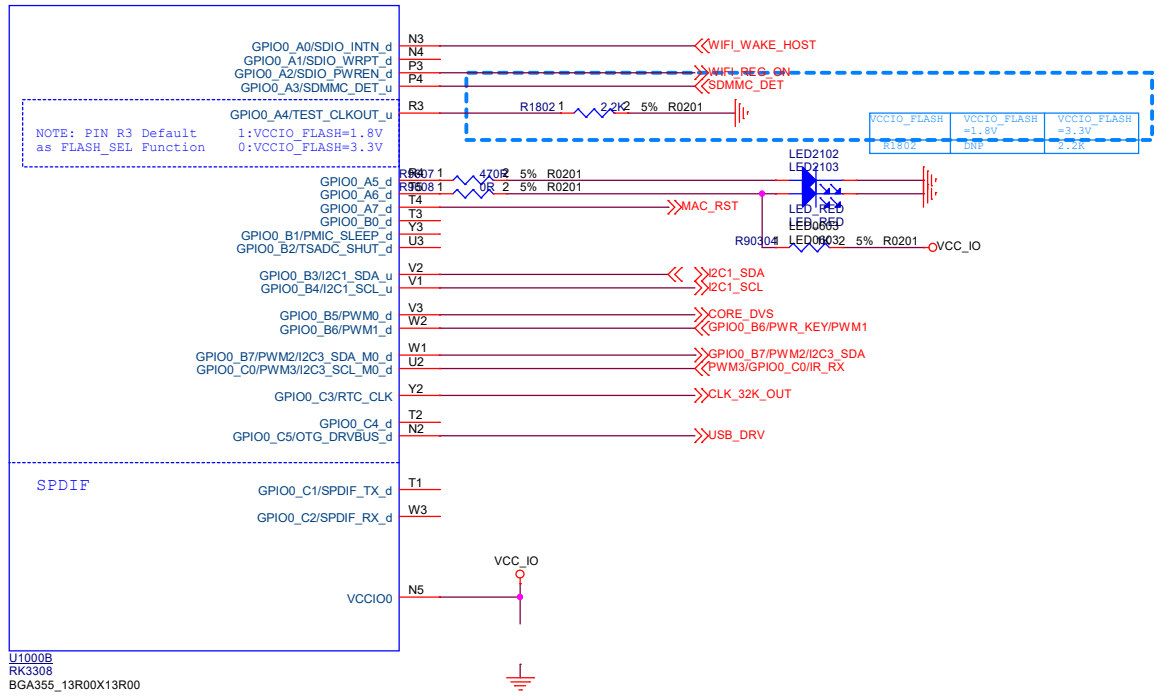


Application scene	MIC IN Channel	Loopback Channel
6MIC IN+ 2Speaker OUT	MIC3~MIC8	MIC1~MIC2
6MIC IN+ 1Speaker OUT	MIC3~MIC8	
5MIC IN+ 2Speaker OUT	MIC4~MIC8	
5MIC IN+ 1Speaker OUT	MIC4~MIC8	
4MIC IN+ 2Speaker OUT	MIC5~MIC8	
4MIC IN+ 1Speaker OUT	MIC5~MIC8	
3MIC IN+ 1Speaker OUT	MIC6~MIC8	
2MIC IN+ 1Speaker OUT	MIC7~MIC8	

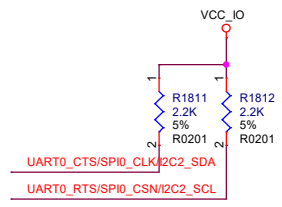
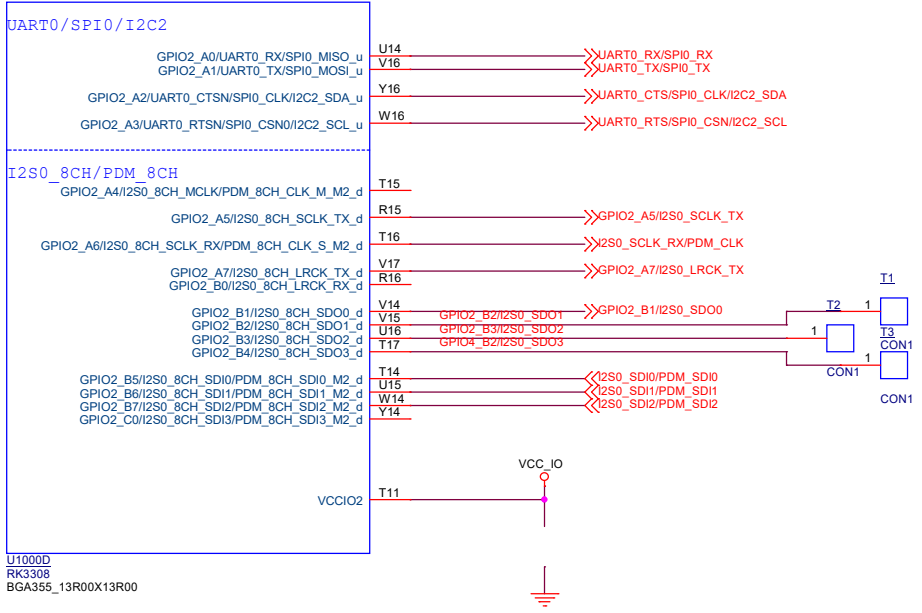
## ACODEC

U1000H  
RK3308  
BGA355\_13R00X13R00

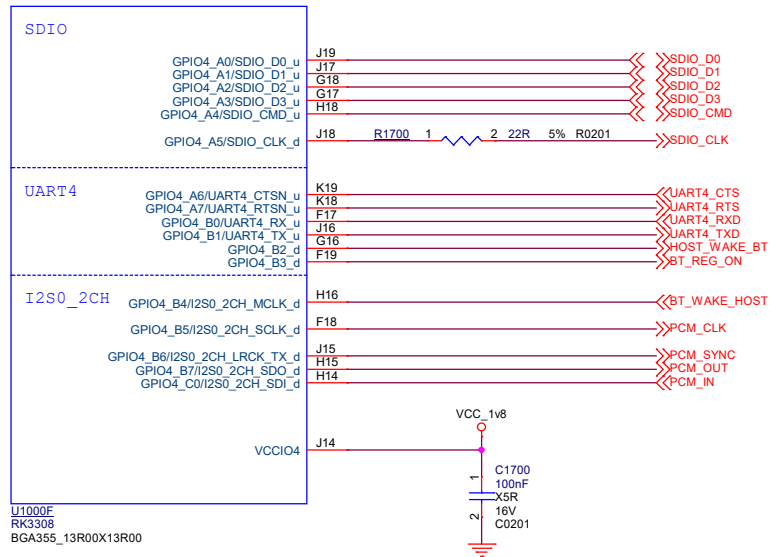
# RK3308 Part-B



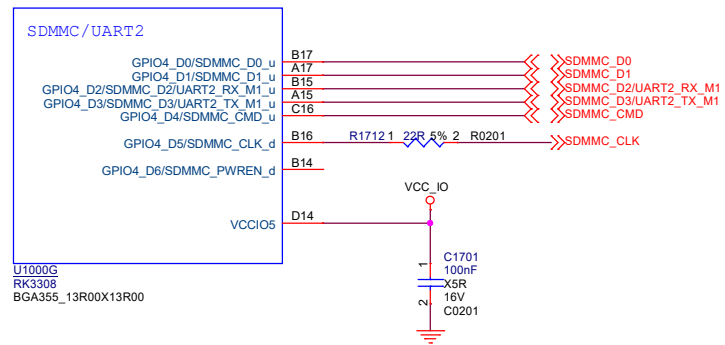
# RK3308 Part-D



# RK3308 Part-F

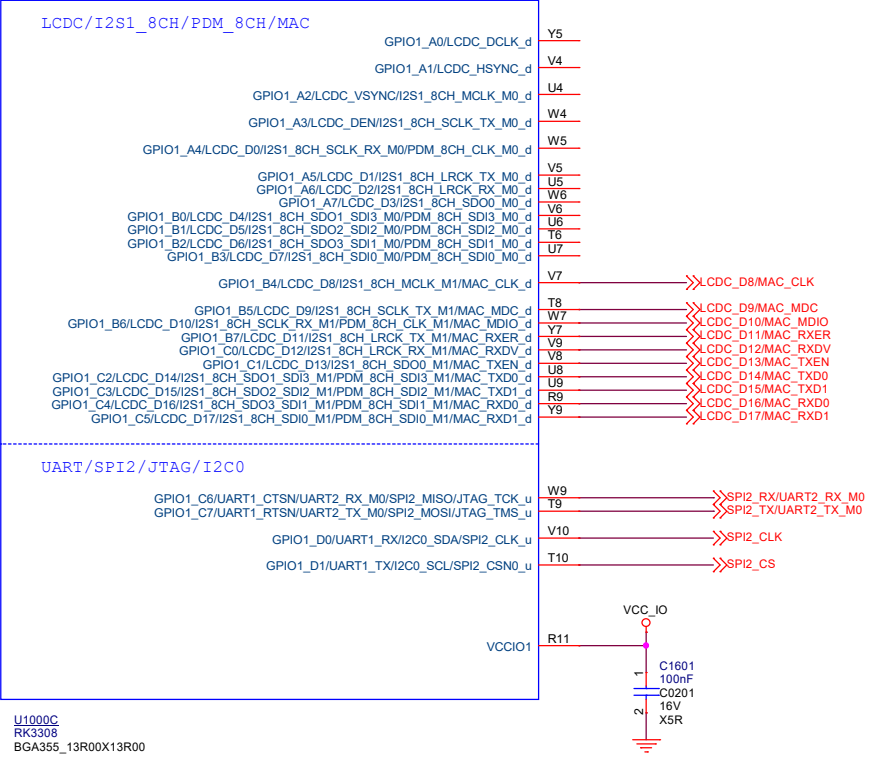


# RK3308 Part-G





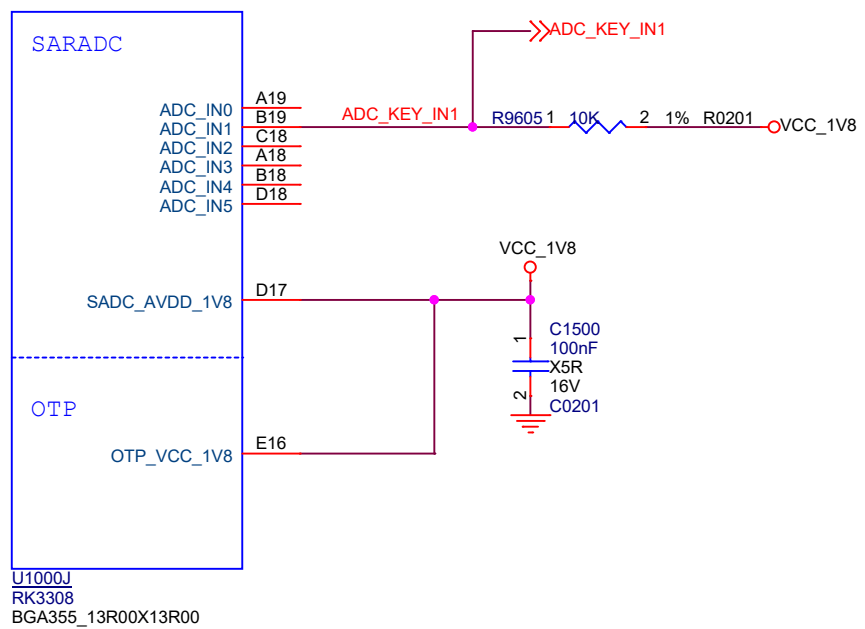
# RK3308 Part-C



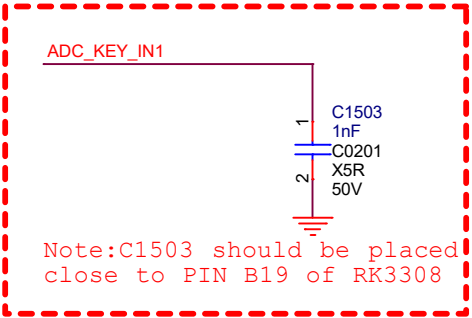
U1000C  
RK3308  
BGA355\_13R00X13R00

Correspondence between LCDC DATA and RGB/MCU				
LCDC	18bit RGB Panel	24bit RGB Panel	16bit MCU Panel	8bit MCU Panel
LCDC_D0	B0	B2	DB0	DB0
LCDC_D1	B1	B3	DB1	DB1
LCDC_D2	B2	B4	DB2	DB2
LCDC_D3	B3	B5	DB3	DB3
LCDC_D4	B4	B6	DB4	DB4
LCDC_D5	B5	B7	DB5	DB5
LCDC_D6	G0	G2	DB6	DB6
LCDC_D7	G1	G3	DB7	DB7
LCDC_D8	G2	G4	DB8	
LCDC_D9	G3	G5	DB9	
LCDC_D10	G4	G6	DB10	
LCDC_D11	G5	G7	DB11	
LCDC_D12	R0	R2	DB12	
LCDC_D13	R1	R3	DB13	
LCDC_D14	R2	R4	DB14	
LCDC_D15	R3	R5	DB15	
LCDC_D16	R4	R6		
LCDC_D17	R5	R7		
LCDC_CLK	LCDC_CLK	LCDC_CLK	MCU_CMD (Command)	MCU_CMD (Command)
LCDC_HSYNC	LCDC_HSYNC	LCDC_HSYNC	MCU_WR (write signal)	MCU_WR (write signal)
LCDC_VSYNC	LCDC_VSYNC	LCDC_VSYNC	MCU_CS (Chip select)	MCU_CS (Chip select)
LCDC_DEN	LCDC_DEN	LCDC_DEN	MCU_RD (Read signal)	MCU_RD (Read signal)

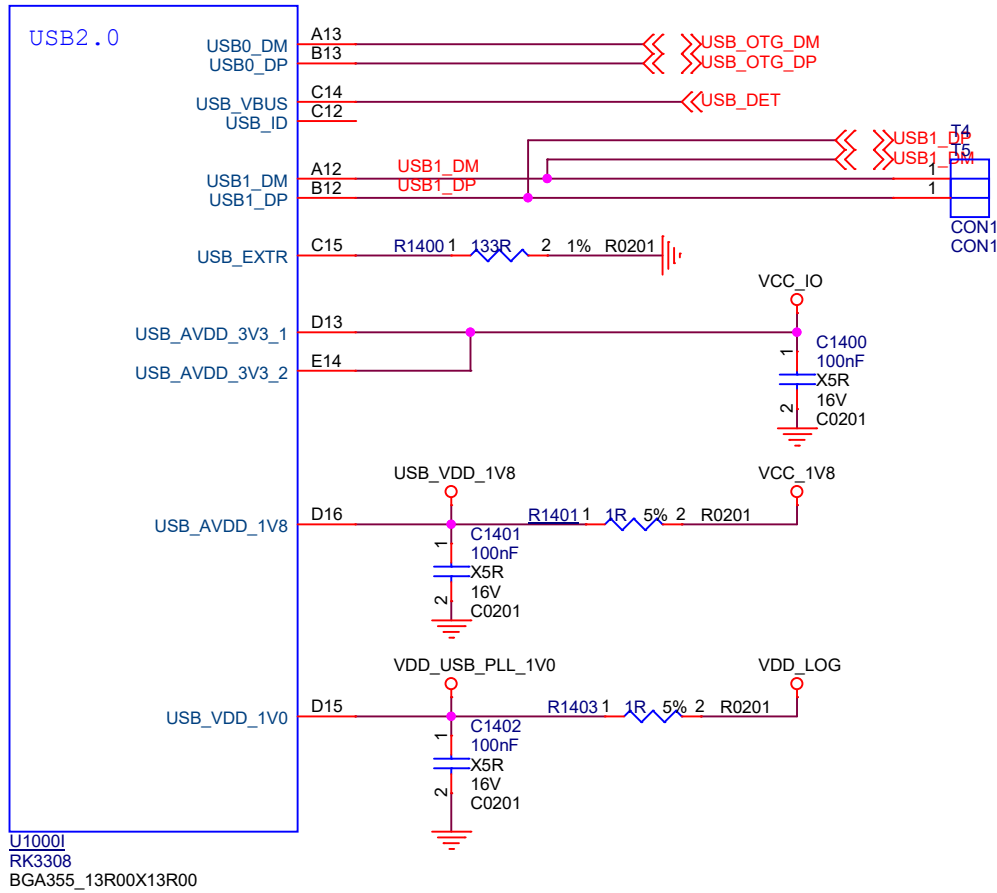
# RK3308 Part-J



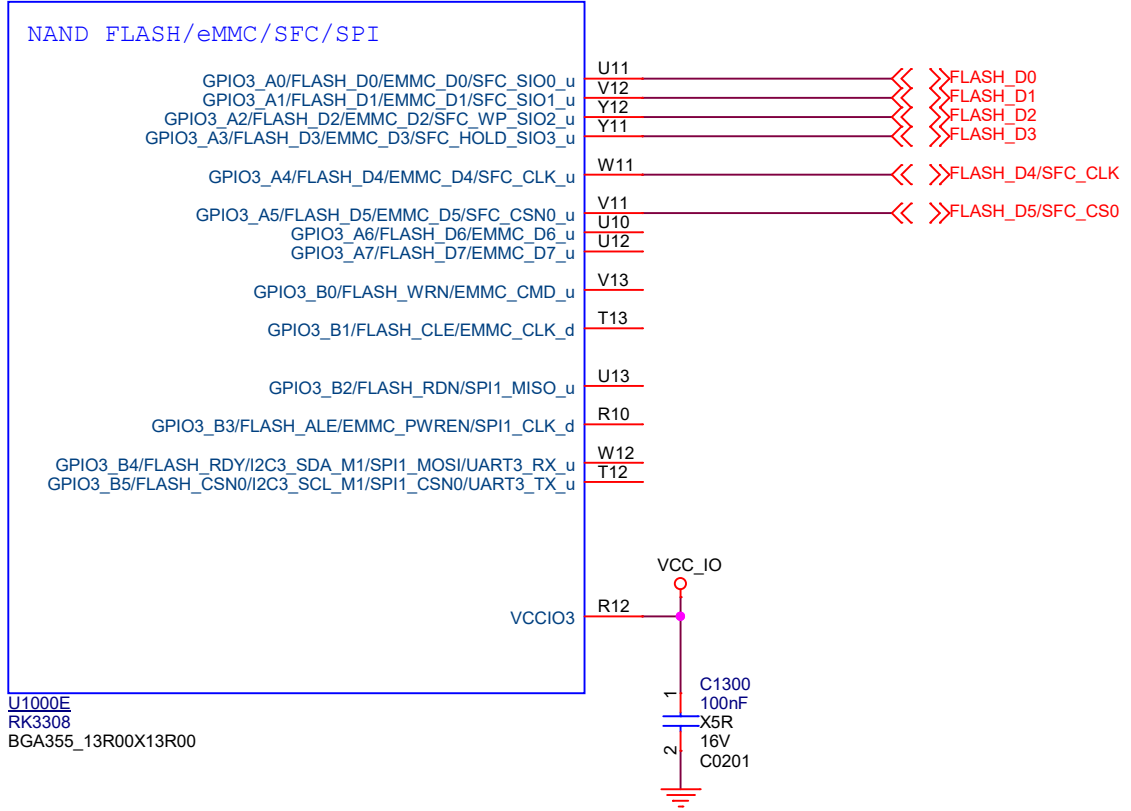
U1000J  
RK3308  
BGA355\_13R00X13R00



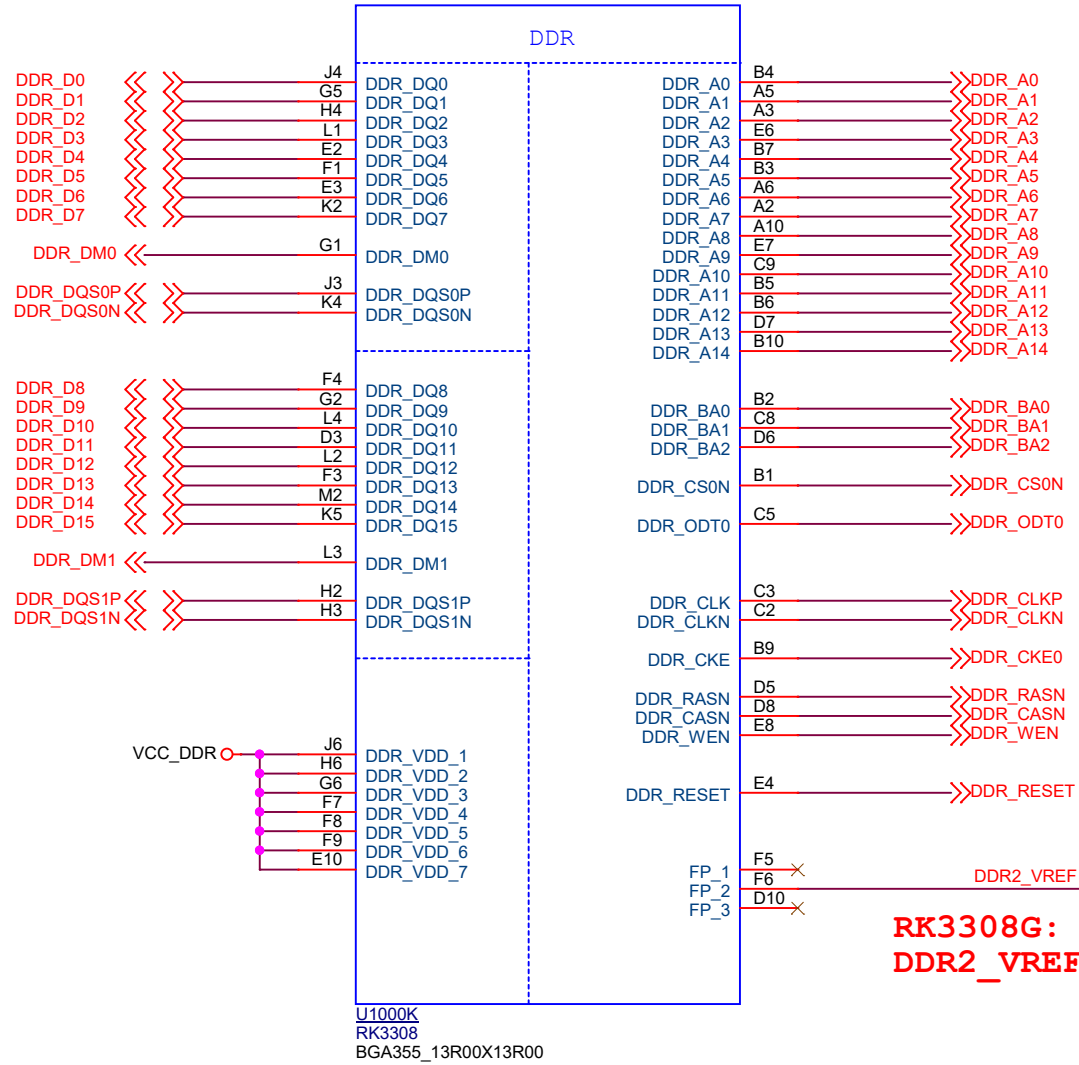
# RK3308 Part-I



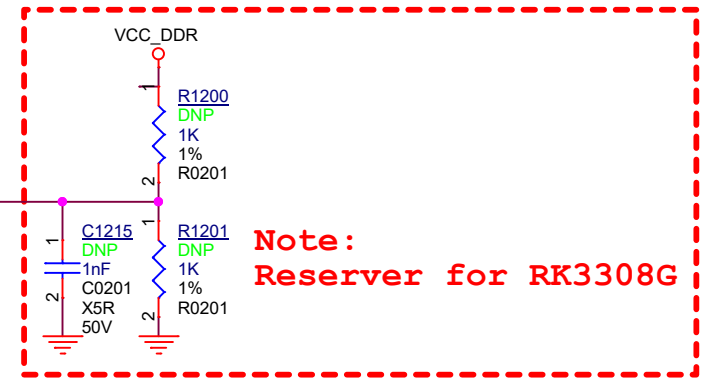
# RK3308 Part-E



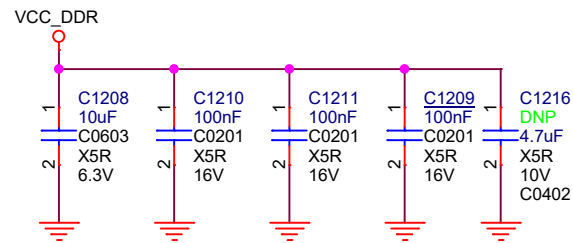
# RK3308 Part-K



**RK3308G:  
DDR2\_VREF**

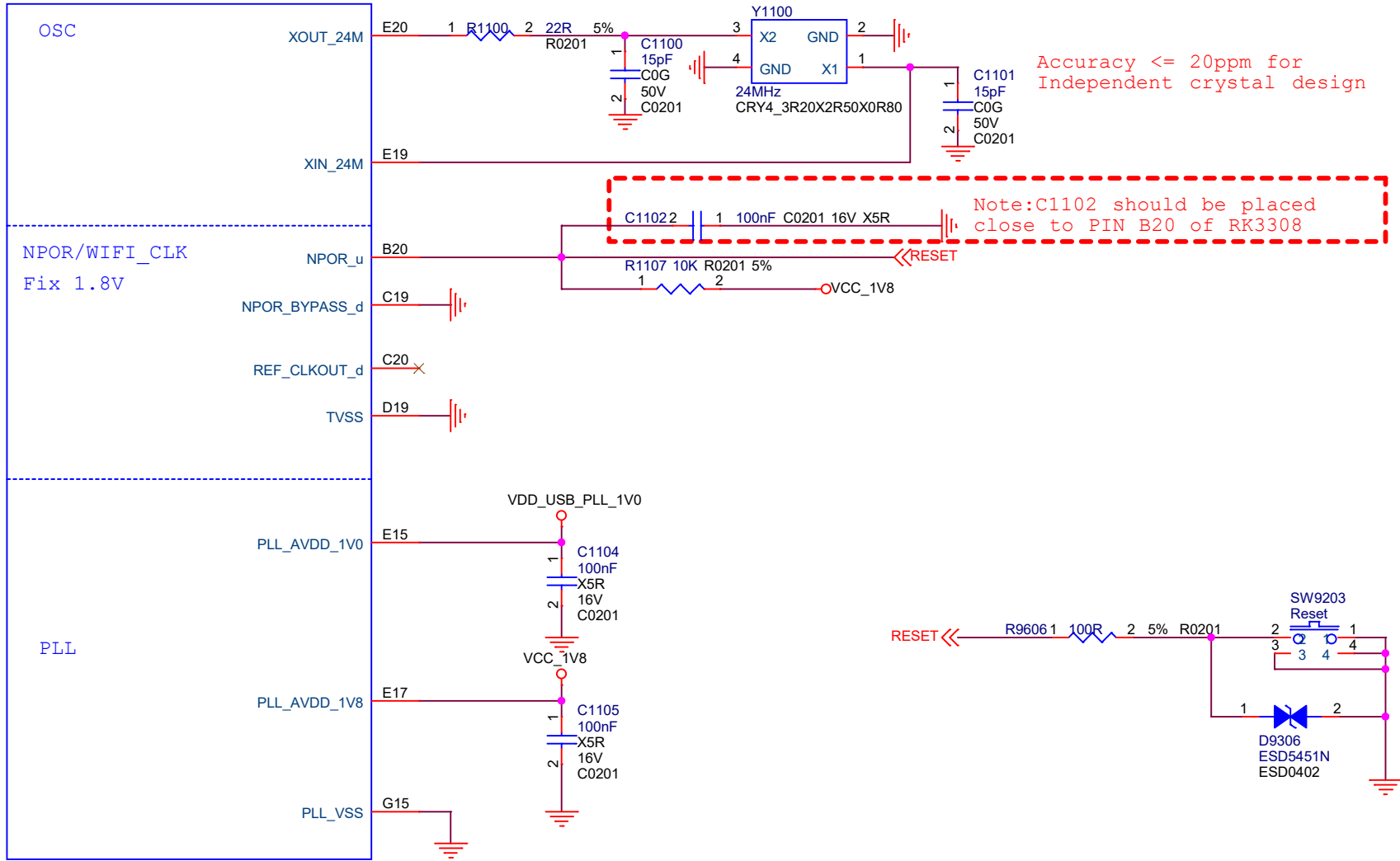


## Power Filter



Note: All the Power filter capacitors should be placed close to the power pins of RK3308

# RK3308 Part-A

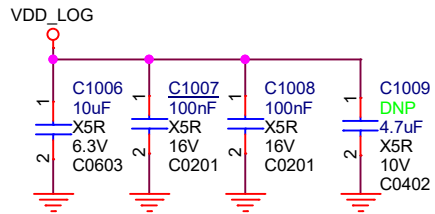
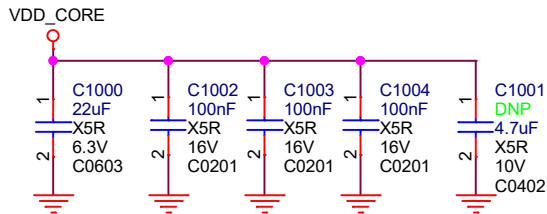
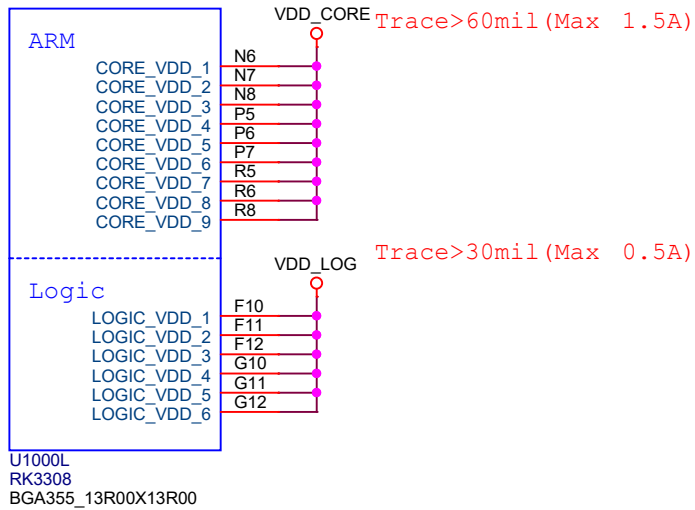


Accuracy <= 20ppm for Independent crystal design

Note: C1102 should be placed close to PIN B20 of RK3308

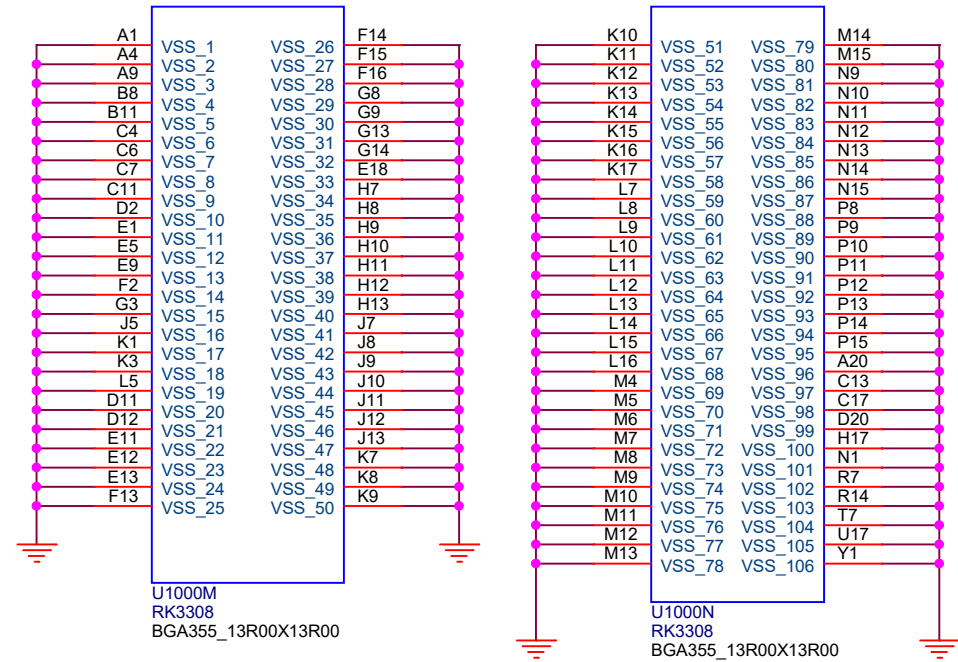
U1000A  
RK3308  
BGA355\_13R00X13R00

# RK3308 Part-L

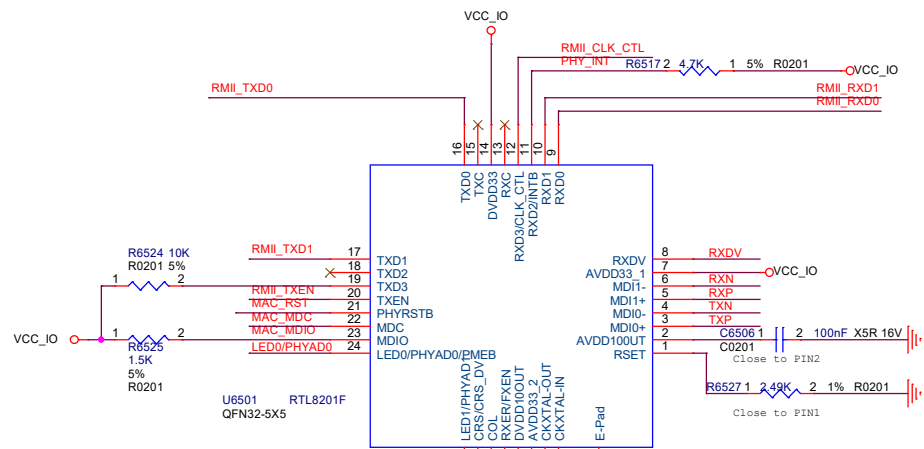
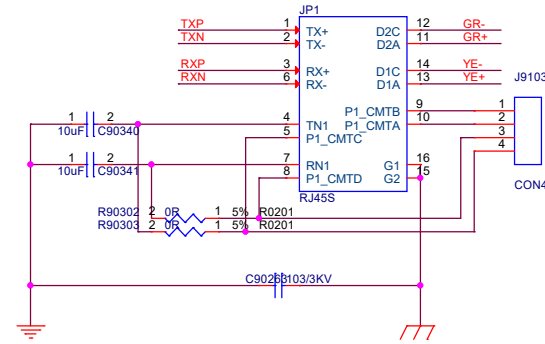
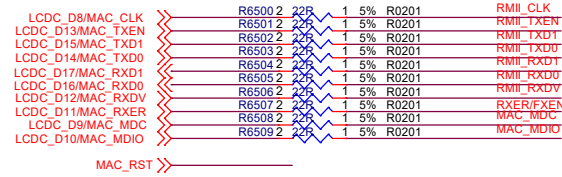


Note: All the Power filter capacitors should be placed close to the power pins of RK3308

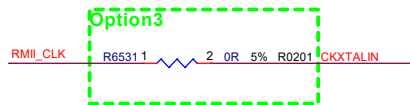
# RK3308 Part-N RK3308 Part-M



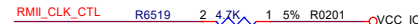
# 10/100M PHY-RTL8201F



	25MHz Crystal	NO 25MHz Crystal (Default)
Option1	0R	DNP
Option2	DNP	10K
Option3	DNP	0R
Option4	Be mounted	Not be mounted
Option5	R6518=4.7K R6519=DNP	R6518=DNP R6519=4.7K

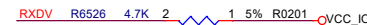


## RMII REF\_CLK direction



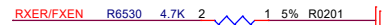
Pull Low for RMII REF\_CLK Output mode  
Pull High for RMII REF\_CLK Input mode

## MII/RMII Selection



Pull High for MII mode(default)

## UTP / Fiber Selection



Pull Low for UTP Mode(default)  
Pull High for Fiber Mode

## PHY Address/LED

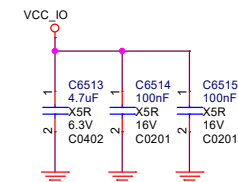
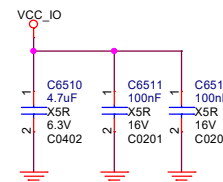


## WOL/LED0 Selection

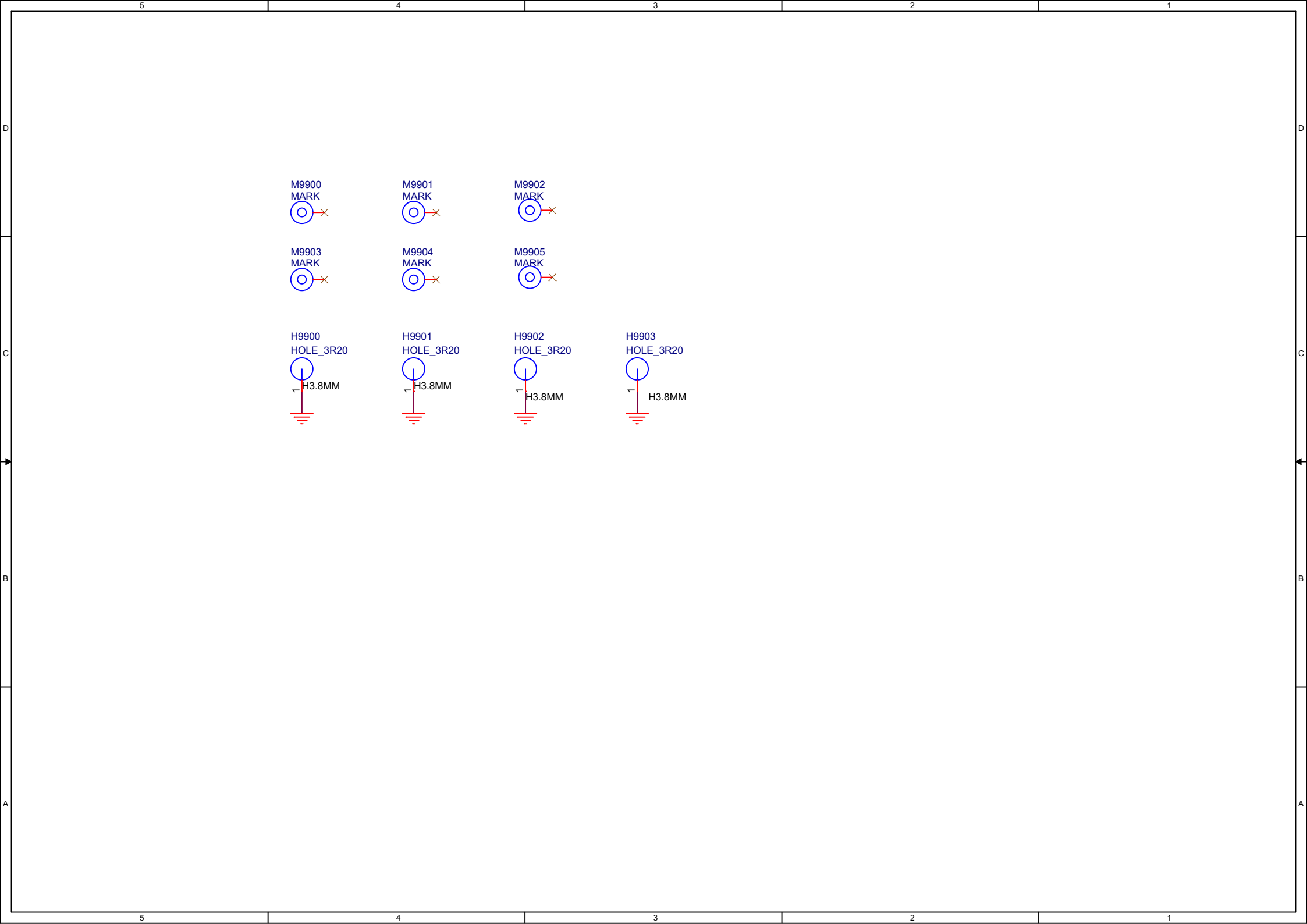


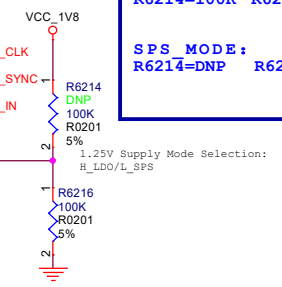
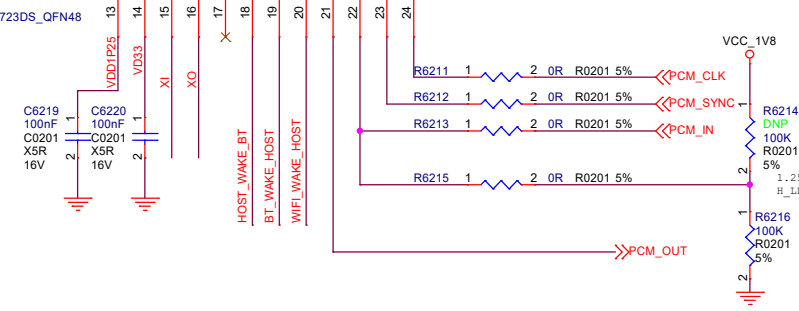
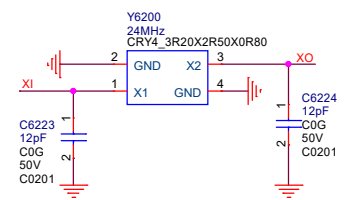
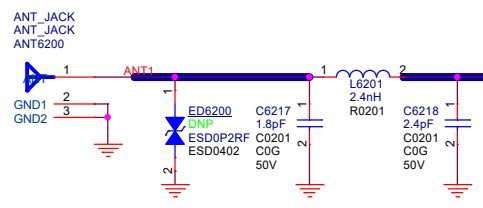
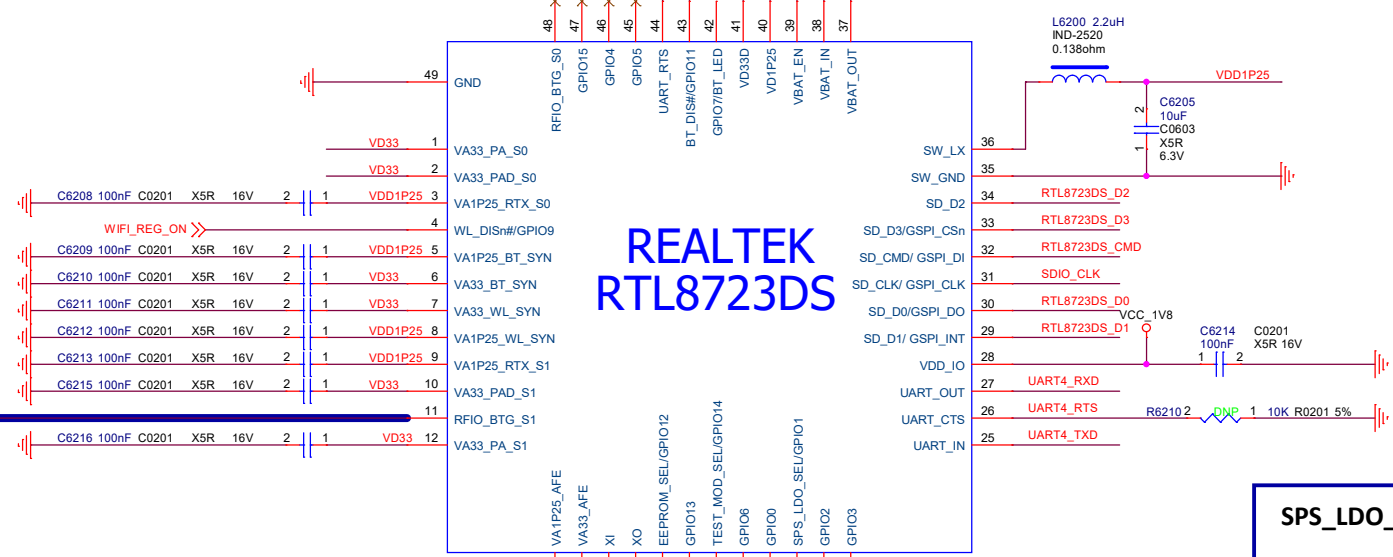
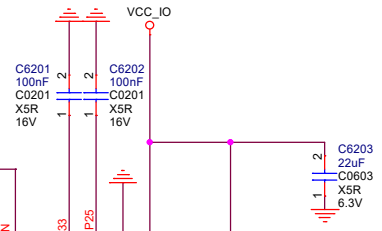
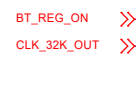
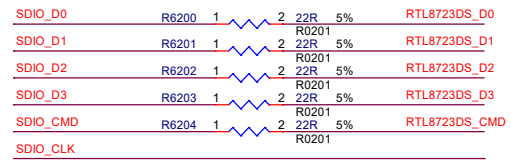
Pull Low for LED0 Mode(default)  
Pull High for WOL Mode  
WOL:Wake-on-LAN

## POWER









**SPS\_LDO\_SEL**

LDO MODE:  
R6214=100K R6216=DNP L6200=0R

SPS MODE:  
R6214=DNP R6216=100K L6200=2.2uH

**REALTEK  
RTL8723DS**

RTL8723DS\_QFN48

1.25V Supply Mode Selection:  
H\_Ldo/I\_SPS

