Radxa C201 VMARC IO Board

An Application Board for VMARC Series

Revision 1.0

2026-08-28

Radxa Computer

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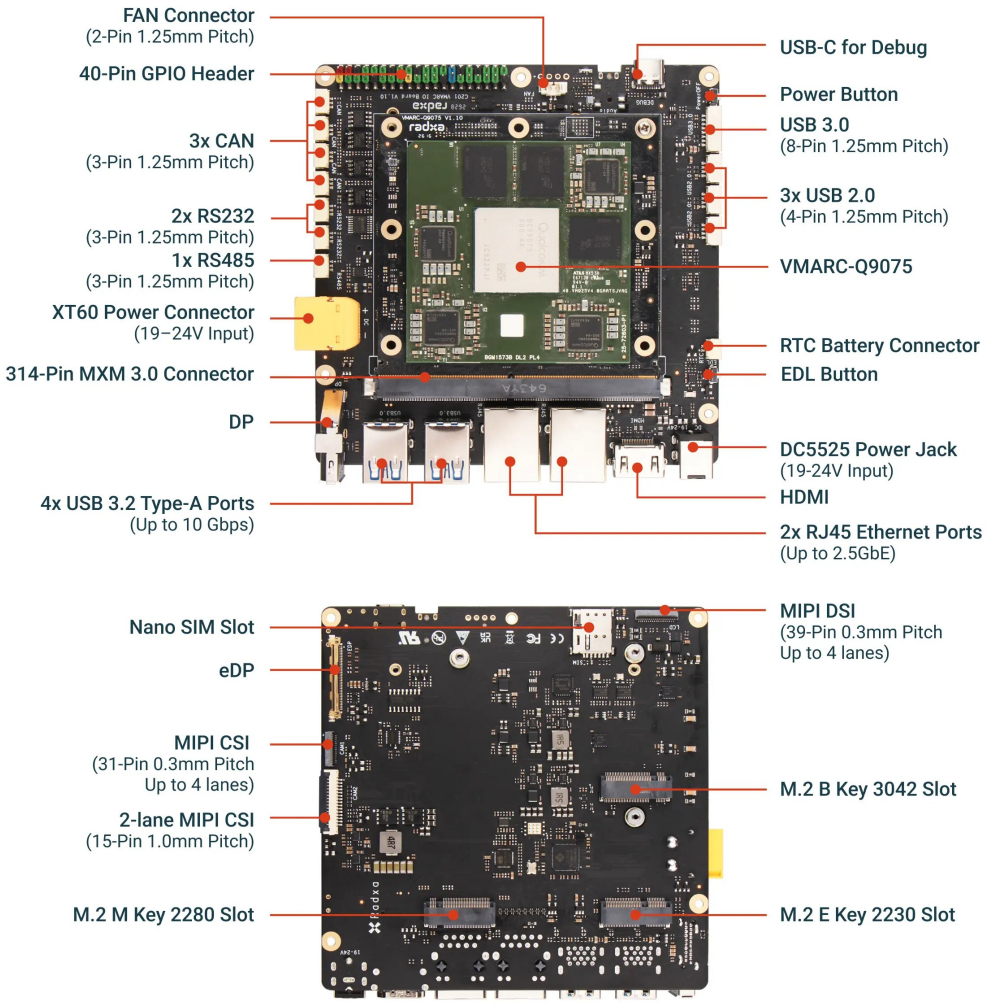
1 Revision Control Table

Version	Date	Changes from previous version
1.0	2026-08-28	First version

2 Introduction

The Radxa C201 VMARC IO Board is designed specifically for the Radxa VMARC series and is intended to provide a comprehensive evaluation platform for VMARC SoMs such as the VMARC-Q9075. With the VMARC IO Board, developers can easily verify the storage expansion, multimedia support, and functionality of the various interfaces of the VMARC SoM.



3 Compatible SoM

- Radxa VMARC-Q9075

Note: The maximum data rates and lane configurations of some interfaces depend on the installed SoM.

4 Interfaces

- 1× M.2 M Key 2280 Slot for NVMe SSD
- 2× RJ45 Ports, up to 2.5GbE
- 1× M.2 E Key 2230 Slot for WiFi / Bluetooth Module
- 1× M.2 B Key 3042 Slot for Cellular Module
- 1× Nano SIM Slot
- 1× MIPI DSI (39-Pin, 0.3mm), up to 4 lanes
- 1× eDP
- 1× HDMI (Standard Type-A)
- 1× DisplayPort (DP)
- 1× MIPI CSI (31-Pin, 0.3mm), up to 4 lanes
- 1× MIPI CSI (15-Pin, 1.0mm), up to 2 lanes
- 4× USB 3.2 Type-A, up to 10Gbps
- 1× USB 3.2 Header, up to 10Gbps
- 3× USB 2.0 Headers
- 1× USB Type-C (Debug)
- 1× Power Button
- 1× EDL Button
- 1× 2-Pin 1.25mm Fan Connector
- 1× RTC Battery Connector
- 4× CAN Bus Connectors
- 2× RS-232 Ports
- 1× RS-485 Port
- 1× 40-Pin GPIO Headers (UART / SPI / I2C / GPIO, etc.)
- DC5525 Power Jack or XT60 Connector (19–24V Input)
- 314-Pin MXM 3.0 Connector

5 Mechanical Specification

120 mm × 120 mm

6 Availability

Radxa guarantees availability of the Radxa C201 VMARC IO Board until at least December 2038.

7 Support

For support please see the hardware documentation section of the Radxa Document website and post questions to the Radxa forum.